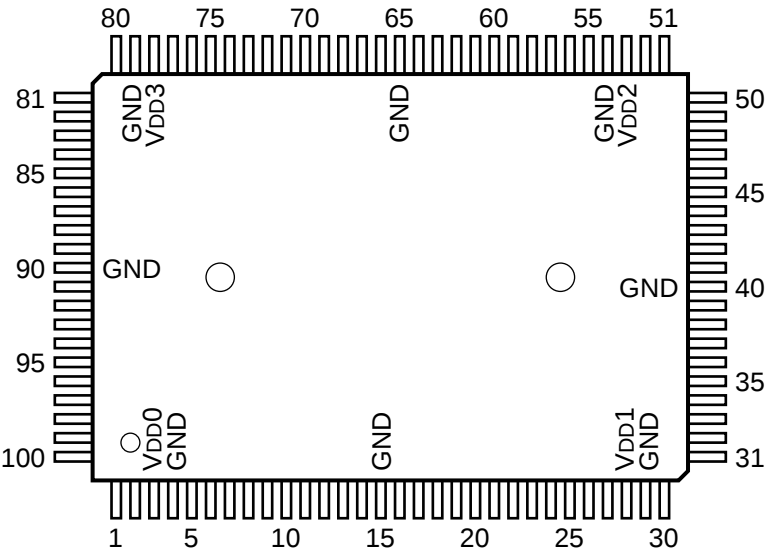


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C-MOS VIDEO SCAN CONVERTER
-TOP VIEW-



VDD = +5V

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	SCLK	26	O	CD4	51	I	TST6	76	I	YSD5
2	I	SCTR	27	O	CD3	52	I	CSD0	77	I	YSD6
3	—	VDD0	28	—	VDD1	53	—	VDD2	78	—	VDD3
4	—	GND	29	—	GND	54	—	GND	79	—	GND
5	I	SDAT	30	O	CD2	55	I	CSD1	80	I	YSD7
6	I	TST0	31	O	CD1	56	I	CSD2	81	I	YFD0
7	I/O	P0	32	O	CD0	57	I	CSD3	82	I	YFD1
8	I/O	P1	33	O	YD7	58	I	CSD4	83	I	YFD2
9	I/O	P2	34	O	YD6	59	I	CSD5	84	I	YFD3
10	O	VBLK	35	O	YD5	60	I	CSD6	85	I	YFD4
11	I	TST1	36	O	YD4	61	I	CSD7	86	I	YFD5
12	I	FSL1	37	O	YD3	62	I	CFD0	87	I	YFD6
13	I	FSL2	38	O	YD2	63	I	CFD1	88	I	YFD7
14	I	TST2	39	O	YD1	64	I	CFD2	89	O	HCR0
15	—	GND	40	—	GND	65	—	GND	90	—	GND
16	I	BLNK	41	O	YD0	66	I	CFD3	91	O	VCR0
17	I	TST3	42	O	BYCK	67	I	CFD4	92	O	WEN0
18	I	TST4	43	O	RYCK	68	I	CFD5	93	O	ADCK
19	I	TST5	44	I	YCK	69	I	CFD6	94	I	ODEV
20	O	VOUT	45	I	RDCK	70	I	CFD7	95	O	HRET
21	O	HOUT	46	O	REN1	71	I	YSD0	96	I	HIN
22	O	HBLK	47	O	VCR1	72	I	YSD1	97	I	VIN
23	O	CD7	48	O	HCR1	73	I	YSD2	98	I	CKI
24	O	CD6	49	O	INC1	74	I	YSD3	99	O	RYOE
25	O	CD5	50	O	INC2	75	I	YSD4	100	O	BYOE

INPUT

BLNK	; OUTPUT DATA CONTROL (H: BLACK INDICATION)
CFD0-7	; B-Y/R-Y UPPER LINE DATA
CKI	; CLOCK FOR WRITE
CSD0-7	; B-Y/R-Y LOWER LINE DATA
FSL1	; FIELD DISCRIMINATION SELECT (H: INTERNAL, L: EXTERNAL)
FSL2	; FIELD INFORMATION POLARITY SELECT
HIN	; HORIZONTAL SYNC. SIGNAL
ODEV	; FIELD INFORMATION
RDCK	; CLOCK FOR READ OUT
SCLK	; SERIAL TRANSFER CLOCK
SCTR	; SERIAL TRANSFER CONTROL
SDAT	; SERIAL TRANSFER DATA
TEST0-6	; TEST
VIN	; VERTICAL SYNC. SIGNAL
YCK	; CLOCK FOR D/A CONVERTER
YFD0-7	; Y UPPER LINE DATA
YSD0-7	; Y LOWER LINE DATA

OUTPUT

ADCK	; CLOCK FOR A/D CONVERTER
BYCK	; CLOCK FOR B-Y D/A CONVERTER
BYOE	; B-Y A/D CONVERTER ENABLE
CD0-7	; B-Y/R-Y DATA
HBLK	; HORIZONTAL BLANKING SIGNAL
HCR0	; WRITE MEMORY HORIZONTAL CLEAR
HCR1	; READ OUT MEMORY HORIZONTAL CLEAR
HOUT	; HORIZONTAL SYNC. SIGNAL
HRET	; OUTPUT FOR PHASE COMPARATOR
INC1,2	; READ OUT MEMORY LINE INCREMENT
REN1	; READ OUT MEMORY ENABLE
RYCK	; CLOCK FOR R-Y D/A CONVERTER
RYOE	; R-Y A/D CONVERTER ENABLE
VBLK	; VERTICAL BLANKING SIGNAL
VCR0	; WRITE MEMORY VERTICAL CLEAR
VCR1	; READ OUT MEMORY VERTICAL CLEAR
VOUT	; VERTICAL SYNC. SIGNAL
WEN0	; WRITE MEMORY ENABLE
YD0-7	; Y DATA

INPUT/OUTPUT

P0-2	; I/O PORT
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