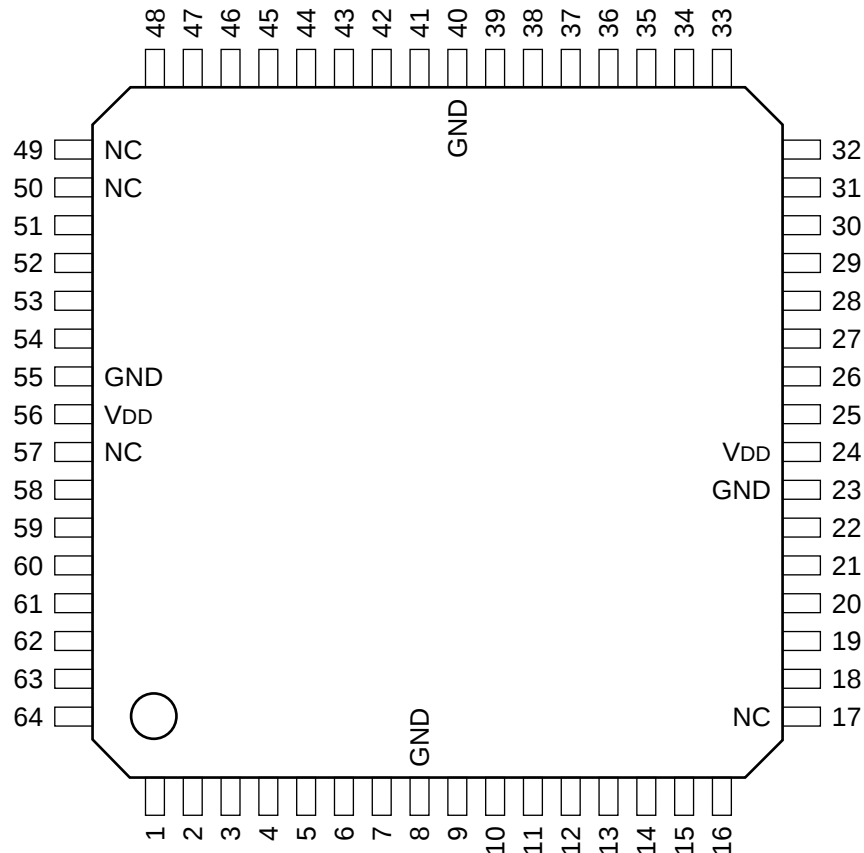


C-MOS TIMING GENERATOR FOR CCD CAMERA

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	D0	17	—	NC	33	I	CLKI	49	—	NC
2	O	D1	18	O	SHD	34	O	CLKO	50	—	NC
3	O	D2	19	I	BBI	35	O	XSUB	51	O	SDO
4	O	D3	20	O	BBO	36	O	XH2	52	O	BCO
5	I	VD	21	O	VCLP	37	O	XH1	53	I	BCI
6	I	HD	22	O	HCLP2	38	O	XSG2	54	O	XRG
7	O	CLK	23	—	GND	39	O	XSG1	55	—	GND
8	—	GND	24	—	VDD	40	—	GND	56	—	VDD
9	I	TEST1	25	I	TEST6	41	O	XV4	57	—	NC
10	I	TEST2	26	O	HCLP1	42	O	XV3	58	I	HTSG
11	I	TEST3	27	I	PBLKON	43	O	XV2	59	I	RST
12	I	TEST4	28	O	PBLK	44	O	XV1	60	I	TEST12
13	I	TEST5	29	I	TEST7	45	O	TEST8	61	I	FLD/FRM
14	O	SHP	30	I	SD	46	O	TEST9	62	I	EIA/CCIR
15	I	BAI	31	I	SC	47	O	TEST10	63	I	MODE
16	O	BAO	32	I	LD	48	O	TEST11	64	I	TEST13

INPUT

BAI	: BUFFER INPUT FOR SHP PHASE ADJUSTMENT
BBI	: BUFFER INPUT FOR SHD PHASE ADJUSTMENT
BCI	: BUFFER INPUT FOR XRG PHASE ADJUSTMENT
CLKI	: CLOCK
EIA/CCIR	: SYSTEM SELECT (H:EIA/L:CCIR)
FLD/FRM	: MODE SELECT (H:FIELD CHARGE/L:FRAME CHARGE)
HD	: HORIZONTAL DRIVE
HTSG	: ON/OFF CONTROL FOR READ PULSE
LD	: LATCH PULSE FOR ELECTRONIC SHUTTER CONTROL
MODE	: MODE SELECT (H:COMPONENT DIGITAL/L:COMPOSITE DIGITAL)
PBLKON	: ON/OFF CONTROL FOR PBLK
RST	: FOR TEST
SC	: CLOCK FOR ELECTRONIC SHUTTER CONTROL
SD	: SERIAL DATA FOR ELECTRONIC SHUTTER CONTROL
TEST1 - TEST7	: FOR TEST
TEST12, TEST13	: FOR TEST
VD	: VERTICAL DRIVE

OUTPUT

BAO	: NOT-INVERTED OUTPUT OF BAI
BBO	: NOT-INVERTED OUTPUT OF BBI
BCO	: NOT-INVERTED OUTPUT OF BCI
CLK	: DIVIDED-BY-TWO CLOCK (PIN-33)
CLKO	: INVERTED OUTPUT OF CLKI
D0 - D3	: EXPANSION I/O
HCLP1 - HCLP2	: HORIZONTAL CLAMP PULSE
PBLK	: PRE-BLANKING PULSE
SDO	: SERIAL DATA FOR ELECTRONIC SHUTTER CONTROL
SHD	: SAMPLING PULSE FOR CCD'S SIGNAL LEVEL
SHP	: SAMPLING PULSE FOR CCD'S PRE-CHARGE LEVEL
TEST8 - TEST11	: FOR TEST
VLCP	: VERTICAL CLAMP PULSE
XH1, XH2	: HORIZONTAL REGISTER DRIVE CLOCK
XRG	: OUTPUT SECTION RESET GATE PULSE
XSG1, XSG2	: SENSOR'S ELECTRON CHARGE READ PULSE
XSUB	: SUBSTRATE PULSE FOR ELECTRONIC SHUTTER
XV1 - XV4	: VERTICAL REGISTER DRIVE CLOCK

