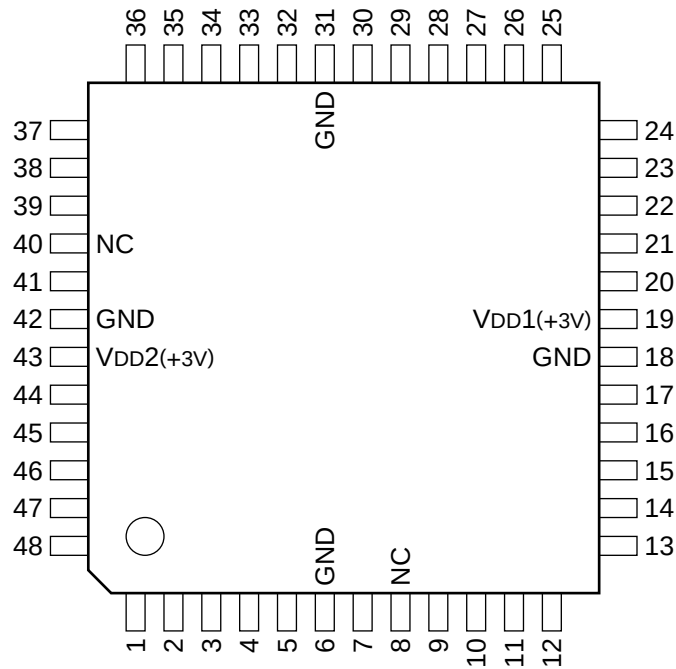

C-MOS AUTO FOCUS SIGNAL DETECTOR
—TOP VIEW—



(VDD = +3V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	VD	13	O	TO0	25	O	TO10	37	I	AFY5
2	I	SCK	14	O	TO1	26	O	TO11	38	I	AFY6
3	I	SI	15	O	TO2	27	O	TVDL	39	I	AFY7
4	O	SO	16	O	TO3	28	O	THDL	40	—	NC
5	I	XCE	17	O	TO4	29	O	THCK	41	I	TMOD
6	—	GND	18	—	GND	30	I	MCK	42	—	GND
7	O	RST	19	—	VDD	31	—	GND	43	—	VDD
8	—	NC	20	O	TO5	32	I	AFY0	44	O	GATEA
9	I	TIN1	21	O	TO6	33	I	AFY1	45	O	GATEB
10	I	TIN2	22	O	TO7	34	I	AFY2	46	I	EXTWDA
11	I	TIN3	23	O	TO8	35	I	AFY3	47	I	EXTWDB
12	I	TIN4	24	O	TO9	36	I	AFY4	48	I	HD

INPUT

AFY0 - AFY7	; AFY DATA
EXTWDA, EXTWNDB	; EXTERNAL DETECTED WINDOW
HD	; HORIZONTAL SYNC
MCK	; MASTER CLOCK
RST	; RESET (H : NORMAL, L : POWER SAVE)
SCK	; SERIAL CLOCK
SI	; SERIAL DATA
TIN1 - TIN4	; TEST
TMOD	; TEST
VD	; VERTICAL SYNC
XCE	; SERIAL STROBE

OUTPUT

GATEA, GATEB	; DETECTED WINDOW
SO	; SERIAL DATA
THCK	; TEST
THDL	; TEST
TO0 - TO11	; TEST
TVDL	; TEST

