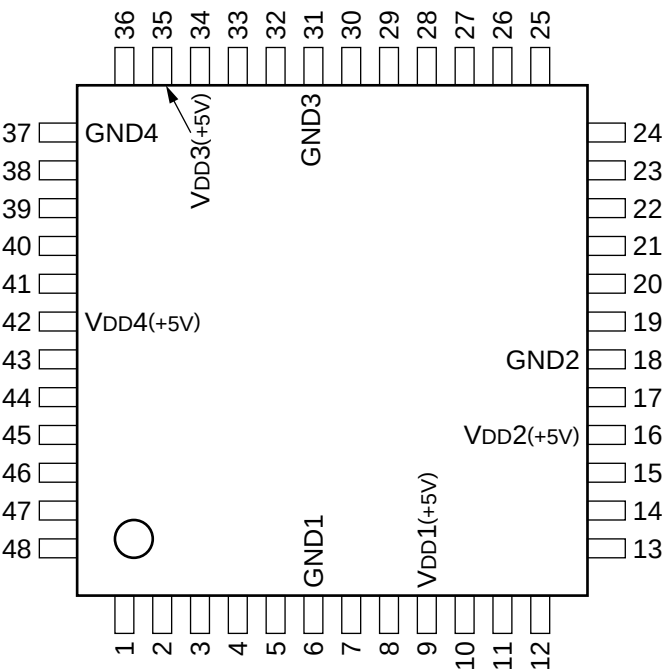

C-MOS ELECTRONIC IRIS CONTROL IC
-TOP VIEW-



(VDD = +5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	OSCI	13	O	ID	25	O	XV1	37	—	GND4
2	O	OSCO	14	O	BFG	26	O	XSG1	38	I	SPUPV
3	I	CK	15	O	CLP2	27	O	XV3	39	I	IRIN
4	I	TEST	16	—	VDD2	28	O	XSG2	40	I	SPDNV
5	O	CL	17	O	XSHD	29	O	XV4	41	—	VREG
6	—	GND1	18	—	GND2	30	O	XSUB	42	—	VDD4
7	I	VD	19	O	XSHP	31	—	GND3	43	I	ENB
8	I	HD	20	O	XSP1	32	O	H1	44	I	IRENB
9	—	VDD1	21	O	XSP2	33	O	H2	45	I	PS
10	O	CLP4	22	O	XDL1	34	O	RG	46	I	LIMIT1
11	O	CLP1	23	O	XDL2	35	—	VDD3	47	I	LIMIT2
12	O	PBLK	24	O	XV2	36	I	GM	48	I	NTSC

INPUT

CK ; CLOCK
 ENB ; ELECTRONIC SHUTTER PULSE GENERATION/STOP SWITCH CONTROL
 HD ; HORIZONTAL SYNCHRONIZING SIGNAL
 IRENB ; ELECTRONIC IRIS/SHUTTER SWITCH CONTROL
 IRIN ; ELECTRONIC IRIS MODE : IRIS SIGNAL
 ELECTRONIC SHUTTER SERIAL MODE : CLOCK
 LIMIT1, 2 ; SHUTTER SPEED LIMIT VALUE SELECT
 NTSC ; NTSC/PAL SWITCH CONTROL
 OSCI ; OSCILLATION INVERTER
 PS ; ELECTRONIC SHUTTER SPEED DATA PARALLEL/SERIAL SWITCH CONTROL
 SPDNV ; ELECTRONIC IRIS MODE : SHUTTER SPEED DOWN REFERENCE VOLTAGE
 ELECTRONIC SHUTTER SERIAL MODE : DATA
 SPUPV ; ELECTRONIC IRIS MODE : SHUTTER SPEED UP REFERENCE VOLTAGE
 ELECTRONIC SHUTTER SERIAL MODE : STROBE
 TEST ; TEST
 VD ; VERTICAL SYNCHRONIZING SIGNAL

OUTPUT

BFG ; BURST FLAG GATE PULSE
 CL ; CK/2 CLOCK
 CLP1, 2, 4 ; CLAMP PULSE
 H1, 2 ; CCD HORIZONTAL CLOCK
 ID ; LINE DETECT
 OSCO ; OSCILLATION INVERTER
 PBLK ; BLANKING CLEANING PULSE
 RG ; CCD RESET GATE PULSE
 XDL1, 2 ; CLOCK (DELAY LINE)
 XSG1, 2 ; CLOCK (CCD SENSOR READ OUT)
 XSHD ; SAMPLE/HOLD PULSE (DATA LEVEL)
 XSHP ; SAMPLE/HOLD PULSE (PRE-CHARGE LEVEL)
 XSP1, 2 ; SAMPLE/HOLD PULSE
 XSUB ; CLOCK (CCD ELECTRON SHUTTER)
 XV1, 2, 3, 4 ; CCD VERTICAL CLOCK

