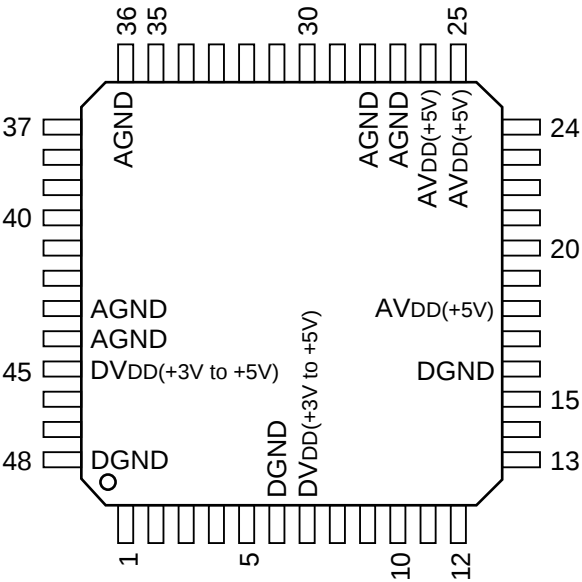
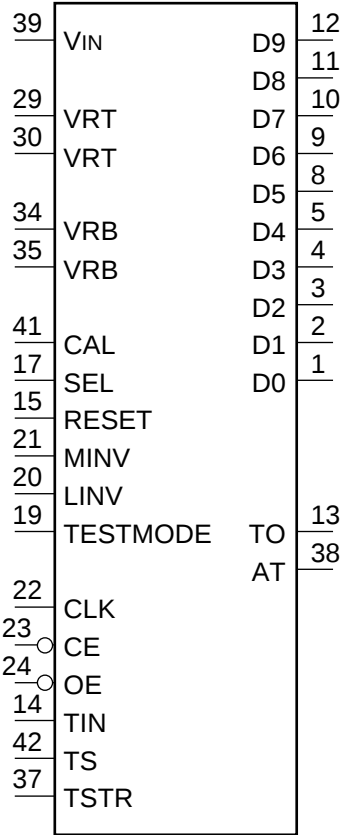

C-MOS 10-BIT 20MSPS VIDEO A/D CONVERTER
—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	D0	13	O	TO	25	—	AVDD	37	I	TSTR
2	O	D1	14	I	TIN	26	—	AVDD	38	O	AT
3	O	D2	15	I	RESET	27	—	AGND	39	I	V IN
4	O	D3	16	—	DGMD	28	—	AGND	40	—	NC
5	O	D4	17	I	SEL	29	I	VRT	41	I	CAL
6	—	DGND	18	—	AVDD	30	I	VRT	42	I	TS
7	—	DVDD	19	I	TESTMODE	31	—	NC	43	—	AGND
8	O	D5	20	I	LINV	32	—	NC	44	—	AGND
9	O	D6	21	I	MINV	33	—	NC	45	—	DVDD
10	O	D7	22	I	CLK	34	I	VRB	46	—	NC
11	O	D8	23	I	OE	35	I	VRB	47	—	NC
12	O	D9	24	I	CE	36	—	AGND	48	—	DGND



INPUT

CAL ; CALIBRATION PULSE
CE ; CHIP ENABLE
CLK ; CLOCK
LINV ; INVERSION (D0-D8)
MINV ; INVERSION (D9)
OE ; OUTPUT ENABLE
RESET ; CALIBRATION RESET
SEL ; DATA SELECT
TESTMODE ; TEST MODE
TIN ; TEST (NORMAL = AVDD or AGND CONNECT)
TS ; TEST (NORMAL = AVDD CONNECT)
TSTR ; TEST (NORMAL = AVDD or AGND CONNECT)
V IN ; ANALOG
VRB ; REFERENCE BOTTOM
VRT ; REFERENCE TOP

OUTPUT

AT ; TEST (NORMAL = OPEN)
D0-D9 ; DATA
TO ; TEST (NORMAL = OPEN)

