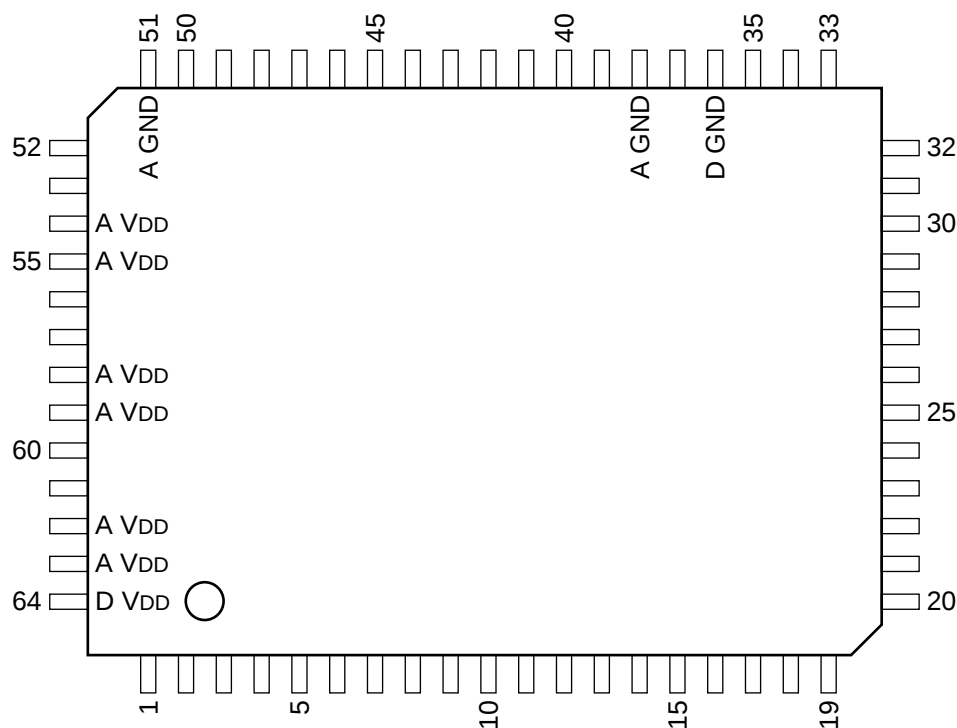
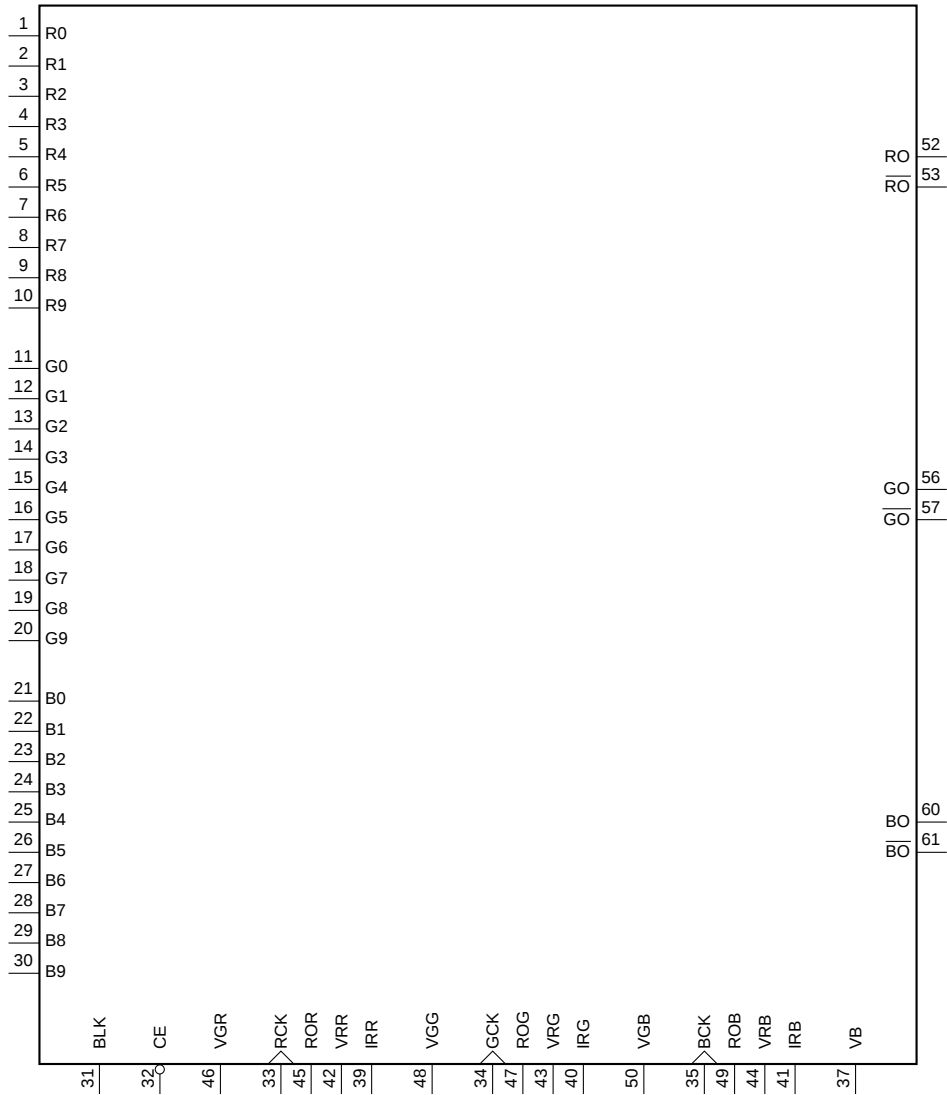


C-MOS 10-BIT HIGH SPEED D/A CONVERTER

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	R0 (LSB)	14	I	G3	27	I	B6	40	I	IRG	53	O	$\overline{RO}$
2	I	R1	15	I	G4	28	I	B7	41	I	IRB	54	—	A V _{DD}
3	I	R2	16	I	G5	29	I	B8	42	I	VRR	55	—	A V _{DD}
4	I	R3	17	I	G6	30	I	B9	43	I	VRG	56	O	GO
5	I	R4	18	I	G7	31	I	BLK	44	I	VRB	57	O	$\overline{GO}$
6	I	R5	19	I	G8	32	I	$\overline{CE}$	45	I	ROR	58	—	A V _{DD}
7	I	R6	20	I	G9	33	I	RCK	46	I	VGR	59	—	A V _{DD}
8	I	R7	21	I	B0 (LSB)	34	I	GCK	47	I	ROG	60	O	BO
9	I	R8	22	I	B1	35	I	BCK	48	I	VGG	61	O	$\overline{BO}$
10	I	R9	23	I	B2	36	—	D GND	49	I	ROB	62	—	A V _{DD}
11	I	G0 (LSB)	24	I	B3	37	I	VB	50	I	VGB	63	—	A V _{DD}
12	I	G1	25	I	B4	38	—	A GND	51	—	A GND	64	—	D V _{DD}
13	I	G2	26	I	B5	39	I	IRR	52	O	RO			



INPUT

B0 - B9 : Bch DIGITAL DATA
BCK : Bch CLOCK
BLK : BLANKING SIGNAL
CE : CHIP ENABLE
G0 - G9 : Gch DIGITAL DATA
GCK : Gch CLOCK
IRB : CONNECT 1.2KΩ TO A GND
IRG : CONNECT 1.2KΩ TO A GND
IRR : CONNECT 1.2KΩ TO A GND
R0 - R9 : Rch DIGITAL DATA
RCK : Rch CLOCK

ROB : OUTPUT AMPLITUDE CONTROL
ROG : OUTPUT AMPLITUDE CONTROL
ROR : OUTPUT AMPLITUDE CONTROL
VB : CONNECT CAPACITOR TO D GND
VGB : CONNECT CAPACITOR
VGG : CONNECT CAPACITOR
VGR : CONNECT CAPACITOR
VRB : SET FULL SCALE (2.0V) OF OUTPUT
VRG : SET FULL SCALE (2.0V) OF OUTPUT
VRR : SET FULL SCALE (2.0V) OF OUTPUT

OUTPUT

BO, BO : Bch CURRENT OUTPUT
GO, GO : Gch CURRENT OUTPUT
RO, RO : Rch CURRENT OUTPUT

