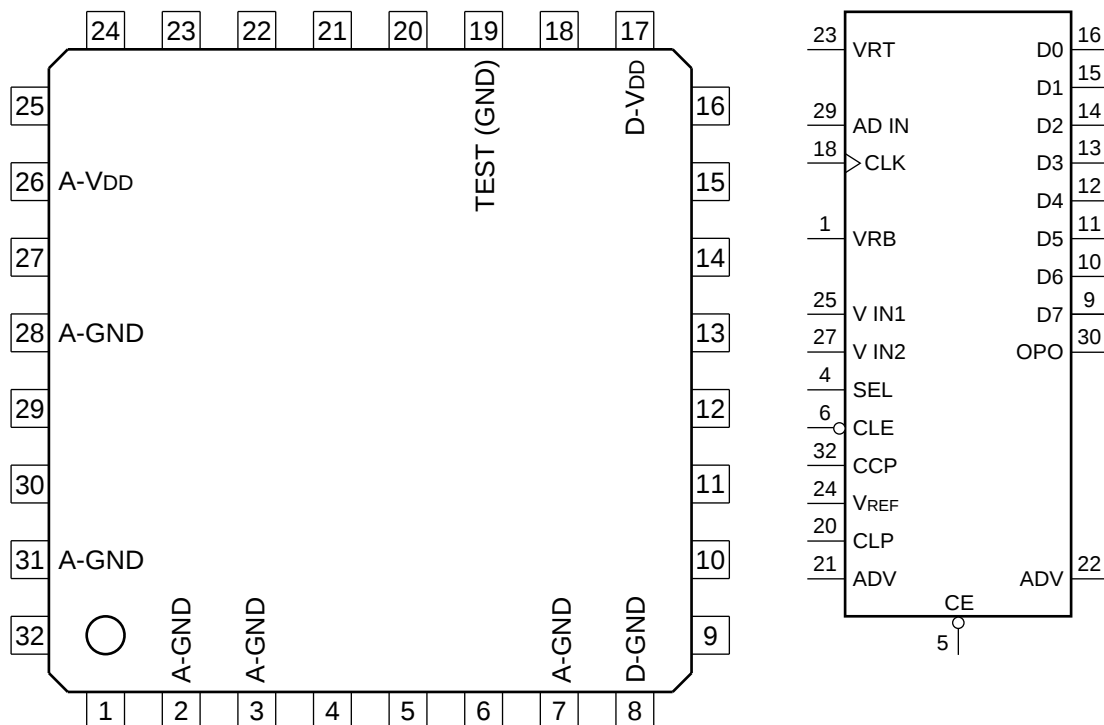


C-MOS 8-BIT VIDEO A/D CONVERTER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	VRB	17	—	D-V _{DD}
2	—	A-GND	18	I	CLK
3	—	A-GND	19	—	TEST
4	I	SEL	20	I	CLP
5	I	$\overline{\text{CE}}$	21	I	ADV
6	I	$\overline{\text{CLE}}$	22	O	ADV
7	—	A-GND	23	I	VRT
8	—	D-GND	24	I	V _{REF}
9	O	D7 (MSB)	25	I	V _{IN1}
10	O	D6	26	—	A-V _{DD}
11	O	D5	27	I	V _{IN2}
12	O	D4	28	—	A-GND
13	O	D3	29	I	AD IN
14	O	D2	30	O	OPO
15	O	D1	31	—	A-GND
16	O	D0 (LSB)	32	I	CCP

INPUT

AD IN : ANALOG SIGNAL
 ADV : CAPACITOR CONNECTION
 CCP : CLAMP CONTROL VOLTAGE
 $\overline{\text{CE}}$: STANDBY MODE $\overline{\text{ON}}$ /OFF
 $\overline{\text{CLE}}$: CLAMP ENABLE
 CLK : A/D CLOCK
 CLP : CLAMP PULSE
 SEL : OP-AMP INPUT SELECT
 V_{IN1}, V_{IN2} : OP-AMP INPUT
 V_{REF} : CLAMP REFERENCE VOLTAGE
 VRB : REFERENCE VOTAGE (BOTTOM)
 VRT : REFERENCE VOTAGE (TOP)

OUTPUT

D0 - D7 : DATA
 ADV : CAPACITOR CONNECTION
 OPO : OP-AMP OUTPUT

