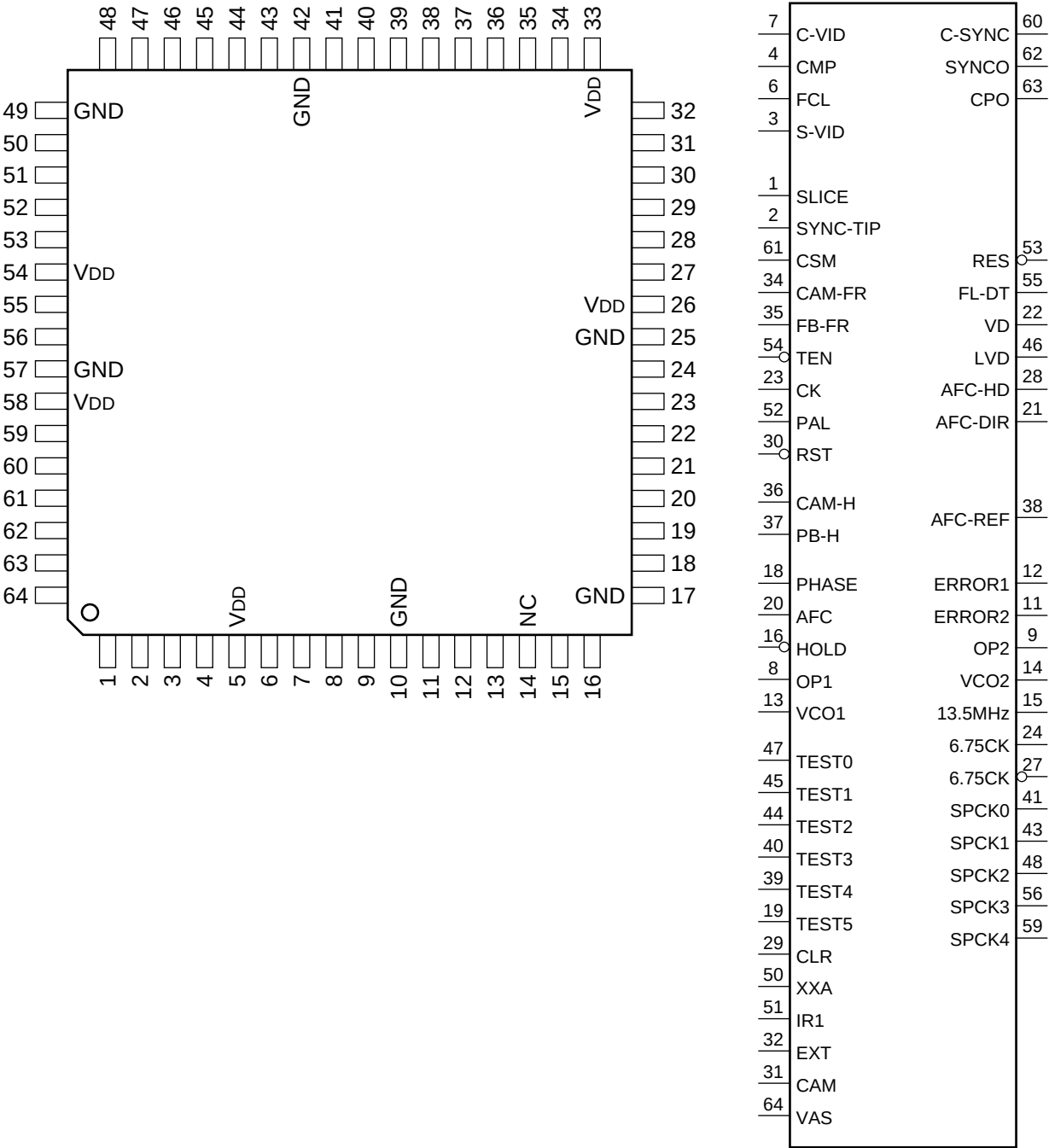


C-MOS AFC FOR SYNC SIGNAL

—TOP VIEW—



INPUT

AFC	; AFC LOCK PHASE ADJUSTMENT INPUT
CAM	; CAMERA/LINE SELECT INPUT (H = CAMERA, L = LINE)
CAM-FR	; FRAME SIGNAL IN CAMERA INPUT MODE
CAM-H	; H SYNC INPUT IN CAMERA INPUT MODE
CK	; MASTER CLOCK INPUT (6.75MHz)
CLR	; DIRECT CLEAR INPUT
CMP	; SLICE LEVEL OF THE FEEDBACK CLAMP SYSTEM
CSM	; ABNORMAL SIGNAL COMPENSATOR ON/OFF (H = ON, L = OFF)
C-VID	; VIDEO INPUT OF THE FEEDBACK CLAMP SYSTEM
EXT	; EXTERNAL/INTERNAL SELECT INPUT (H = EXT, L = INT)
FB-FR	; FRAME SIGNAL IN NORMAL PB MODE
FCL	; SYNC-TIP LEVEL OF THE FEEDBACK CLAMP SYSTEM
HOLD	; AFC ERROR HOLD INPUT (L = ERROR HOLD)
OP1	; OPERATIONAL AMPLIFIER INPUT
PAL	; SYSTEM SELECT INPUT (H = PAL, L = NTSC)
PB-H	; H SYNC INPUT IN NORMAL PB MODE
PHASE	; AFC PHASE ADJUST INPUT
RST	; AFC RESET INPUT (L = RESET)
S-VID	; VIDEO INPUT OF THE SYNC CLAMP SYSTEM
SLICE	; SLICE LEVEL OF THE SYNC CLAMP SYSTEM
SYNC-TIP	; SYNC-TIP LEVEL OF THE SYNC CLAMP SYSTEM
TEN	; V SYNC ADJUST INPUT
VAS	; V SYNC ADJUST INPUT
VCO1	; VCO INPUT

OUTPUT

13.5MHz	; 13.5MHz OUTPUT
6.75CK	; 6.75MHz OUTPUT
AFC-HD	; HD FROM THE AFC OUTPUT
AFC-REF	; AFC REFERENCE OUTPUT
C-SYNC	; SEPARATED SYNC OUTPUT OF THE SYNC CLAMP SYSTEM
ERROR1	; AFC MAIN LOOP PHASE ERROR OUTPUT
ERROR2	; AFC SUB LOOP PHASE ERROR OUTPUT
FL-DT	; ODD/EVEN DETECTED OUTPUT (H = 1st and 3rd FIELDS)
LVD	; VERTICAL SYNC OUTPUT (FOR NO SIGNAL DETECTION)
OP2	; OPERATIONAL AMPLIFIER OUTPUT
RES	; V SYNC LPF OUTPUT (FOR TEST)
SPCK1 - SPCK4	; 13.5MHz OUTPUT
SYNCO	; SEPARATED SYNC OUTPUT (FOR TEST)
VCO2	; VCO OUTPUT
VD	; DETECTED VERTICAL SYNC OUTPUT

INPUT/OUTPUT

AFC-DIR	; AFC DIRECT (FOR TEST)
CPO	; SYNC CLAMP MONITORING TERMINAL
IR1	; FOR TEST
XXA	; FOR TEST

