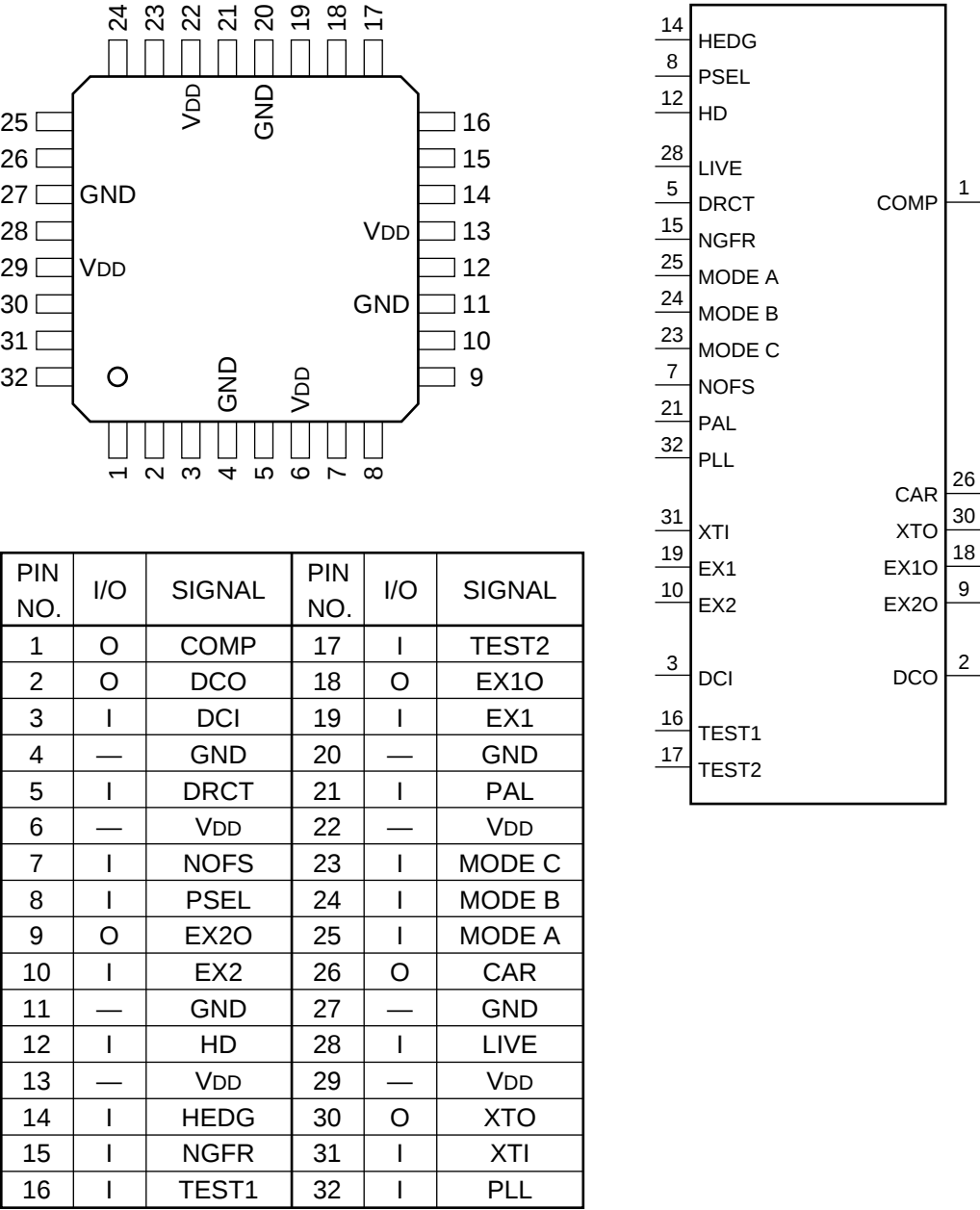


C-MOS PLL FOR NTSC/PAL VIDEO SIGNAL

—TOP VIEW—



INPUT

DCI ; DC VOLTAGE (FOR FREERUN)
 DRCT ; FOR TEST (H : NORMAL)
 EX1 ; 1st EXTERNAL CLOCK
 EX2 ; 2nd EXTERNAL CLOCK
 HEDG ; PHASE COMPARATOR TRIGGER EDGE SELECT
 (H : RISE EDGE OF HD)
 (L : FALL EDGE OF HD)
 HD ; HD
 LIVE ; POWER SAVE MODE SELECT
 (H : NORMAL, L : POWER SAVE)
 MODE A, B ; MODE CONTROL
 MODE C ; FOR TEST (L : NORMAL)
 NGFR ; FOR TEST (L : NORMAL)
 NOFS ; FRAME OFFSET SELECT (H : OFF, L : ON)
 PAL ; PAL/NTSC SELECT (H : PAL, L : NTSC)
 PSEL ; PHASE COMPARATOR OUTPUT POLARITY SELECT
 TEST1 , 2 ; FOR TEST (L : NORMAL)
 XTI ; 4fsc OSCILLATOR

OUTPUT

CAR ; 4fsc
 COMP ; PHASE COMPARATOR
 DCO ; DC VOLTAGE OUTPUT (FOR FREERUN)
 EX1O ; 1st EXTERNAL CLOCK
 EX2O ; 2nd EXTERNAL CLOCK
 XTO ; 4fsc OSCILLATOR

I/O

PLL ; FOR TEST (L: NORMAL)

