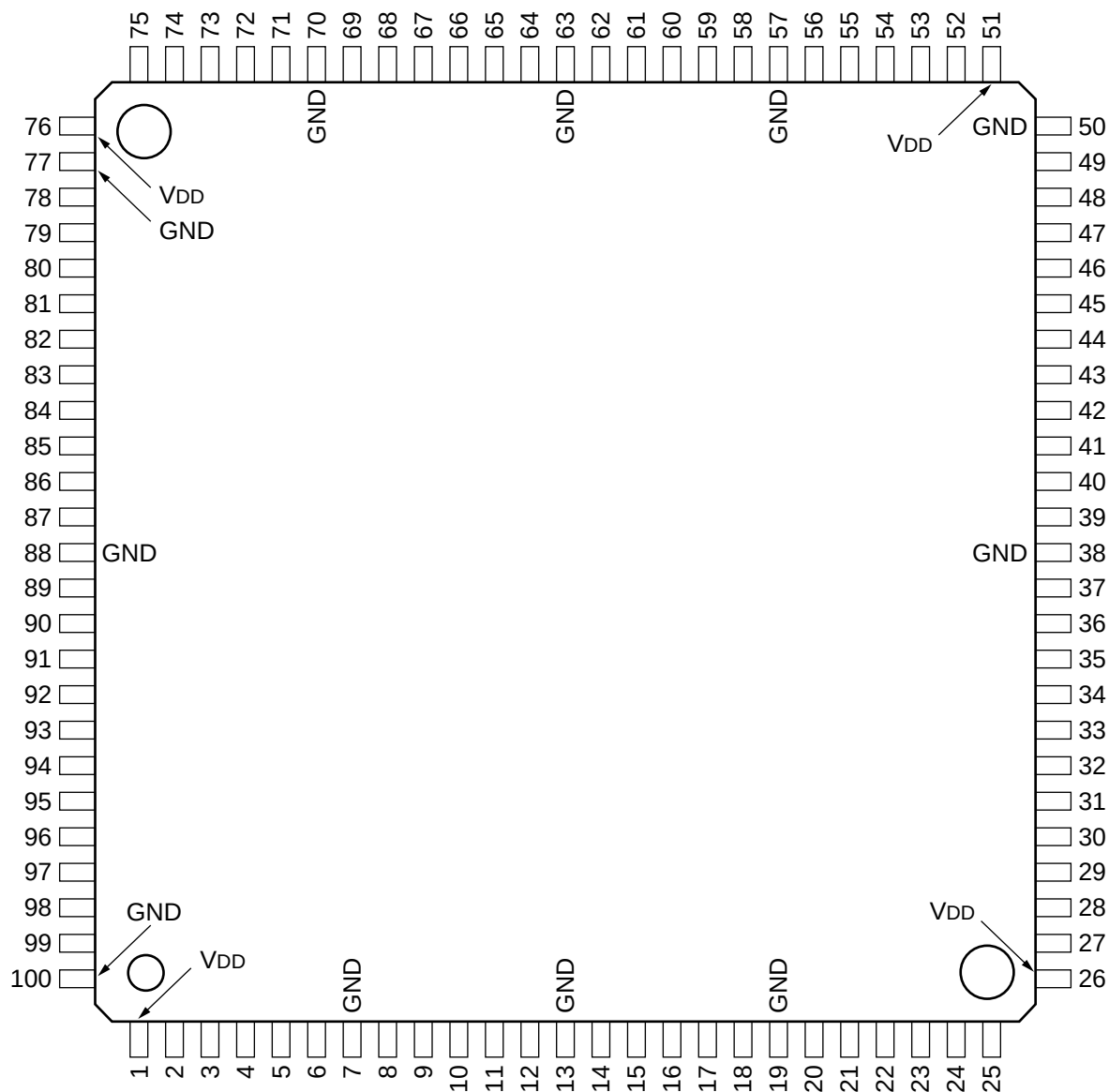


DIGITAL VCR AUDIO REC/PB SIGNAL PROCESSOR

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	VDD	26	—	VDD	51	—	VDD	76	—	VDD
2	O	PBON	27	I	SCK	52	O	AO09	77	—	GND
3	O	DIIN	28	I	SPSI	53	O	AO08	78	O	AO10
4	I	TTCK	29	O	SPSO	54	O	AO13	79	O	CS
5	I	FLTT	30	I	DSP	55	O	WE	80	I/O	D07
6	I	FLTA	31	I	ATT	56	O	AO14	81	O	PRST
7	—	GND	32	I	ACS	57	—	GND	82	O	PCEZ
8	I	TRCK	33	O	FSE2	58	O	AO12	83	O	VEN
9	I/O	TSDA	34	O	FSE1	59	O	AO07	84	O	HEN
10	I/O	TSCK	35	O	EMAP	60	O	AO06	85	O	ERS
11	O	OTEN	36	O	BCKO	61	O	AO05	86	O	SLMX
12	I	STRT	37	I	LRCI	62	O	AO04	87	O	SLVX
13	—	GND	38	—	GND	63	—	GND	88	—	GND
14	I	SFDP	39	I	256CK	64	O	AO03	89	I	TSTM
15	O	SFDR	40	I	BCKI	65	O	AO02	90	I	TSAD
16	I	P-SAVE	41	O	LRCO	66	O	AO01	91	O	DV32
17	I	MUTE	42	O	LSBO	67	O	AO00	92	O	INT
18	I	RST	43	O	DATO	68	I/O	D00	93	I	JRDY
19	—	GND	44	I	DATI	69	I/O	D01	94	I	JDAT
20	—	BC	45	O	DSPO	70	—	GND	95	O	TNOM
21	—	TCK	46	O	ATC	71	I/O	D02	96	O	TSTN
22	—	TDI	47	O	ATLT	72	I/O	D03	97	O	TBIS
23	—	TENA1	48	O	AO11	73	I/O	D04	98	O	ERRF
24	—	TDO	49	O	OE	74	I/O	D05	99	I/O	TSG
25	I	VST	50	—	GND	75	I/O	D06	100	—	GND

INPUT

256CK	: 256fs CLOCK INPUT
ACS	: COMMUNICATION CHIP SELECT (L = ACTIVE) INPUT
ATT	: AUTO ATTENUATION INTERFACE CHIP SELECT INPUT
BCKI	: 64fs INPUT
DATI	: AUDIO DATA INPUT
DSP	: ADSP INTERFACE CHIP SELECT INPUT
FLTA	: FRAME SIGNAL FOR RECORDING INPUT
FLTT	: FRAME SIGNAL FOR TRANSMISSION AND PLAYBACK INPUT
JDAT	: EXTERNAL DEVICE INTERFACE INPUT
JRDY	: EXTERNAL DEVICE INTERFACE INPUT
LRCI	: fs CLOCK INPUT
MUTE	: DIRECT MUTE INPUT (L = MUTE)
P-SAVE	: POWER SAVE INPUT (L = POWER SAVE)
RST	: RESET INPUT (L = RESET)
SCK	: SYSTEM CLOCK INPUT
SFDP	: PLAYBACK DATA INPUT
SPSI	: COMMUNICATION DATA INPUT
STRT	: REC/PB DATA COMMUNICATION START PULSE INPUT
TRCK	: MAIN CLOCK INPUT (NTSC = 18.1259 MHz, PAL = 18.144 MHz)
TSAD	: FOR TEST
TSTM	: FOR TEST
TTCK	: AUTO ATTENUATION AND ADSP INTERFACE CLOCK INPUT
VST	: FOR TEST

OUTPUT

ATC	: AUTO ATTENUATION INTERFACE CLOCK OUTPUT
ATLT	: AUTO ATTENUATION INTERFACE LATCH PULSE OUTPUT
AO00 - AO14	: EXTERNAL RAM ADDRESS OUTPUT
BCKO	: 64fs OUTPUT
CS	: EXTERNAL RAM CHIP SELECT SIGNAL OUTPUT
PBON	: MICROCOMPUTER INTERFACE SERIAL/PARALLEL OUTPUT (WORD2/BIT0)
DATO	: AUDIO DATA OUTPUT
DIIN	: MICROCOMPUTER INTERFACE SERIAL/PARALLEL OUTPUT (WORD2/BIT1)
DSPO	: ADSP INTERFACE CLOCK OUTPUT
DV32	: EXTERNAL DEVICE INTERFACE OUTPUT
EMPA	: MICROCOMPUTER INTERFACE SERIAL/PARALLEL OUTPUT (WORD2/BIT3)
ERRF	: PB AUDIO DATA ERROR ID SIGNAL OUTPUT
ERS	: EXTERNAL DEVICE RESET OUTPUT
FSE1, FSE2	: MICROCOMPUTER INTERFACE SERIAL/PARALLEL OUTPUT (WORD2/BIT4, 5)
HEN	: EXTERNAL DEVICE INTERFACE OUTPUT
INT	: PHASE COMPARATOR CLOCK OUTPUT
LRCO	: fs OUTPUT
LSBO	: AUDIO DATA OUTPUT (LSB FIRST)
OE	: EXTERNAL RAM OUTPUT ENABLE SIGNAL OUTPUT
OTEN	: ENABLE OUTPUT FOR RECORDING DATA TRANSMISSION
PCEZ	: EXTERNAL DEVICE INTERFACE OUTPUT
PRST	: EXTERNAL DEVICE INTERFACE OUTPUT
SFDO	: RECORDING DATA OUTPUT
SLVX	: EXTERNAL DEVICE INTERFACE OUTPUT
SLXM	: EXTERNAL DEVICE INTERFACE OUTPUT
SPSO	: COMMUNICATION DATA OUTPUT
TBIS	: FOR TEST
TNOM	: FOR TEST
TSTN	: FOR TEST
VEN	: EXTERNAL DEVICE INTERFACE OUTPUT
WE	: EXTERNAL RAM WRITE ENABLE SIGNAL OUTPUT

INPUT/OUTPUT

D00 - D07	: EXTERNAL RAM DATA INPUT/OUTPUT
TSCK	: FOR TEST
TSDA	: FOR TEST
TSG	: FOR TEST

