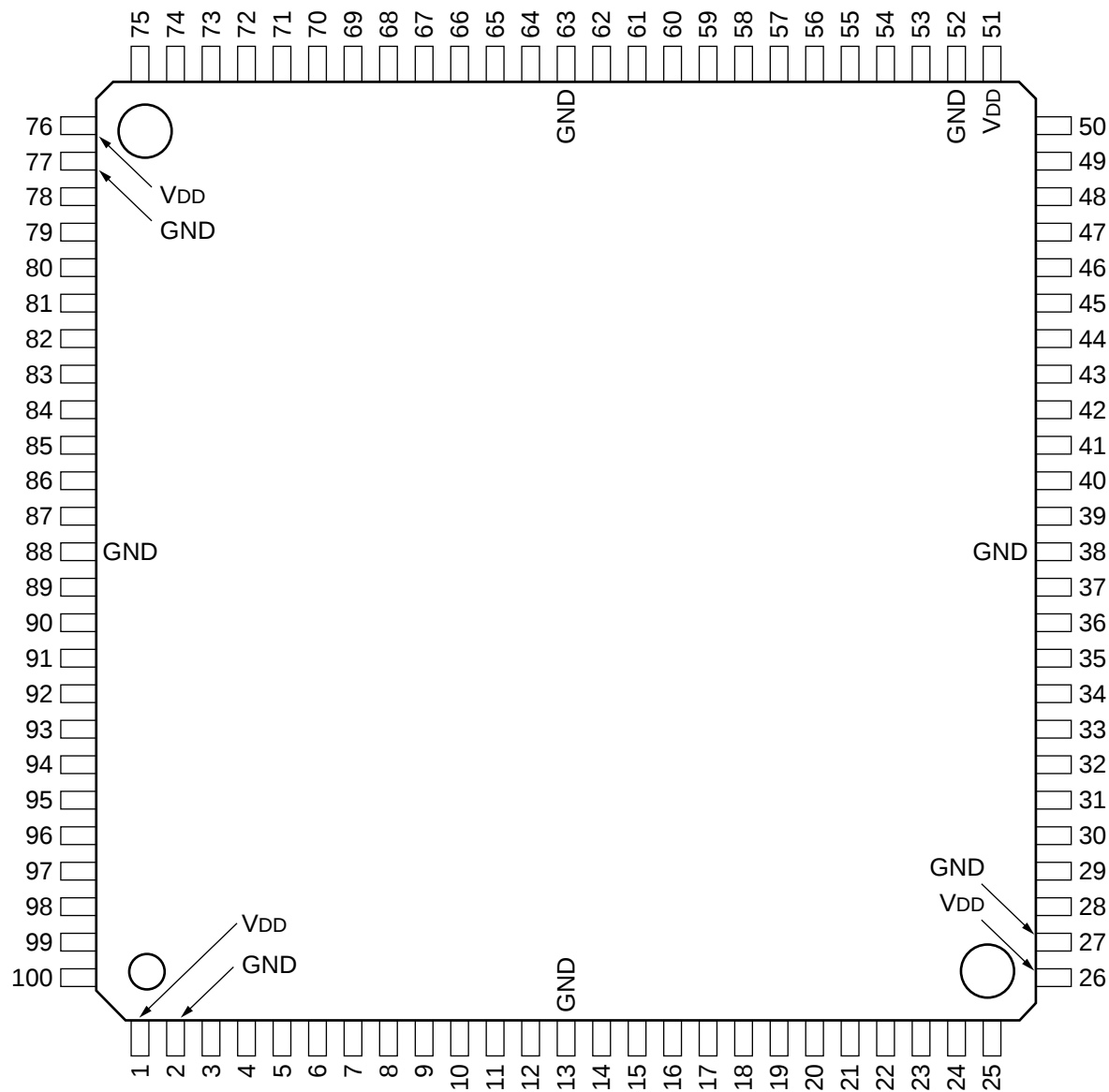


C-MOS CHANNEL ENCODER/DECODER(FOR DIGITAL VCR)
—TOP VIEW—



INPUT

AZIMUTH	: AZIMUTH ID SIGNAL
CK (13.5M)	: 13.5 MHz PLL ADJUSTING CLOCK
CLOCK	: MICROCOMPUTER CLOCK INPUT
CS	: MICROCOMPUTER CHIP SELECT
DO PLS	: DROPOUT PULSE
DRCK REF	: DRCK CLOCK REFERENCE
FR PLS	: FRAME PULSE
PBDT0 - 6	: PLAYBACK DATA
PB CK IN	: PLAYBACK CLOCK INPUT
RST	: SYSTEM RESET (L = RESET)
SERIAL IN	: MICROCOMPUTER SERIAL DATA INPUT
SUB-CODE	: SUB-CODE AREA SIGNAL
TEST SEL0, 1	: TEST SIGNAL MODE
TR PLS	: TRACK PULSE
VCO IN	: DRCK PLL OSCILLATOR INPUT

OUTPUT

AFTER REC TIMG	: AFTER RECORDING TIMING
AV DATA EN	: AUDIO/VIDEO DATA ENABLE PERIOD (H = ENABLE)
CLK DLY	: DRCK CLOCK DELAY ROUGH ADJUSTMENT
DRCK OUT	: DRCK PLL OUTPUT
EYE CK	: EYE-PATTERN CHECK CLOCK (TEST DATA)
EYE DATA 0 - 6	: EYE-PATTERN CHECK DATA (TEST DATA)
ERROR PLS	: ERROR PULSE
PB ADJ CK	: 1/25 PLAYBACK CLOCK (FOR ADJUSTMENT)
PB CK OUT	: PLAYBACK CLOCK OUTPUT
PB DATA 0 - 7	: PLAYBACK DATA
PD OUT	: DRCK PLL PHASE DETECT
PLL F-RUN	: PLL FREE-RUN MODE
PLL-SEARCH	: PLL HIGH-SPEED SEARCH MODE
P-SAVE0, 1	: POWER SAVE (P-SAVE0 = EVEN CH, P-SAVE1 = ODD CH)
REC	: RECORDING PERIOD
REC2	: RECORDING PERIOD
REC CK	: RECORDING SYSTEM CLOCK (5 MHz)
REC DATA	: RECORDING DATA
REC DATA 0 - 7	: RECORDING DATA
REC DATA CK	: CLOCK (41.85 MHz) FOR RECORDING DATA
SERIAL OUT	: MICROCOMPUTER SERIAL DATA OUTPUT
SUB CODE EN	: SUB-CODE ENABLE PERIOD (H = ENABLE)
SYNC PLS	: SYNC PULSE
TAPE MODE	: EQ MP TAPE MODE
VCO OUT	: DRCK PLL OSCILLATOR OUTPUT

INPUT/OUTPUT

PB TEST	: TEST SIGNAL I/O FOR PLAYBACK SYSTEM
---------	---------------------------------------

