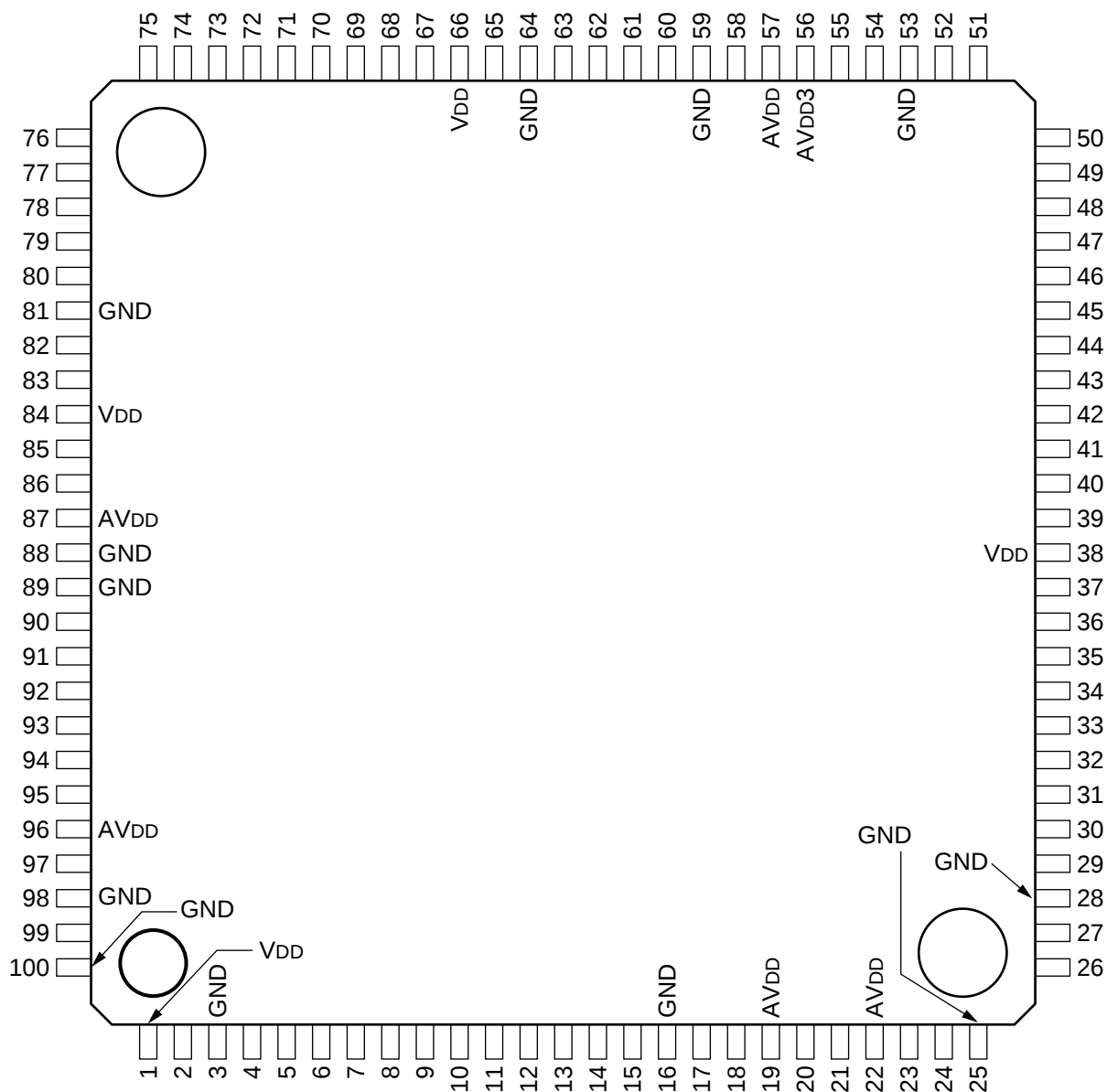


C-MOS SIGNAL PROCESSOR FOR SINGLE-PANEL CCD COLOR CAMERAS

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	V _{DD}	26	I	VREFC	51	O	FLD	76	O	CSTG
2	I	SIFSEL	27	I	VBC	52	O	SYNC	77	O	TGVD
3	—	GND	28	—	GND	53	—	GND	78	O	TGHD
4	I	AEME	29	I/O	DCK	54	O	OSCO	79	I	ID
5	I	FLON	30	O	YO7	55	I	OSCI	80	I	MCK
6	I	BLCOF	31	O	YO6	56	—	AV _{DD3}	81	—	GND
7	I	MIRIS	32	O	YO5	57	—	AV _{DD}	82	I	AESHUT
8	I	AEREF	33	O	YO4	58	I	ISSET	83	O	CLP1
9	I	AGCMAX	34	O	YO3	59	—	GND	84	—	V _{DD}
10	I	GAMMA	35	O	YO2	60	I	HRI	85	I	AD9
11	I	AWB1	36	O	YO1	61	I	VRI	86	I	AD8
12	I	AWB2	37	O	YO0	62	I/O	LRI	87	I	AD7
13	I	AWB3	38	—	V _{DD}	63	O	PCOMP	88	I	AD6
14	I	VBY	39	O	CO7	64	—	GND	89	I	AD5
15	I	VREFY	40	O	CO6	65	I	XCLP	90	I	AD4
16	—	GND	41	O	CO5	66	—	V _{DD}	91	I	AD3
17	O	IREFY	42	O	CO4	67	I	SCK	92	I	AD2
18	I	VGY	43	O	CO3	68	I	SI	93	I	AD1
19	—	AV _{DD}	44	O	CO2	69	O	SO	94	I	AD0
20	O	IOY	45	O	CO1	70	I	XCS	95	I	VRT
21	O	IOC	46	O	CO0	71	O	CASCK	96	—	AV _{DD}
22	—	AV _{DD}	47	O	NRB	72	I	CASI	97	I	SHIN
23	I	VGC	48	O	FSCO	73	O	CASO	98	—	GND
24	O	IREFC	49	O	HD	74	O	CSROM	99	I	VRB
25	—	GND	50	O	VD	75	O	CSEVR	100	—	GND

INPUT

AD9 - AD0 ; DIGITAL DATA FROM EXTERNAL A/D
 AEME ; AE MODE SELECT
 AEREF ; AE LEVEL SELECT (AUTO) /FIX GAIN SELECT (MANUAL)
 AESHUT ; H=SHUTTER SPEED MANUAL AND AGC AUTO MODE
 AGCMAX ; AGC MAX GAIN SELECT (AUTO)/FIX GAIN SELECT (MANUAL)
 AWB1 ; AWB MODE SELECT (L = AUTO, H = MANUAL)
 AWB2 ; ATW/PUSH-LOCK SELECT (AUTO) /FIX WB MODE SELECT (MANUAL)
 AWB3 ; PUSH-LOCK SIGNAL (AUTO) /FIX WB MODE SELECT (MANUAL)
 BLCOF ; BACKLIGHT CORRECTION OFF (AUTO) /SHUTTER SPEED CONTROL (MANUAL)
 CASI ; SERIAL DATA FOR CAMERA'S PERIPHERAL IC (TO EEPROM)
 FLON ; FLICKERLESS MODE (AUTO) /SHUTTER SPEED CONTROL (MANUAL)
 GAMMA ; GAMMA CORRECTION (L = ON, H = OFF)
 HRI ; EXTERNAL SYNC (COMPOSITE VIDEO/H RESET)
 ID ; LINE IDENTIFICATION
 ISET ; CURRENT SOURCE
 MCK ; MASTER CLOCK
 MIRIS ; IRIS MODE SELECT (AUTO) /SHUTTER SPEED CONTROL (MANUAL)
 OSCI ; 4fsc OSCILLATOR
 SCK ; SERIAL CLOCK
 SHIN ; ANALOG SIGNAL FOR INTERNAL A/D CONVERTER
 SI ; RS-232C SERIAL DATA
 SIFSEL ; SERIAL INTERFACE MODE SELECT
 VBC ; EXTERNAL CAPACITOR
 VBY ; EXTERNAL CAPACITOR
 VGC ; EXTERNAL CAPACITOR
 VGY ; EXTERNAL CAPACITOR
 VRB ; REFERENCE BOTTOM VOLTAGE FOR INTERNAL A/D CONVERTER
 VREFC ; REFERENCE VOLTAGE SETTING
 VREFY ; REFERENCE VOLTAGE SETTING
 VRI ; EXTERNAL SYNC (EXTERNAL BURST/V RESET)
 VRT ; REFERENCE TOP VOLTAGE FOR INTERNAL A/D CONVERTER
 XCLR ; CLEAR
 XCS ; CHIP SELECT

OUTPUT

CASCK ; SERIAL CLOCK FOR CAMERA'S PERIPHERAL IC (TO TG, EVR, EEPROM)
 CASO ; SERIAL DATA FOR CAMERA'S PERIPHERAL IC (TO TG, EVR, EEPROM)
 CLP1 ; CLAMP PULSE FOR ANALOG OPTICAL BLACK LEVEL
 CO7 - CO0 ; CHROMA DIGITAL
 CSEVR ; CHIP SELECT FOR CAMERA'S PERIPHERAL IC (TO EVR)
 CSROM ; CHIP SELECT FOR CAMERA'S PERIPHERAL IC (TO EEPROM)
 CSTG ; CHIP SELECT FOR CAMERA'S PERIPHERAL IC (TO TG)
 FLD ; FIELD IDENTIFICATION
 FSCO ; SUBCARRIER
 HD ; HORIZONTAL SYNC
 IOC ; CHROMA SIGNAL
 IOY ; Y SIGNAL
 IREFC ; REFERENCE CURRENT SETTING
 IREFY ; REFERENCE CURRENT SETTING
 NRB ; COLOR DISCRIMINATION SIGNAL
 OSCO ; 4fsc OSCILLATOR
 PCOMP ; HPLL/VPLL PHASE COMPARATOR OUTPUTS
 SO ; RS-232C SERIAL DATA
 SYNC ; COMPOSITE SYNC
 TGHD ; HORIZONTAL SYNC FOR TG
 TGVD ; VERTICAL SYNC FOR TG
 VD ; VERTICAL SYNC
 YO7 - YO0 ; Y DIGITAL

INPUT/OUTPUT

DCK ; DIGITAL OUTPUT YO/CO CLOCK
 LRI ; EXTERNAL SYNC (LALT OUTPUT/LALT RESET INPUT/INTERNAL SC INPUT)