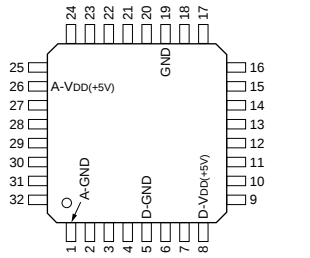
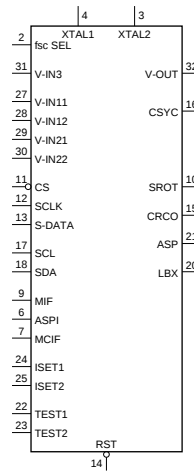

C-MOS VIDEO ADDITIONAL DATA (ASPECT RATIO IDENTIFICATION) ENCODER/DECODER
- TOP VIEW -



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	A-GND	17	I	SCL
2	I	fsc SEL	18	I/O	SDA
3	O	XTAL2	19	—	D-GND
4	I	XTAL1	20	O	LBX
5	—	D-GND	21	O	ASP
6	I	ASPI	22	I	TEST1
7	I	MCIF	23	I	TEST2
8	—	D-VDD	24	I	ISET1
9	I	MIF	25	I	ISET2
10	I/O	SROT	26	—	A-VDD
11	I	CS	27	I	V-IN11
12	I	SCLK	28	I	V-IN12
13	I	S-DATA	29	I	V-IN21
14	I	RST	30	I	V-IN22
15	O	CRCO	31	I	V-IN3
16	O	CSYC	32	O	V-OUT



INPUT

ASPI ; ASPECT SELECT (H = 16 : 9, L = 4 : 3)
CS ; CHIP SELECT
fsc SEL ; fsc/4fsc SELECT
ISET1, ISET2 ; ANALOG BIAS CURRENT SETTING
MCIF ; MICROCOMPUTER INTERFACE SELECT (L = I²C, H = SERIAL)
MIF ; MICROCOMPUTER INTERFACE SELECT (L = EXIST, H = NONE)
RST ; RESET
SCL ; I²C BUS CLOCK
SCLK ; CLOCK
S-DATA ; SERIAL DATA
TEST1, TEST2 ; FOR TEST
V-IN11 ; SYNC SEP INPUT
V-IN12 ; DECODE DATA SLICER INPUT
V-IN21 ; SYNC SEP INPUT
V-IN22 ; DECODE DATA SLICER INPUT
V-IN3 ; ENCODE INPUT
XTAL1 ; CRYSTAL OSCILLATOR

OUTPUT

ASP ; ASPECT (H = 16 : 9, L = 4 : 3)
CRCO ; CRCC CHECK MONITORING
CSYC ; COMPOSITE SYNC MONITORING
LBX ; LETTER BOX (H = LETTER BOX, L = NORMAL)
V-OUT ; ENCODE OUTPUT
XTAL2 ; CRYSTAL OSCILLATOR

INPUT/OUTPUT

SDA ; I²C BUS DATA
SROT ; SERIAL INTERFACE OUTPUT TO THE MICROCOMPUTER

