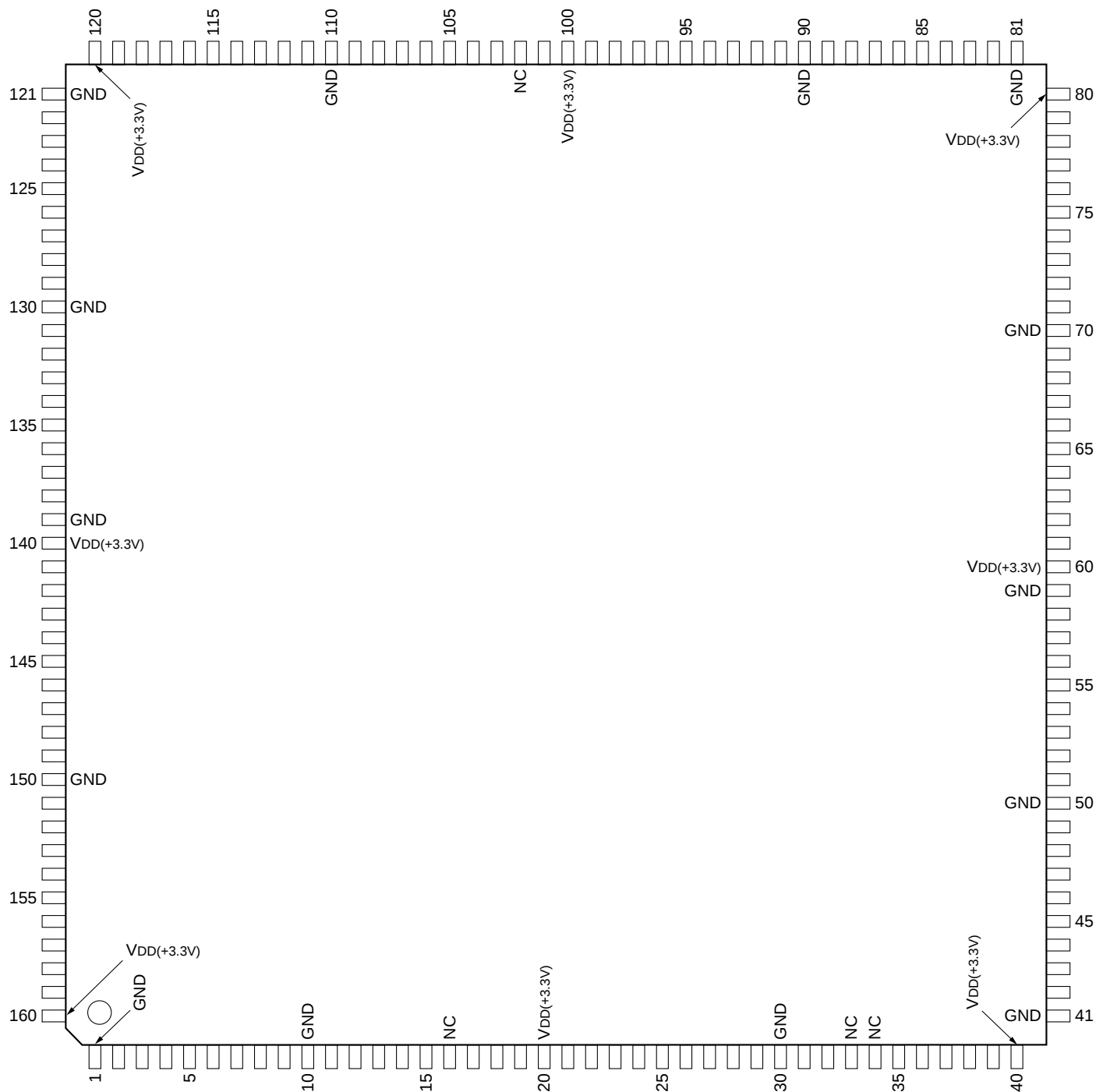

AUDIO SIGNAL PROCESSOR FOR FOUR-TIME FEED

—TOP VIEW—



(V_{DD} = +3.3V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	GND	41	—	GND	81	—	GND	121	—	GND
2	I	ITCK 0	42	I/O	SYSIO7	82	I	ITCK 3	122	O	WD2 6
3	I	AUID1 0	43	I/O	SYSIO6	83	O	ENC4D	123	O	WD2 5
4	I	AUID1 1	44	I/O	SYSIO5	84	O	ENC3D	124	O	WD2 4
5	I	AUID1 2	45	I/O	SYSIO4	85	O	ENC2D	125	O	WD2 3
6	I	SWFS1	46	I/O	SYSIO3	86	O	ENC1D34	126	O	WD2 2
7	I	AUV1	47	I/O	SYSIO2	87	O	ENC1D12	127	O	WD2 1
8	I	AUD1 34	48	I/O	SYSIO1	88	I	OMODE	128	O	WD2 0
9	I	AUD1 12	49	I/O	SYSIO0	89	O	ENCWCK	129	I	ITCK 1
10	—	GND	50	—	GND	90	—	GND	130	—	GND
11	I	GOPNO1 0	51	I	STAT1	91	O	ENCBCK	131	I	RD1 7
12	I	GOPNO1 1	52	I	STAT0	92	O	ENC32FS	132	I	RD1 6
13	I	GOPNO1 2	53	I	STRB	93	O	ENC64FS	133	I	RD1 5
14	I	GOPST1	54	I	CS	94	O	ENC128FS	134	I	RD1 4
15	I	FRST1	55	I	ITCK 5	95	O	ENC256FS	135	I	RD1 3
16	—	NC	56	I	ITCK 6	96	I	ITCK 2	136	I	RD1 2
17	I	AUID2 0	57	I	ITCK 7	97	O	ENC5FP2	137	I	RD1 1
18	I	AUID2 1	58	I	ITCK 8	98	O	ENC5FP1	138	I	RD1 0
19	I	AUID2 2	59	—	GND	99	O	ENC5FP0	139	—	GND
20	—	V _{DD}	60	—	V _{DD}	100	—	V _{DD}	140	—	V _{DD}
21	I	SWFS2	61	I	FS512	101	O	ENCFP	141	O	OE1
22	I	AUV2	62	I	FS	102	—	NC	142	O	RRST1
23	I	AUD2 34	63	I	REF5FID2	103	I	RD2 7	143	O	REN1
24	I	AUD2 12	64	I	REF5FID1	104	I	RD2 6	144	O	RCK1
25	I	GOPNO2 0	65	I	REF5FID0	105	I	RD2 5	145	O	WRST1
26	I	GOPNO2 1	66	I	REFGOP	106	I	RD2 4	146	O	WEN1
27	I	GOPNO2 2	67	I	REFV	107	I	RD2 3	147	O	WCK1
28	I	GOPST2	68	I	ITCK 4	108	I	RD2 2	148	O	WD1 7
29	I	FRST2	69	O	GOPNO2	109	I	RD2 1	149	O	WD1 6
30	—	GND	70	—	GND	110	—	GND	150	—	GND
31	I	RESET	71	O	GOPNO1	111	I	RD2 0	151	O	WD1 5
32	I	TMODE	72	O	GOPNO0	112	O	OE2	152	O	WD1 4
33	—	NC	73	O	GOPST	113	O	RRST2	153	O	WD1 3
34	—	NC	74	O	WRITEEN	114	O	REN2	154	O	WD1 2
35	I	TCK	75	O	STATUS3/4	115	O	RCK2	155	O	WD1 1
36	I	TDI	76	O	STATUS1/2	116	O	WRST2	156	O	WD1 0
37	I	TENAI	77	O	AFRAID2	117	O	WEN2	157	I	ITEST
38	O	TDO	78	O	AFRAID1	118	O	WCK2	158	I	BETA34
39	I	VST	79	O	AFRAID0	119	O	WD2 7	159	I	BETA12
40	—	V _{DD}	80	—	V _{DD}	120	—	V _{DD}	160	—	V _{DD}

INPUT

AUD1 12	; AUDIO DATA (CH-1/CH-2) IN BETACAM PB MODE
AUD1 34	; AUDIO DATA (CH-3/CH-4) IN BETACAM PB MODE
AUD2 12	; AUDIO DATA (CH-1/CH-2)
AUD2 34	; AUDIO DATA (CH-3/CH-4)
AUID1 0 - AUID1 2	; ID OF AUDIO FIVE-FIELD SEQUENCE PULSE
AUID2 0 - AUID2 2	; ID OF AUDIO FIVE-FIELD SEQUENCE PULSE
AUV1, AUV2	; AUDIO ONE-FIELD DATA START
BETA12	; AUDIO INPUT DATA (CH-1/CH-2) IN BETACAM PB MODE
BETA34	; AUDIO INPUT DATA (CH-3/CH-4) IN BETACAM PB MODE
CS	; CHIP SELECT
FRST1, FRST2	; AUDIO FRAME START PULSE
FS	; INTERNAL CLOCK PULSE
FS512	; MASTER CLOCK FOR INTERNAL CLOCK
GOPNO1 0 - GOPNO1 2	; ID OF GOP NUMBER
GOPNO2 0 - GOPNO2 2	; ID OF GOP NUMBER
GOPST1, GOPST2	; GOP START PULSE
ITCK 0 - ITCK 8	; CLOCK
ITEST	; IC TEST
OMODE	; MODE SELECT FOR AUDIO DATA OUTPUT
RD1 7 - RD1 0	; DATA FROM FIFO1
RD2 7 - RD2 0	; DATA FROM FIFO2
REF5FID0 - REF5FID2	; FIVE-FIELD ID FOR FIVE-FRAME IN BETACAM PB MODE
REFGOP	; GOP START PULSE IN BETACAM PB MODE
REFV	; REFERENCE FIELD PULSE
RESET	; RESET
STAT0, STAT1	; STATUS
STRB	; STROBE
SWFS1, SWFS2	; SAMPLING CLOCK (fs OR 8/3fs)
TCK	; IC TEST
TDI	; IC TEST
TENAI	; IC TEST
TMODE	; TEST MODE
VST	; IC TEST

OUTPUT

AFRAID0 - AFRAID2	; AUDIO FRAME ID
ENC1D12	; AUDIO DATA (CH-1/CH-2)
ENC1D34	; AUDIO DATA (CH-3/CH-4)
ENC2D - ENC4D	; AUDIO DATA IN FEED MODE
ENC5FP0 - ENC5FP2	; AUDIO FIVE FRAME ID
ENC32FS	; 32fs CLOCK
ENC64FS	; 64fs CLOCK
ENC128FS	; 128fs CLOCK
ENC256FS	; 256fs CLOCK
ENCBCK	; BIT CLOCK
ENCFP	; AUDIO FRAME PULSE
ENCWCK	; WORD CLOCK
GOPNO0 - GOPNO2	; GOP CYCLIC ID NUMBER
GOPST	; GOP DATA START PULSE (LOW ACTIVE)
OE1, OE2	; FIFO OUTPUT ENABLE
RCK1, RCK2	; FIFO READ CLOCK
REN1, REN2	; FIFO READ ENABLE
RRST1, RRST2	; FIFO READ RESET
STATUS1/2	; AUDIO EMPHASIS AND MUTE FLAG (CH-1/CH-2)
STATUS3/4	; AUDIO EMPHASIS AND MUTE FLAG (CH-3/CH-4)
WCK1, WCK2	; FIFO WRITE CLOCK
WD1 0 - WD1 7	; DATA TO FIFO1
WD2 0 - WD2 7	; DATA TO FIFO2
WEN1, WEN2	; FIFO WRITE ENABLE
WRITEEN	; AUDIO DATA EXISTENCE (H : NONE, L : EXIST)
WRST1, WRST2	; FIFO WRITE RESET

INPUT/OUTPUT

SYSIO0 - SYSIO7	; ADDRESS/DATA
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