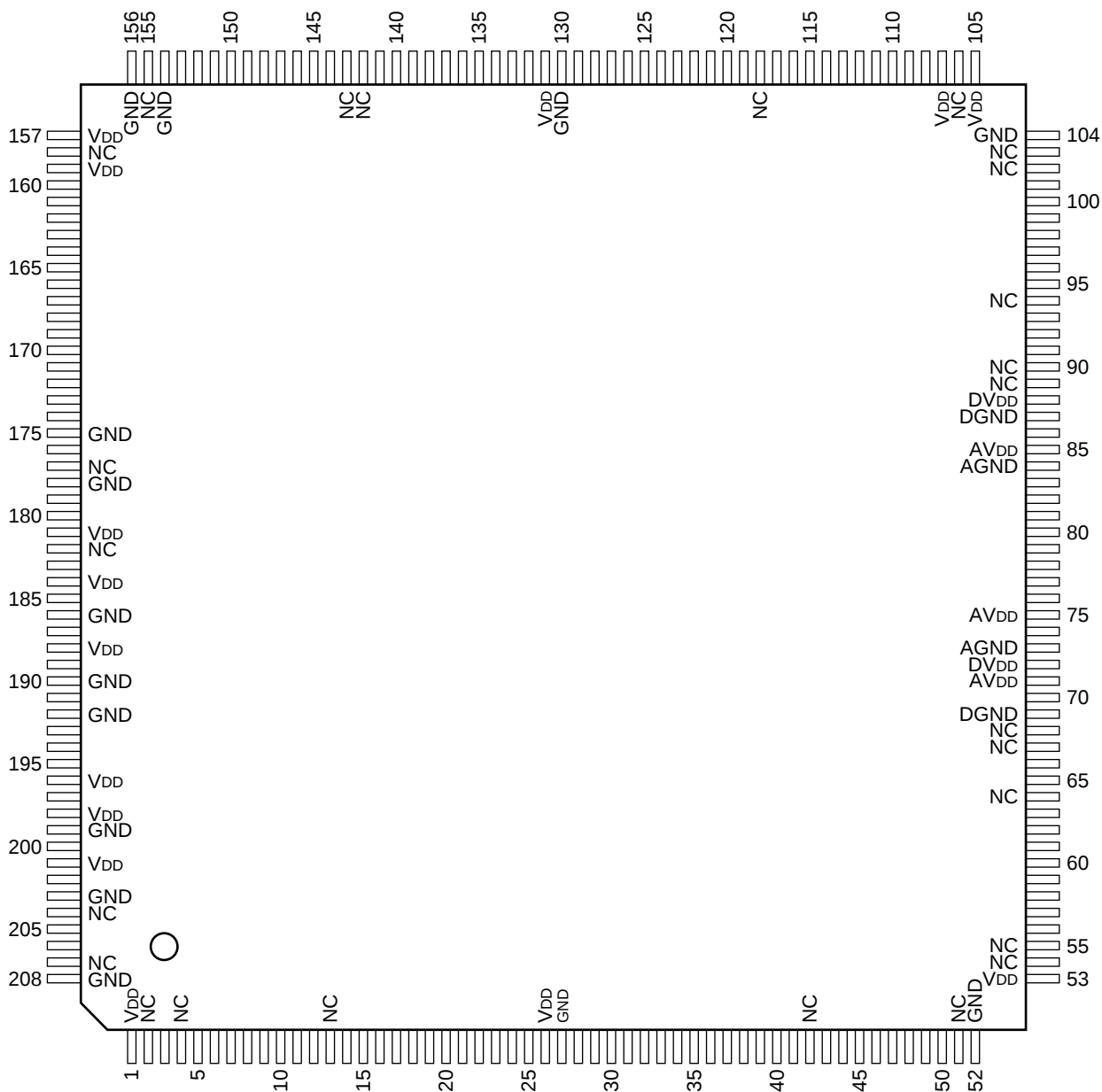


—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	VDD	53	—	VDD	105	—	VDD	157	—	VDD
2	—	NC	54	—	NC	106	—	NC	158	—	NC
3	I	RST	55	—	NC	107	—	VDD	159	—	VDD
4	—	NC	56	I	DINY7	108	I	MIB4	160	O	RXIA
5	I	SASL	57	I	DINY6	109	I	MIB3	161	O	RRA
6	I	TESTSW	58	I	DINY5	110	I	MIB2	162	O	RXAD
7	I	TEST1	59	I	DINY4	111	I	MIB1	163	O	MOA0
8	I	TEST2	60	I	DINY3	112	I	MIB0	164	O	MOA1
9	I	TEST3	61	I	DINY2	113	O	REB	165	O	MOA2
10	O	YS	62	I	DINY1	114	O	RXIB	166	O	MOA3
11	O	HSR	63	I	DINY0	115	O	RRB	167	O	MOA4
12	O	VSR	64	—	NC	116	O	MOB0	168	O	MOA5
13	—	NC	65	I	CLKD	117	O	MOB1	169	O	MOA6
14	I	AINY7	66	I	HSD	118	—	NC	170	O	MOA7
15	I	AINY6	67	—	NC	119	O	MOB2	171	O	MOA8
16	I	AINY5	68	—	NC	120	O	MOB3	172	O	MOA9
17	I	AINY4	69	—	DGND	121	O	MOB4	173	O	MOA10
18	I	AINY3	70	O	IO3	122	O	MOB5	174	O	MOA11
19	I	AINY2	71	—	AVDD	123	O	MOB6	175	—	GND
20	I	AINY1	72	—	DVDD	124	O	MOB7	176	I	CLKX
21	I	AINY0	73	—	AGND	125	O	MOB8	177	—	NC
22	I	AINU7	74	O	IO2	126	O	MOB9	178	—	GND
23	I	AINU6	75	—	AVDD	127	O	MOB10	179	I	XTALI
24	I	AINU5	76	I	VG3	128	O	MOB11	180	O	XTALO
25	I	AINU4	77	I	IRI3	129	O	WADE	181	—	VDD
26	—	VDD	78	I	VG2	130	—	GND	182	—	NC
27	—	GND	79	I	IRI2	131	—	VDD	183	O	DIV1
28	I	AINU3	80	I	VG1	132	O	WCLK	184	—	VDD
29	I	AINU2	81	I	IRI1	133	O	RADE	185	I	HSWI
30	I	AINU1	82	I	VRI	134	O	RCLK	186	—	GND
31	I	AINU0	83	I	VRO	135	O	WXIA	187	—	FILTER1
32	I	AINV7	84	—	AGND	136	O	WEA	188	—	VDD
33	I	AINV6	85	—	AVDD	137	O	WRA	189	I/O	CLK1
34	I	AINV5	86	O	IO1	138	O	WXAD	190	—	GND
35	I	AINV4	87	—	DGND	139	I	MIA11	191	O	CKDIV1
36	I	AINV3	88	—	DVDD	140	I	MIA10	192	—	GND
37	I	AINV2	89	—	NC	141	I	MIA9	193	I/O	SDA
38	I	AINV1	90	—	NC	142	—	NC	194	I	SCL
39	I	AINV0	91	O	WXIB	143	—	NC	195	I	VSWI
40	O	CLPO	92	O	WEB	144	I	MIA8	196	—	VDD
41	O	ADCLK	93	O	WRB	145	I	MIA7	197	O	DIV2
42	—	NC	94	—	NC	146	I	MIA6	198	—	VDD
43	I	DINC7	95	I	MIB11	147	I	MIA5	199	—	GND
44	I	DINC6	96	I	MIB10	148	I	MIA4	200	—	FILTER2
45	I	DINC5	97	I	MIB9	149	I	MIA3	201	—	VDD
46	I	DINC4	98	I	MIB8	150	I	MIA2	202	I/O	CLK2
47	I	DINC3	99	I	MIB7	151	I	MIA1	203	—	GND
48	I	DINC2	100	I	MIB6	152	I	MIA0	204	—	NC
49	I	DINC1	101	I	MIB5	153	O	REA	205	O	VSWO
50	I	DINC0	102	—	NC	154	—	GND	206	O	HSWO
51	—	NC	103	—	NC	155	—	NC	207	—	NC
52	—	GND	104	—	GND	156	—	GND	208	—	GND

INPUT

AINU7 - AINU0	: B-Y SIGNAL
AINV7 - AINV0	: R-Y SIGNAL
AINY7 - AINY0	: Y SIGNAL
CLKD	: DIGITAL INTERFACE CLOCK
CLKX	: EXTERNAL CLOCK
DINC7 - DINC0	: DIGITAL COLOR DIFFERENCE SIGNAL
DINY7 - DINY0	: DIGITAL Y SIGNAL
HSD	: DIGITAL INTERFACE H SYNC
HSWI	: HORIZONTAL SYNC SIGNAL
IRI3 - IRI1	: EXTERNAL RESISTOR
MIA11 - MIA0	: FIELD MEMORY A READ DATA
MIB11 - MIB0	: FIELD MEMORY B READ DATA
RST	: POWER ON RESET
SASL	: I ² C BUS SLAVE ADDRESS SETTING
SCL	: I ² C BUS CLOCK
TEST1 - TEST3	: (FOR TEST)
TESTSW	: (FOR TEST)
VG3 - VG1	: EXTERNAL CAPACITOR
VRI	: EXTERNAL CAPACITOR
VRO	: EXTERNAL CAPACITOR
VSWI	: VERTICAL SYNC SIGNAL
XTALI	: EXTERNAL CRYSTAL

OUTPUT

ADCLK	: A/D CONVERSION CLOCK
CKDIV1	: EXTERNAL CLOCK DIVIDER SELECT
CLPO	: CLAMP PULSE
DIV1 - DIV2	: DIVIDED CLOCK TO EXTERNAL PLL
HSR	: HORIZONTAL SYNC SIGNAL
HSWO	: WRITE HORIZONTAL SYNC SIGNAL
IO3 - IO1	: DAC CURRENT
MOA0 - MOA11	: FIELD MEMORY A WRITE DATA
MOB0 - MOB11	: FIELD MEMORY B WRITE DATA
RADE	: FIELD MEMORY A/B READ ADDRESS ENABLE
RCLK	: FIELD MEMORY A/B READ CLOCK
REA	: FIELD MEMORY A READ ENABLE
REB	: FIELD MEMORY B READ ENABLE
RRA	: FIELD MEMORY A READ ADDRESS RESET
RRB	: FIELD MEMORY B READ ADDRESS RESET
RXAD	: FIELD MEMORY A READ ADDRESS
RXIA	: FIELD MEMORY A READ ADDRESS INCREMENT
RXIB	: FIELD MEMORY B READ ADDRESS INCREMENT
VSR	: VERTICAL SYNC SIGNAL
VSWO	: WRITE VERTICAL SYNC SIGNAL
WADE	: FIELD MEMORY A/B ADDRESS ENABLE
WCLK	: FIELD MEMORY A/B WRITE CLOCK
WEA	: FIELD MEMORY A WRITE ENABLE
WEB	: FIELD MEMORY B WRITE ENABLE
WRA	: FIELD MEMORY A WRITE ADDRESS RESET
WRB	: FIELD MEMORY B WRITE ADDRESS RESET
WXAD	: FIELD MEMORY A WRITE ADDRESS
WXIA	: FIELD MEMORY A WRITE ADDRESS INCREMENT
WXIB	: FIELD MEMORY B WRITE ADDRESS INCREMENT
XTALO	: EXTERNAL CRYSTAL
YS	: YS SIGNAL

INPUT/OUTPUT

CLK1 - CLK2	: CLOCK FROM EXTERNAL PLL
SDA	: I ² C BUS DATA

