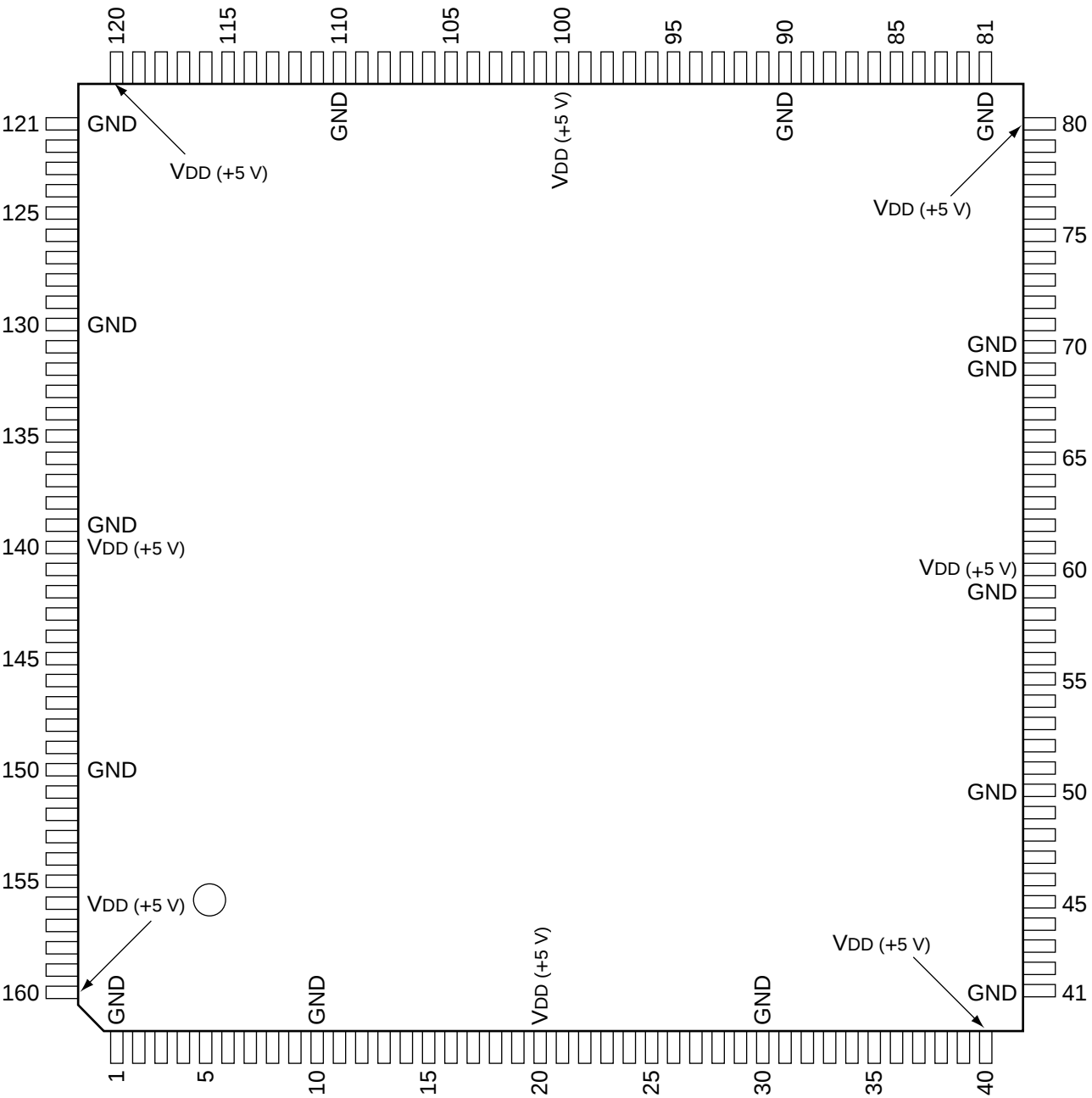

C-MOS GATE ARRAY
—TOP VIEW—



(V_{DD} = +5 V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	—	GND	41	—	GND	81	—	GND	121	—	GND
2	I	D9	42	I	M RQ	82	I	CAS	122	O	CA5
3	I	D10	43	O	M AK	83	I	RAS	123	O	CA6
4	I	D11	44	O	M WE	84	O	CRKEY	124	O	CA7
5	I	D12	45	O	M RE	85	O	CRR	125	O	CA8
6	I	D13	46	O	TLR	86	O	CRG	126	O	CA9
7	I	D14	47	O	TLW	87	O	CRB	127	O	CA10
8	I	D15	48	I	J RQ	88	O	IN 1	128	O	CA11
9	I	A23	49	O	J AK	89	O	IN 2	129	O	CA12
10	—	GND	50	—	GND	90	—	GND	130	—	GND
11	I	A22	51	O	JWE	91	O	IN 3	131	O	CA13
12	I	A21	52	O	JRE	92	O	IN 4	132	O	CA14
13	I	A20	53	I	F RQ	93	O	OUT1	133	O	CA15
14	I	A19	54	O	F AK	94	O	OUT2	134	O	CA16
15	I	A18	55	O	F WE	95	O	OUT3	135	I/O	CD0
16	I	A17	56	I	FDTAK	96	O	OUT4	136	I/O	CD1
17	I	A 16	57	O	DMDIR	97	O	OUT5	137	I/O	CD2
18	I	A15	58	O	LDEN1	98	O	OUT6	138	I/O	CD3
19	I	A14	59	—	GND	99	O	IN 5	139	—	GND
20	—	V _{DD}	60	—	V _{DD}	100	—	V _{DD}	140	—	V _{DD}
21	I	A13	61	O	UDEN1	101	I	RST	141	I/O	CD4
22	I	A12	62	O	LDEN2	102	I	TST1	142	I/O	CD5
23	I	A11	63	O	UDEN2	103	O	PRCS	143	I/O	CD6
24	I	A10	64	I	RHD	104	O	EECS	144	I/O	CD7
25	I	A9	65	I	RVD	105	O	JPCS	145	O	DTACK
26	I	A8	66	I	RFLD	106	O	FMCS	146	I	RD
27	I	A7	67	O	BLKO	107	O	ESCS	147	I	LDS
28	I	A6	68	I	RCK	108	O	MSCS	148	I	UDS
29	I	A5	69	—	GND	109	O	FWCS	149	I	AS
30	—	GND	70	—	GND	110	—	GND	150	—	GND
31	I	A4	71	I	WVD	111	O	CRDG	151	I	SYCK (16M)
32	I	A3	72	I	WFLD	112	O	COE	152	I	TST2
33	I	A2	73	I	WCK	113	O	CWE	153	O	DR00
34	I	A1	74	O	VINT	114	O	CCS	154	O	DR01
35	I	TCK	75	O	ADAEN	115	O	CA0	155	O	DR02
36	I	TDI	76	I	W CSNC	116	O	CA1	156	I	DAK0
37	I	TENA1	77	O	W EXT	117	O	CA2	157	I	DAK1
38	O	TDO	78	O	W CTHD	118	O	CA3	158	I	DAK2
39	I	VST	79	O	R CTHD	119	O	CA4	159	I	D8
40	—	V _{DD}	80	—	V _{DD}	120	—	V _{DD}	160	—	V _{DD}