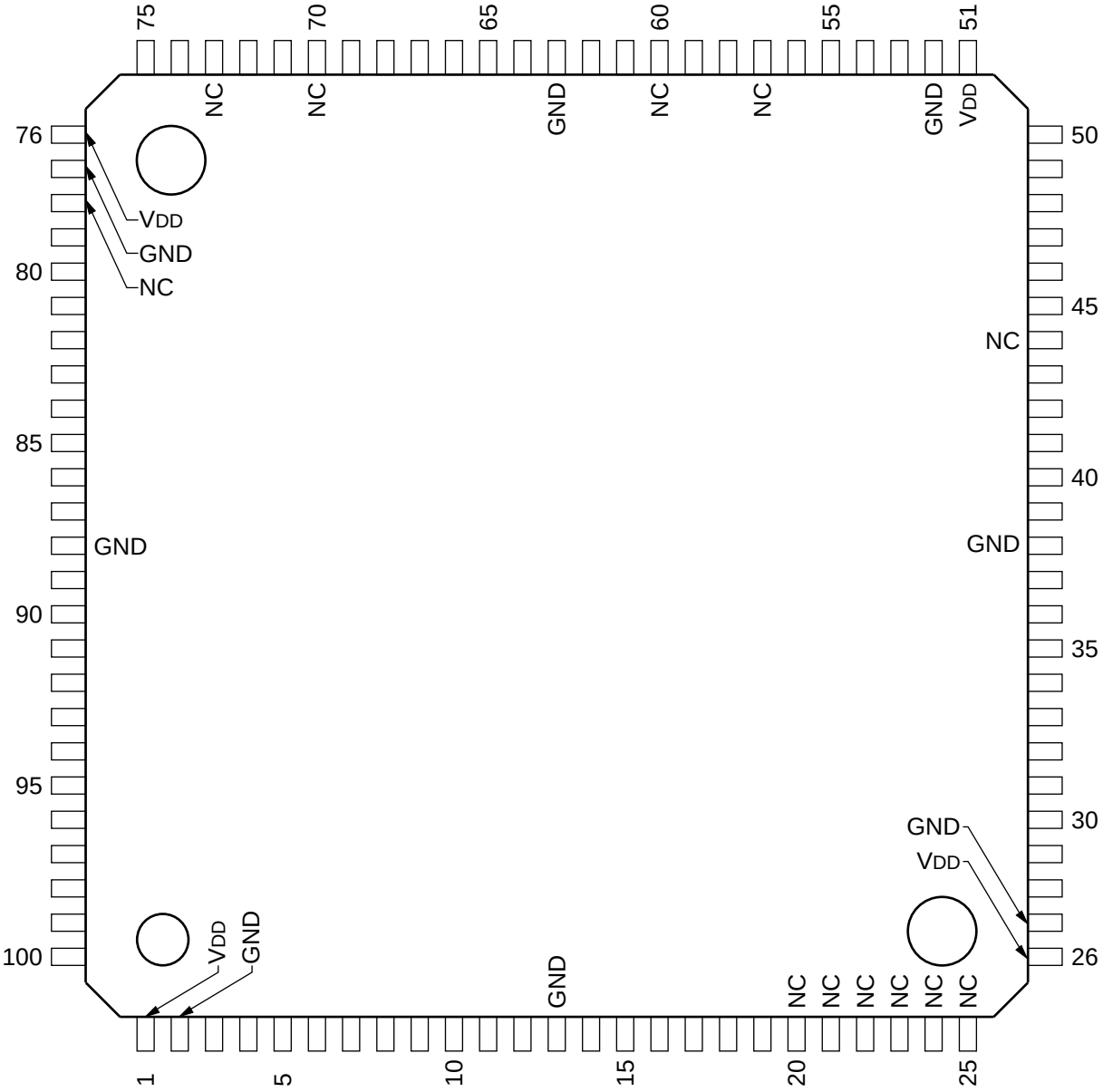
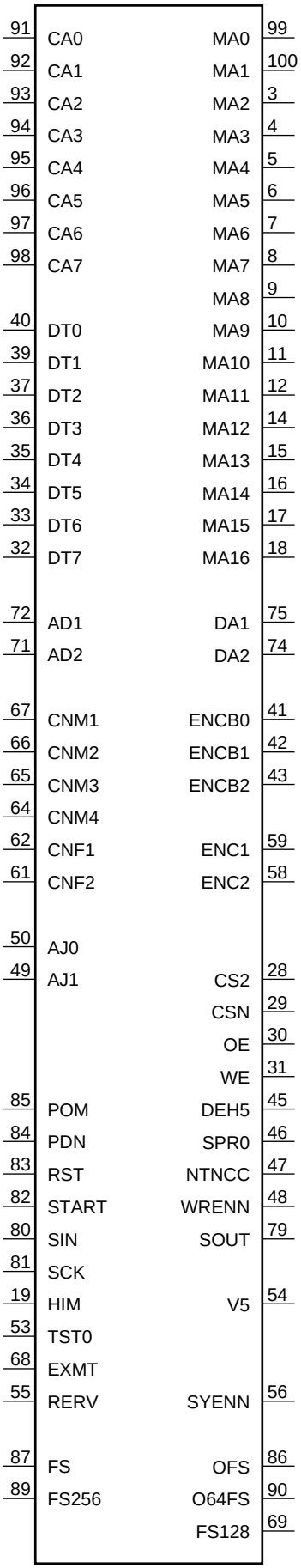
C-MOS AUDIO PROCESSOR

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	—	V_{DD}	26	—	V_{DD}	51	—	V_{DD}	76	—	V_{DD}
2	—	GND	27	—	GND	52	—	GND	77	—	GND
3	O	MA2	28	O	CS2	53	I	TST0	78	—	NC
4	O	MA3	29	O	CSN	54	O	V5	79	O	SOUT
5	O	MA4	30	O	OE	55	I	REFV	80	I	SIN
6	O	MA5	31	O	WE	56	O	SYENN	81	I	SCK
7	O	MA6	32	I/O	DT7	57	—	NC	82	I	START
8	O	MA7	33	I/O	DT6	58	O	ENC2	83	I	RST
9	O	MA8	34	I/O	DT5	59	O	ENC1	84	I	PDN
10	O	MA9	35	I/O	DT4	60	—	NC	85	I	POM
11	O	MA10	36	I/O	DT3	61	I	CNF2	86	O	OFS
12	O	MA11	37	I/O	DT2	62	I	CNF1	87	I	FS
13	—	GND	38	—	GND	63	—	GND	88	—	GND
14	O	MA12	39	I/O	DT1	64	I	CNM4	89	I	FS256
15	O	MA13	40	I/O	DT0	65	I	CNM3	90	O	O64FS
16	O	MA14	41	O	ENCB0	66	I	CNM2	91	I	CA0
17	O	MA15	42	O	ENCB1	67	I	CNM1	92	I	CA1
18	O	MA16	43	O	ENCB2	68	I	EXMT	93	I	CA2
19	I	HIM	44	—	NC	69	O	FS128	94	I	CA3
20	—	NC	45	O	DEH5	70	—	NC	95	I	CA4
21	—	NC	46	O	SPR0	71	I	AD2	96	I	CA5
22	—	NC	47	O	NTNCC	72	I	AD1	97	I	CA6
23	—	NC	48	O	WRENN	73	—	NC	98	I	CA7
24	—	NC	49	I	AJ1	74	O	DA2	99	O	MA0
25	—	NC	50	I	AJ2	75	O	DA1	100	O	MA1



INPUT

AD1	;	A/D 1/2
AD2	;	A/D 3/4
AJ0, AJ1	;	ADJUST 0, 1
CA0 - CA7	;	CHIP ADDRESS 0 - 7
CNF1	;	CONFI 1/2
CNF2	;	CONFI 3/4
CNM1 - CNM4	;	CONFI MUTE 1 - 4
EXMT	;	EXTERNAL MUTE
FS	;	FS
FS256	;	256 FS
PDN	;	H : 1M SRAM / L : 256K
HIM	;	L : POWER DOWN
POM	;	POWER ON MUTE
REFV	;	REFERENCE V
RST	;	RESET
SCK	;	SERIAL CLOCK
SIN	;	SERIAL DATA
START	;	SERIAL START
TST0	;	TEST 0

OUTPUT

CS2	;	CHIP SELECT 2
CSN	;	CHIP SELECT ENABLE
DA1	;	D/A 1/2
DA2	;	D/A 3/4
DEH5	;	ADDRESS DECODE 5H
ENC1	;	ENCODE 1/2
ENC2	;	ENCODE 3/4
ENCB0 - ENCB2	;	ENCODE BIT 0 - 2
FS128	;	128 FS
MA0 - MA16	;	MEMORY ADDRESS 0 - 16
NTNCC	;	H : NTSC / L : PAL
O64FS	;	64 FS
OE	;	OUTPUT ENABLE
OFS	;	FS
SOUT	;	SERIAL DATA
SPR0	;	SPARE 0
SYENN	;	L : SERIAL ENABLE
V5	;	5V CLOCK
WE	;	WRITE ENABLE
WRENN	;	WRITE

INPUT/OUTPUT

DT0 - DT7	;	MEMORY DATA 0 - 7