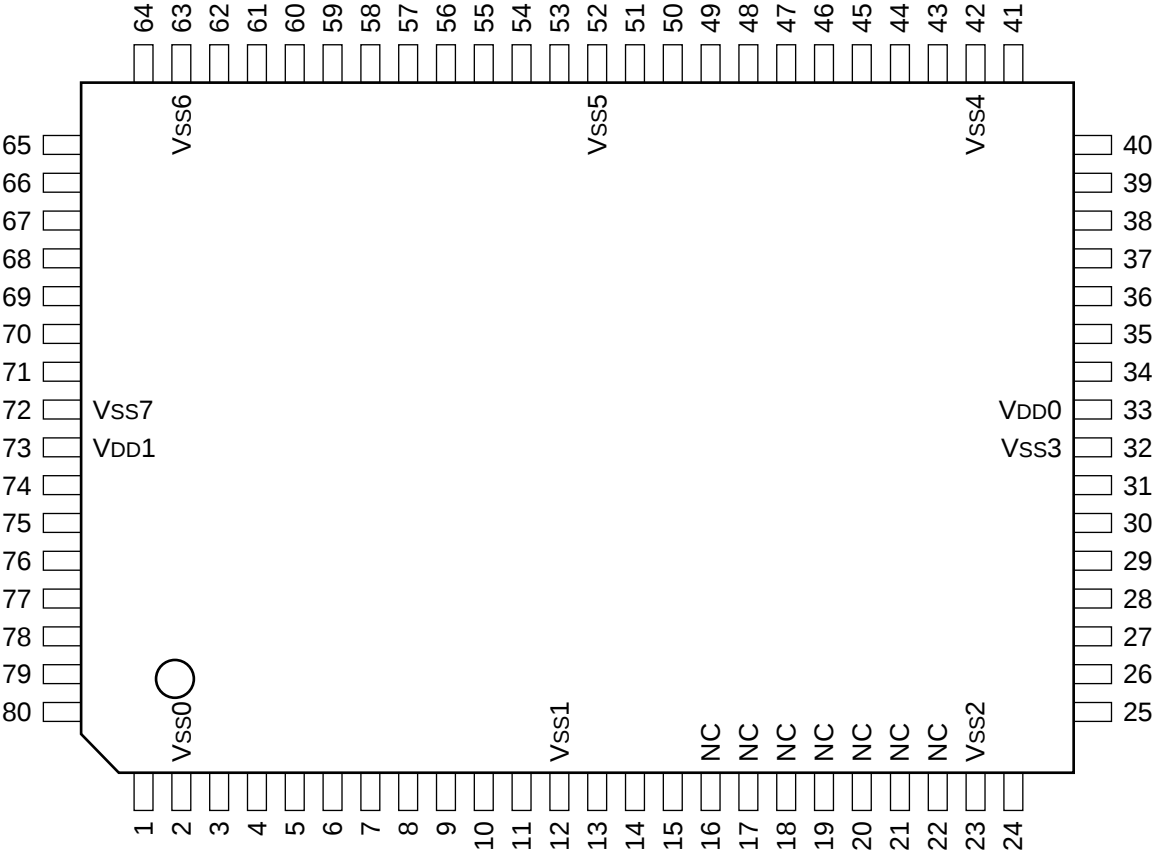


C-MOS A/D CONVERTED SIGNAL INTERFACE

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	BCF	21	—	NC	41	I	C5	61	I	Y5
2	—	Vss0	22	—	NC	42	—	Vss4	62	I	Y4
3	O	BVD	23	—	Vss2	43	I	C4	63	—	Vss6
4	O	BHD	24	I	SC	44	I	C3	64	I	Y3
5	O	BD0	25	I	SI	45	I	C2	65	I	Y2
6	O	BD1	26	I	SR	46	I	C1	66	I	Y1
7	O	BD2	27	O	SOT	47	I	C0	67	I	Y0
8	O	BD3	28	I	A1	48	I	CPA	68	I	YPA
9	O	BD4	29	I	A0	49	O	DB	69	I	FNTSC
10	O	BD5	30	I	VP4	50	O	HCK	70	I	MRST
11	O	BD6	31	I	VP3	51	O	QCK	71	I	WCK
12	—	Vss1	32	—	Vss3	52	—	Vss5	72	—	Vss7
13	O	BD7	33	—	VDD0	53	O	BER	73	—	VDD1
14	O	BD8	34	I	VP2	54	O	RER	74	I	SY
15	O	BD9	35	I	VP1	55	O	YER	75	O	HRO
16	—	NC	36	I	VP0	56	O	CLP	76	O	CHO
17	—	NC	37	I	C9	57	I	Y9	77	O	PEN
18	—	NC	38	I	C8	58	I	Y8	78	I	PH1
19	—	NC	39	I	C7	59	I	Y7	79	I	PH0
20	—	NC	40	I	C6	60	I	Y6	80	O	BP

INPUT

A1 - A0 : IC ADDRESS
C9 - C0 : C (R-Y/B-Y) DATA
CPA : C PARITY
FNTSC : FORCED NTSC MODE
MRST : MASTER RESET
PH1, PH0 : Y/C DELAY ADJ.
SC : MPU I/F SERIAL CLOCK
SI : MPU I/F DATA
SR : MPU I/F START PULSE
SY : SYNC
VP4 - VP0 : VIDEO PHASE ADJ.
WCK : 27 MHz CLOCK
Y9 - Y0 : Y DATA
YPA : Y PARITY

OUTPUT

BCF : COLOR FRAME
BD9 - BD0 : MULTIPLEX DATA
BER : B-Y CLAMP ERROR
BHD : HORIZONTAL DRIVE
BP : PARITY
BVD : VERTICAL DRIVE
CHO : COUNT H
CLP : CLAMP PULSE
DB : D1/B-CAM MODE
HCK : 13.5 MHz CLOCK
HRO : H RESET PULSE
PEN : PHASE COMPARE ENABLE
QCK : 6.75 MHz CLOCK
RER : R-Y CLAMP ERROR
SOT : MPU I/F DATA
YER : Y CLAMP ERROR

