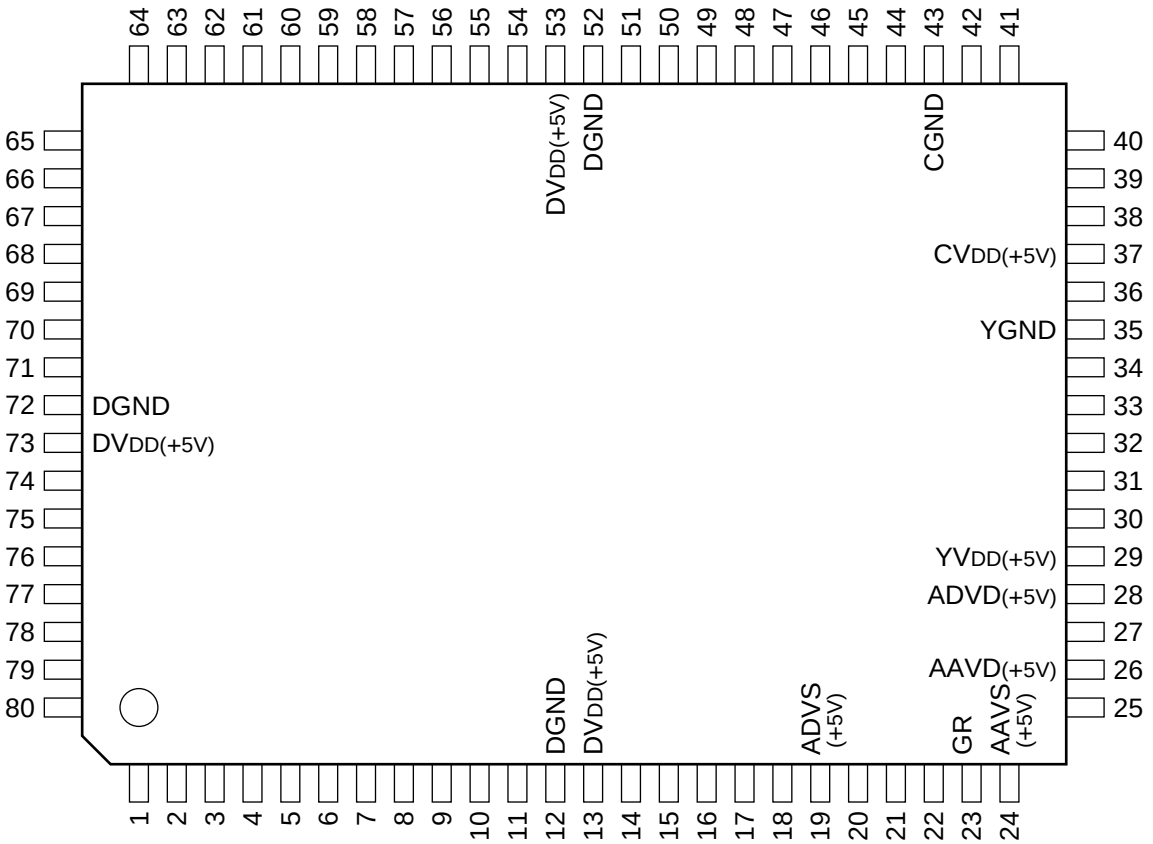
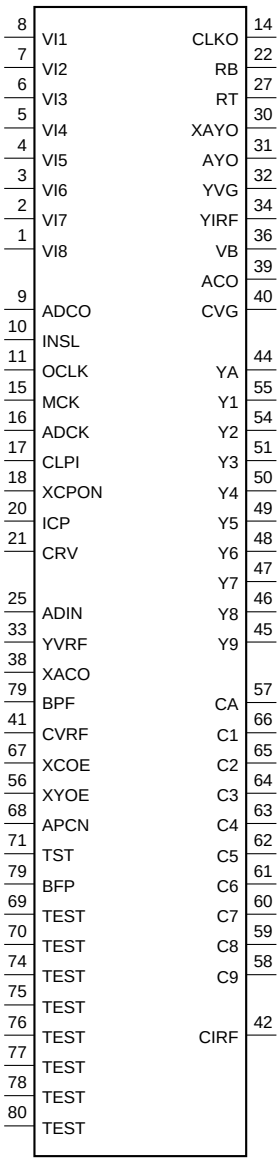
C-MOS DIGITAL COMB FILTER

—TOP VIEW—



(VDD = +5V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	VI8	21	I	CRV	41	I	CVRF	61	O	C6
2	I	VI7	22	O	RB	42	O	CIRF	62	O	C5
3	I	VI6	23	—	GR	43	—	CGND	63	O	C4
4	I	VI5	24	—	AAVS	44	O	YA	64	O	C3
5	I	VI4	25	I	ADIN	45	O	Y9	65	O	C2
6	I	VI3	26	—	AAVD	46	O	Y8	66	O	C1
7	I	VI2	27	O	RT	47	O	Y7	67	I	XCOE
8	I	VI1	28	—	ADVD	48	O	Y6	68	I	APCN
9	I	ADCO	29	—	YVDD	49	O	Y5	69	I	TEST
10	I	INSL	30	O	XAYO	50	O	Y4	70	I	TEST
11	I	OCLK	31	O	AYO	51	O	Y3	71	I	TST
12	—	DGND	32	O	YVG	52	—	DGND	72	—	DGND
13	—	DVDD	33	I	YVRF	53	—	DVDD	73	—	DVDD
14	O	CLKO	34	O	YIRF	54	O	Y2	74	I	TEST
15	I	MCK	35	—	YGND	55	O	Y1	75	I	TEST
16	I	ADCK	36	O	VB	56	I	XYOE	76	I	TEST
17	I	CLPI	37	—	CVDD	57	O	CA	77	I	TEST
18	I	XCPON	38	I	XACO	58	O	C9	78	I	TEST
19	—	ADVS	39	O	ACO	59	O	C8	79	I	BPF
20	I	ICP	40	O	CVG	60	O	C7	80	I	TEST



INPUT

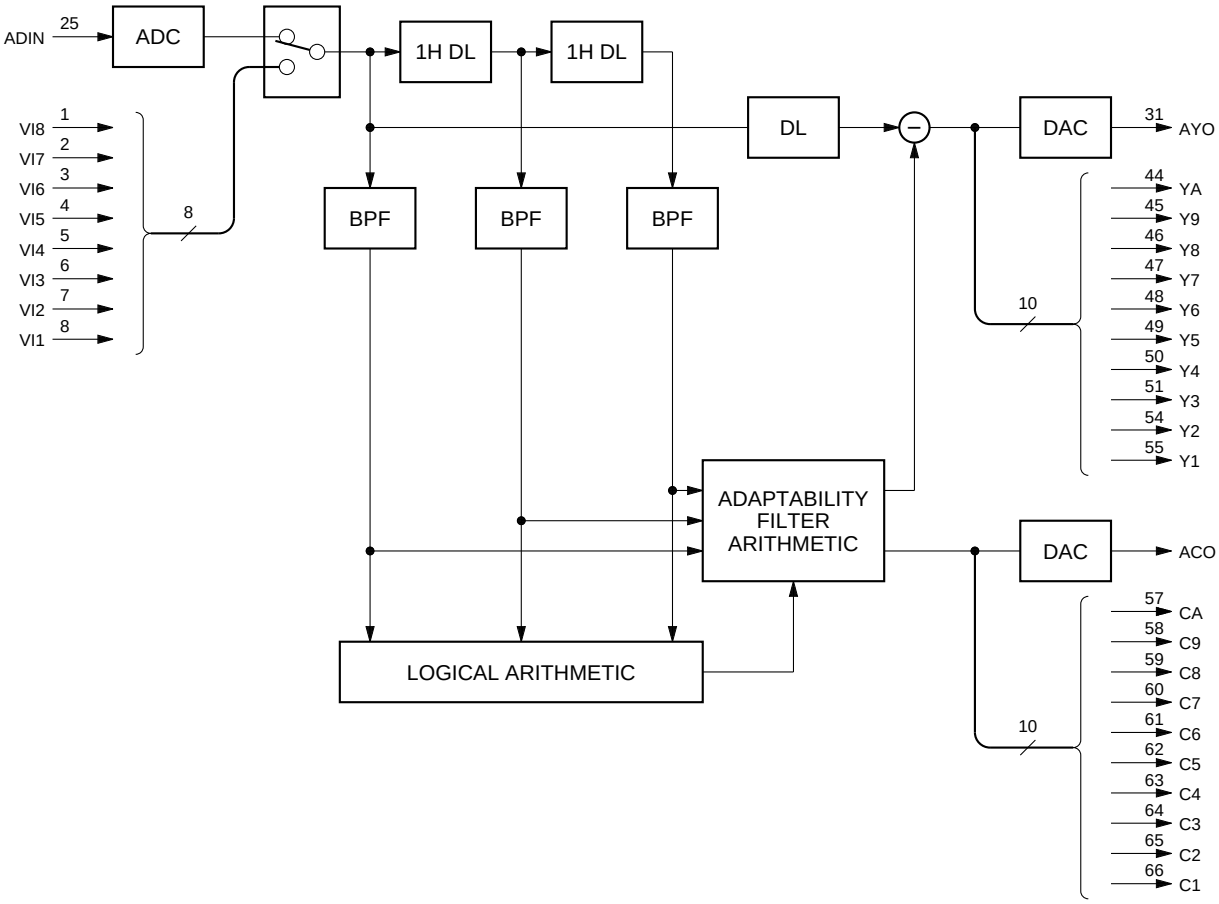
ADCK	;	AD CONVERTER CLOCK
ADCO	;	H : AD CONVERTER
		L : USUALLY MODE
ADIN	;	COMB FILTER ANALOG
APCN	;	APERTURE CORRECTION
		H : APERTURE EFFECT
		L : STANDARD MODE
BPF	;	H : BPF SEPARATION FIXATION
		L : STANDARD MODE
CLPI	;	AD CONVERTER CLAMP PULSE
CRV	;	CLAMP REFERENCE VOLTAGE
CVRF	;	ANALOG CHROMA SIGNAL
ICP	;	CLAMP CONTROL
INSL	;	INPUT SELECT
		H : DIGITAL INPUT
		L : ANALOG INPUT
MCK	;	CLOCK
OCLK	;	CLOCK AMP.
TEST	;	TEST
TST	;	Y OUTPUT SLEW MODE
		H : COMPOSITE VIDEO SIGNAL
		L : Y/C SEPARATION MODE
VI1 - VI8	;	DIGITAL INPUT
XACO	;	ACO INVERT CURRENT
XCPON	;	H : CLAMP FUNCTION “OFF”
		L : CLAMP FUNCTION
XCOE	;	DIGITAL CHROMA SIGNAL CONTROL
		H : HIGH IMPEDANCE
		L : ENABLE
XYOE	;	DIGITAL Y SIGNAL CONTROL
		H : HIGH IMPEDANCE
		L : ENABLE
YVRF	;	ANALOG Y SIGNAL

OUTPUT

ACO	;	ANALOG CHROMA SIGNAL
AYO	;	ANALOG Y SIGNAL
CA, C1 - C9	;	DIGITAL CHROMA SIGNAL
CIRF	;	SETTING FOR ANALOG CHROMA OUTPUT
CLKO	;	CLOCK AMP.
CVG	;	VOLTAGE FOR ANALOG CHROMA SIGNAL
RB	;	REFERENCE VOLTAGE (BOTTOM) STANDARD (0.5V)
RT	;	REFERENCE VOLTAGE (TOP) STANDARD (2.6V)
VB	;	VOLTAGE BIAS
XAYO	;	AYO INVERT CURRENT
YA, Y1 - Y9	;	DIGITAL Y SIGNAL
YIRF	;	SETTING FOR ANALOG Y OUTPUT
YVG	;	VOLTAGE FOR ANALOG Y SIGNAL

OTHER

AAVD	;	AD CONVERTER ANALOG POWER
AAVS	;	AD CONVERTER ANALOG GND
ADVS	;	AD CONVERTER DIGITAL GND
ADVD	;	AD CONVERTER DIGITAL POWER
GR	;	GARD RING
CVSS	;	C/DA CONVERTER GND
CVDD	;	C/DA CONVERTER POWER
YVDD	;	Y/DA CONVERTER POWER
YVSS	;	Y/DA CONVERTER ANALOG GND


ADIN
25
ADC
1H DL
1H DL
DL
DAC
31
AYO
VI8
VI7
VI6
VI5
VI4
VI3
VI2
VI1
1
2
3
4
5
6
7
8
8
BPF
BPF
BPF
10
44
YA
45
Y9
46
Y8
47
Y7
48
Y6
49
Y5
50
Y4
51
Y3
54
Y2
55
Y1
ADAPTABILITY
FILTER
ARITHMETIC
DAC
ACO
LOGICAL ARITHMETIC
57
CA
58
C9
59
C8
60
C7
61
C6
62
C5
63
C4
64
C3
65
C2
66
C1
10