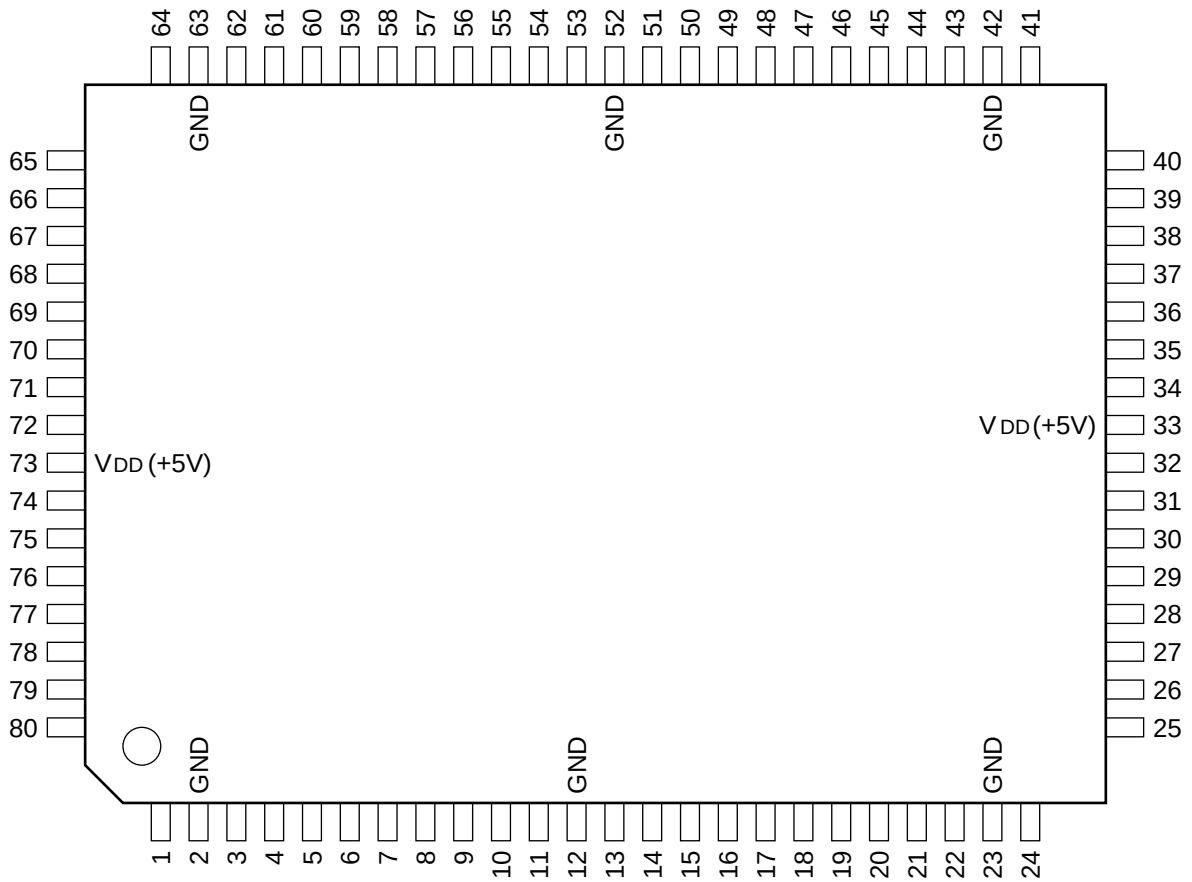


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# C-MOS MOTION DETECTOR FOR DIGITAL FRAME MEMORY TV

- TOP VIEW -



(V<sub>DD</sub> = +5V)

| PIN No. | I/O | SIGNAL | PIN No. | I/O | SIGNAL          | PIN No. | I/O | SIGNAL | PIN No. | I/O | SIGNAL          |
|---------|-----|--------|---------|-----|-----------------|---------|-----|--------|---------|-----|-----------------|
| 1       | I   | DIC1   | 21      | I   | DID1            | 41      | I   | DIE0   | 61      | O   | VCR1            |
| 2       | —   | GND    | 22      | I   | DID0            | 42      | —   | GND    | 62      | O   | VCR2            |
| 3       | I   | DIC0   | 23      | —   | GND             | 43      | I   | TST8   | 63      | —   | GND             |
| 4       | I   | DIB7   | 24      | I   | DIA7            | 44      | I   | TST1   | 64      | O   | C0              |
| 5       | I   | DIB6   | 25      | I   | DIA6            | 45      | I   | TST2   | 65      | O   | C1              |
| 6       | I   | DIB5   | 26      | I   | DIA5            | 46      | O   | X2HS   | 66      | O   | C2              |
| 7       | I   | DIB4   | 27      | I   | DIA4            | 47      | I   | TST3   | 67      | O   | C3              |
| 8       | I   | DIB3   | 28      | I   | DIA3            | 48      | O   | XHS    | 68      | O   | C4              |
| 9       | I   | DIB2   | 29      | I   | DIA2            | 49      | O   | HD2    | 69      | O   | VEC             |
| 10      | I   | DIB1   | 30      | I   | DIA1            | 50      | O   | HD1    | 70      | I   | TST4            |
| 11      | I   | DIB0   | 31      | I   | DIA0            | 51      | O   | HD0    | 71      | I   | FSCK            |
| 12      | —   | GND    | 32      | I   | TST7            | 52      | —   | GND    | 72      | I   | POR             |
| 13      | I   | TST5   | 33      | —   | V <sub>DD</sub> | 53      | O   | SCK    | 73      | —   | V <sub>DD</sub> |
| 14      | I   | TST6   | 34      | I   | DIE7            | 54      | O   | ADRB   | 74      | I   | INEX            |
| 15      | I   | DID7   | 35      | I   | DIE6            | 55      | O   | MD     | 75      | I   | DIC7            |
| 16      | I   | DID6   | 36      | I   | DIE5            | 56      | I   | DFR1   | 76      | I   | DIC6            |
| 17      | I   | DID5   | 37      | I   | DIE4            | 57      | I   | DFR2   | 77      | I   | DIC5            |
| 18      | I   | DID4   | 38      | I   | DIE3            | 58      | O   | XRRS   | 78      | I   | DIC4            |
| 19      | I   | DID3   | 39      | I   | DIE2            | 59      | O   | XWRS   | 79      | I   | DIC3            |
| 20      | I   | DID2   | 40      | I   | DIE1            | 60      | O   | VCR0   | 80      | I   | DIC2            |

## INPUT

DFR1 ; 1 FRAME DELAYED SIGNAL FROM MOTION DETECT SIGNAL  
DFR2 ; 2 FRAMES DELAYED SIGNAL FROM MOTION DETECT SIGNAL  
DIA0-DIA7 ; VIDEO ORIGINAL SIGNAL  
DIB0-DIB7 ; 262H DELAYED SIGNAL FROM VIDEO ORIGINAL SIGNAL  
DIC0-DIC7 ; 263H DELAYED SIGNAL FROM VIDEO ORIGINAL SIGNAL  
DID0-DID7 ; 525H DELAYED SIGNAL FROM VIDEO ORIGINAL SIGNAL  
DIE0-DIE7 ; 787H DELAYED SIGNAL FROM VIDEO ORIGINAL SIGNAL  
FSCK ; 28.6MHz CLOCK  
INEX ; SAMPLING MODE SELECT OF COLOR DIFFERENCE SIGNAL  
(H : 4:1:1, L : 4:2:2)  
POR ; TEST (NORMAL : H)  
TST1-TST4 ; TEST (NORMAL : L)  
TST5-TST8 ; TEST (NORMAL : H)

## OUTPUT

ADRB ; SELECT SIGNAL FOR INPUT COLOR DIFFERENCE SIGNAL  
C0-C4 ; MOTION COMPENSATION CONTROL SIGNAL  
HD0 ; H CYCLE SIGNAL (DUTY 1:1)  
HD1 ; 1.015uS ADVANCED SIGNAL FROM XHS  
HD2 ; 175nS ADVANCED SIGNAL FROM XHS  
MD ; MOTION DETECT SIGNAL  
SCK ; 14.3MHz CLOCK  
VCR0 ; WRITE CONTROL SIGNAL FOR FIELD MEMORY  
VCR1 ; READ CONTROL SIGNAL FOR FIELD MEMORY  
VCR2 ; READ CONTROL SIGNAL FOR FIELD MEMORY  
VEC ; FILTER ON/OFF CONTROL SIGNAL AT VERTICAL EDGE  
X2HS ; DOUBLE-SPEED HORIZONTAL SYNCHRONIZING SIGNAL  
XHS ; HORIZONTAL SYNCHRONIZING SIGNAL  
XRRS ; READ CONTROL SIGNAL FOR LINE MEMORY  
XWRS ; WRITE CONTROL SIGNAL FOR LINE MEMORY

