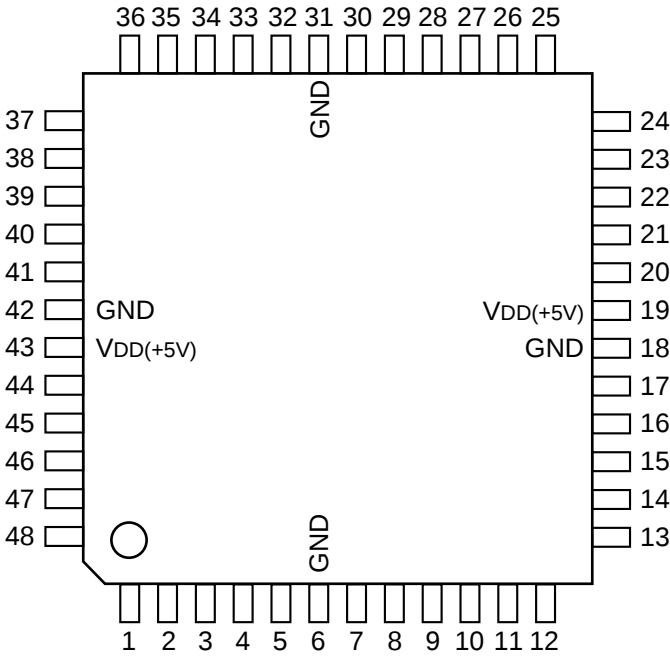

C-MOS VIDEO BUFFER MEMORY
—TOP VIEW—



1	DI0	DO0	36
2	DI1	DO1	35
3	DI2	DO2	34
4	DI3	DO3	33
5	DI4	DO4	32
7	DI5	DO5	30
8	DI6	DO6	29
9	DI7	DO7	28
10	DI8	DO8	27
11	DI9	DO9	26
37	A0		
38	A1		
39	4FA2		
40	INA3		
13	TES1	TES2	48
20	RCK	ADNG	12
21	RSYN	WZ	14
22	RSC	ADV	15
		IN	16
24	OE	ADOK	17
25	TSG	CP	23
41	TBCD		
47	SCCO		
44	WCK		
45	WSYN		
46	WSC		

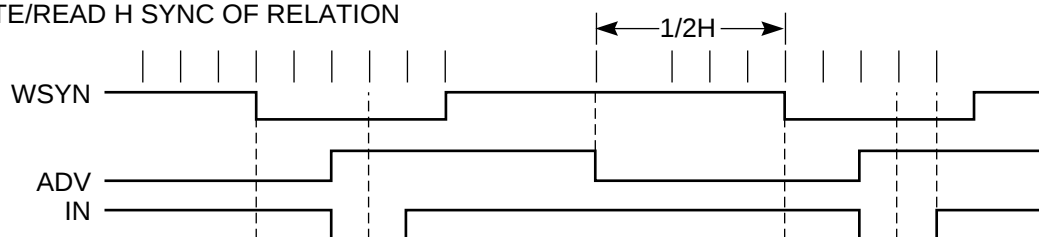
PIN NO.	IN	OUT	SYMBOL	PIN NO.	IN	OUT	SYMBOL	PIN NO.	IN	OUT	SYMBOL	PIN NO.	IN	OUT	SYMBOL
1	O		DI0	13	O		TES1	25	O		TSG	37	O		A0
2	O		DI1	14		O	WZ	26		O	DO9	38	O		A1
3	O		DI2	15		O	ADV	27		O	DO8	39	O		4FA2
4	O		DI3	16		O	IN	28		O	DO7	40	O		INA3
5	O		DI4	17		O	ADOK	29		O	DO6	41	O		TBCD
6			GND	18			GND	30		O	DO5	42			GND
7	O		DI5	19			VDD(+5V)	31			GND	43			VDD(+5V)
8	O		DI6	20	O		RCK	32		O	DO4	44	O		WCK
9	O		DI7	21	O		RSYN	33		O	DO3	45	O		WSYN
10	O		DI8	22	O		RSC	34		O	DO2	46	O		WSC
11	O		DI9	23		O	CP	35		O	DO1	47	O		SCCO
12		O	ADNG	24	O		OE	36		O	DO0	48		O	TES2

INPUT

DI0-DI9	; DATA INPUT
TES	; TEST
RCK	; READ CLOCK INPUT (REFERENCE)
RSYN	; READ SYNC INPUT (REFERENCE)
RSC	; READ SUB CARRIER INPUT (REFERENCE)
OE	; ENABLE OUTPUT (L; OUTPUT/H; HIGH IMPEDANCE)
TSG	; TSG MODE APPOINT (L; TSG/H; TBC OR DL MODE)
A0	; DL MODE OF DELAY TIME APPOINT
A1	; DL MODE: DELAY TIME APPOINT/TBC MODE: 2fH GATE ON, OFF APPOINT (H; ON/L; OFF)
4FA2	; DL MODE: DELAY TIME APPOINT/TBC MODE: INPUT PULSE FORMAT APPOINT (H; 4Fsc/L; 4:2:2)
INA3	; DL MODE: DELAY TIME APPOINT/TBC MODE: H SYNC DETECT (H; INT/L; EXT)
TBCD	; TBC/DL MODE SELECT (H; TBC/L; DL) (TSG LOW; TSG MODE)
WCK	; WRITE CLOCK INPUT
WSYN	; EXT MODE OF WRITE H SYNC INPUT (INT MODE; NC)
WSC	; EXT MODE OF SUB CARRIER INPUT
SCCO	; 4Fsc MODE SC OF PHASE ADJUST ONLY: L

OUTPUT

DO0-DO9	; DATA OUTPUT
ADNG	; TBC UNSTABILIZED; L (READ WRITE 7+1 CLOCK)
WZ	; VIDEO DATA OUTPUT OF ABSOLUTE PHASE
ADV, IN	; WRITE/READ H SYNC OF RELATION



ADOK; TBC STABILIZED; L (READ WRITE 3+1 CLOCK)

CP;

TBCD	4FA2	SCCO	INA3	CP OUTPUT
0	X	0	X	INT13 SYNC+EXT SYNC
0	X	1	X	INT14 SYNC+EXT SYNC
1	0	X	1	INT13 SYNC+EXT SYNC
1	1	X	1	INT14 SYNC EXT SYNC
1	X	X	0	EXT SYNC

0 ; LOW LEVEL

1 ; HIGH LEVEL

X ; DON'T CARE

INT13 SYNC; D2-D9 DETECTED 13.5M FORMAT SYNC

INT14 SYNC; D2-D9 DETECTED 14.3M FORMAT SYNC

EXT SYNC; INPUT SYNC FROM WSYN (45PIN)

