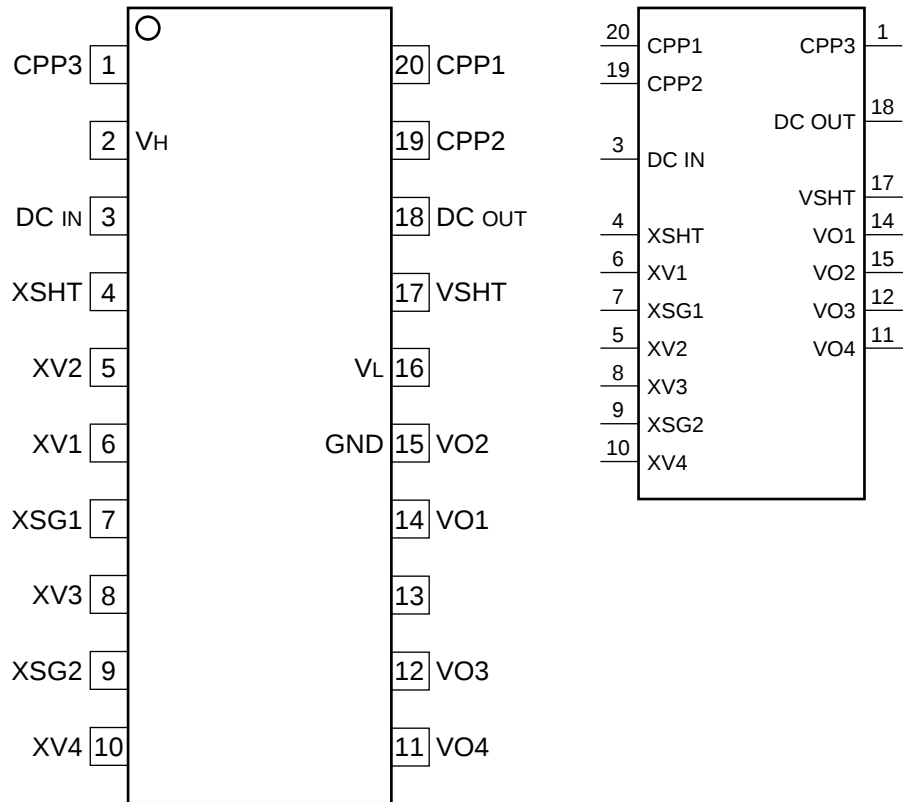


C-MOS VERTICAL CLOCK DRIVER FOR CCD

—TOP VIEW—

**INPUT**

DCIN : OPERATIONAL AMPLIFIRE
 XSG1, XSG2 : SENSOR GATE PULSE
 XSHT : SAMPLE AND HOLD PULSE
 XV1 - XV4 : VERTICAL REGISTER TRANSMISSION CLOCK

OUTPUT

CPP3 : CHARGE PUMP
 DC OUT : OPERATIONAL AMPLIFIRE
 VSHT : SAMPLE AND HOLD PULSE
 VO1 - VO4 : VERTICAL REGISTER TRANSMISSION CLOCK

INPUT				OUTPUT		
XV1, 3	XSG1, 2	XV2, 4	XSHT	VO1, 3	VO2, 4	VSHT
0	0	x	x		x	x
1	0	x	x	HI-Z	x	x
0	1	x	x	GND	x	x
1	1	x	x		x	x
x	x	0	x	x	GND	x
x	x	1	x	x		x
x	x	x	0	x	x	
x	x	x	1	x	x	

0 : LOW LEVEL
1 : HIGH LEVEL
x : DON'T CARE
HI-Z : HIGH IMPEDANCE

