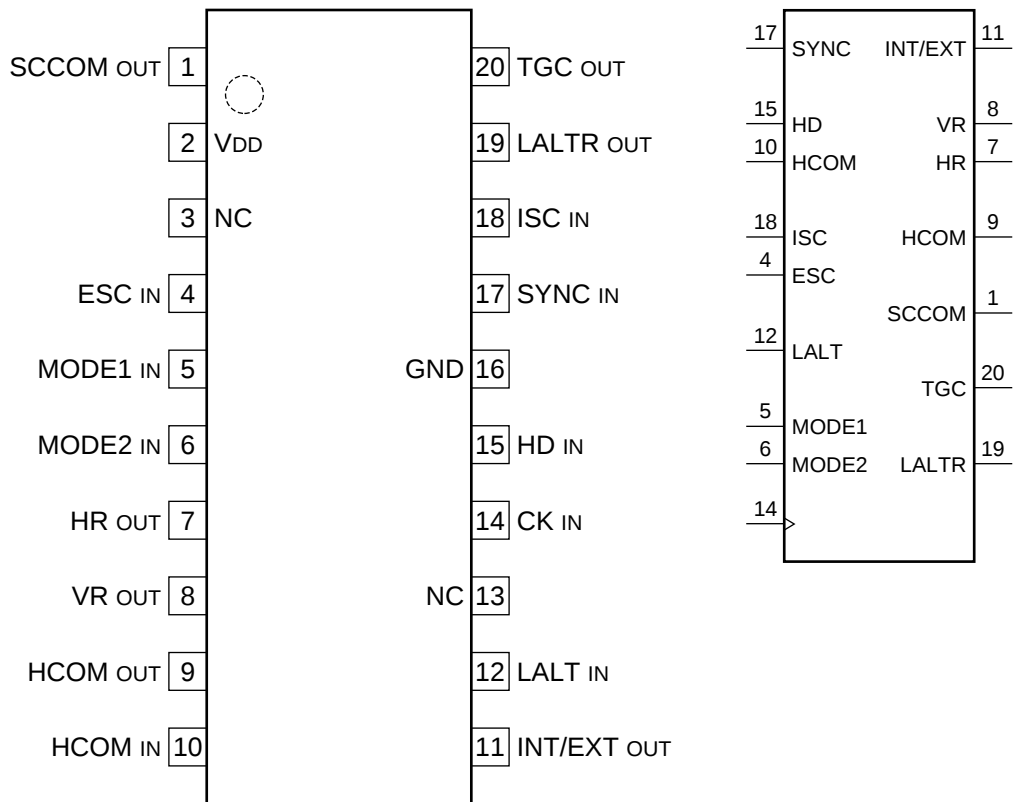


C-MOS GENLOCK DRIVER

—TOP VIEW—



INPUT		MODE	SYSTEM
MODE1	MODE2		
0	0	M1	PAL-VBS
1	0	M2	PALM-VBS
0	1	M3	PAL,SECAM-VS/SC/LALT
1	1	M4	NTSC-VBS,NTSC-VS/SC PALM-VS/SC/LALT

0 : LOW LEVEL
1 : HIGH LEVEL

INPUT

CK : 4fsc CLOCK INPUT
 ESC : SC/COLOR BURST
 HCOM : PHASE COMPARE FROM CXD1217
 HD : H DRIVE FROM CXD1217
 ISC : SUBCARRIER FROM CXD1217
 LALT : LALT FROM REFERENCE SIGNAL GENERATOR
 MODE1, 2 : SYSTEM SELECT
 SYNC : SYNC FROM REFERENCE SIGNAL GENERATOR

OUTPUT

HCOM : PHASE COMPARATOR HR WITH HD
 HR : fH OF SYNC SEPARATE
 INT/EXT : INTERNAL/EXTERNAL SPECIFIED
 LALTR : LINE CHANGE RESET
 SCCOM : PHASE COMPARATOR ESC WITH ISC
 TGC : TRISTATE CONTROL
 VR : fV OF SYNC SEPARATE

