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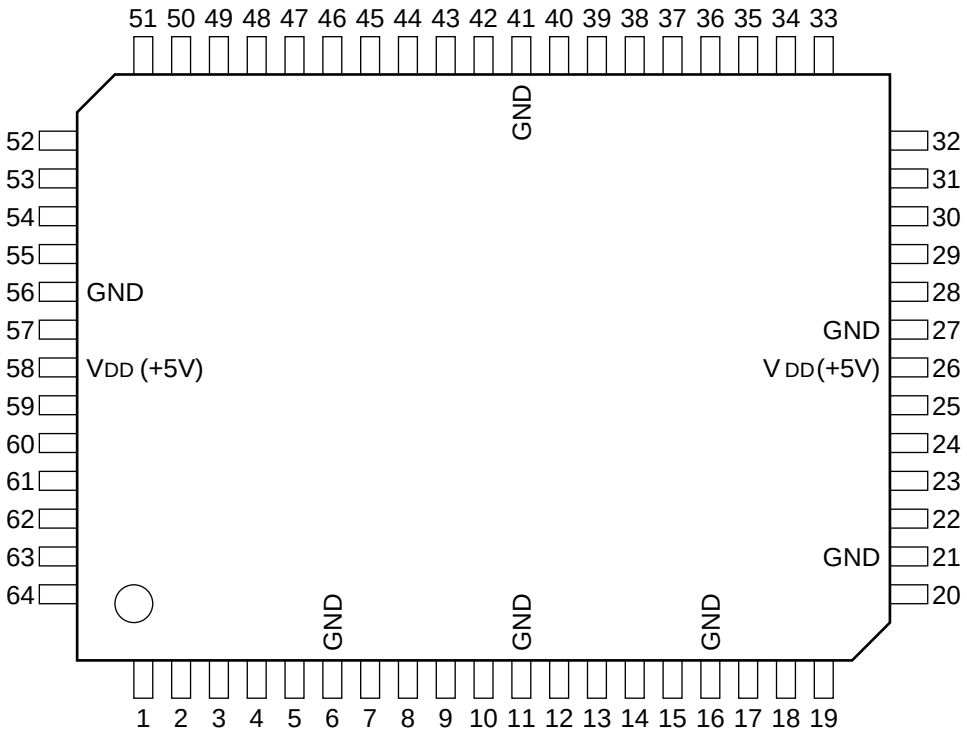
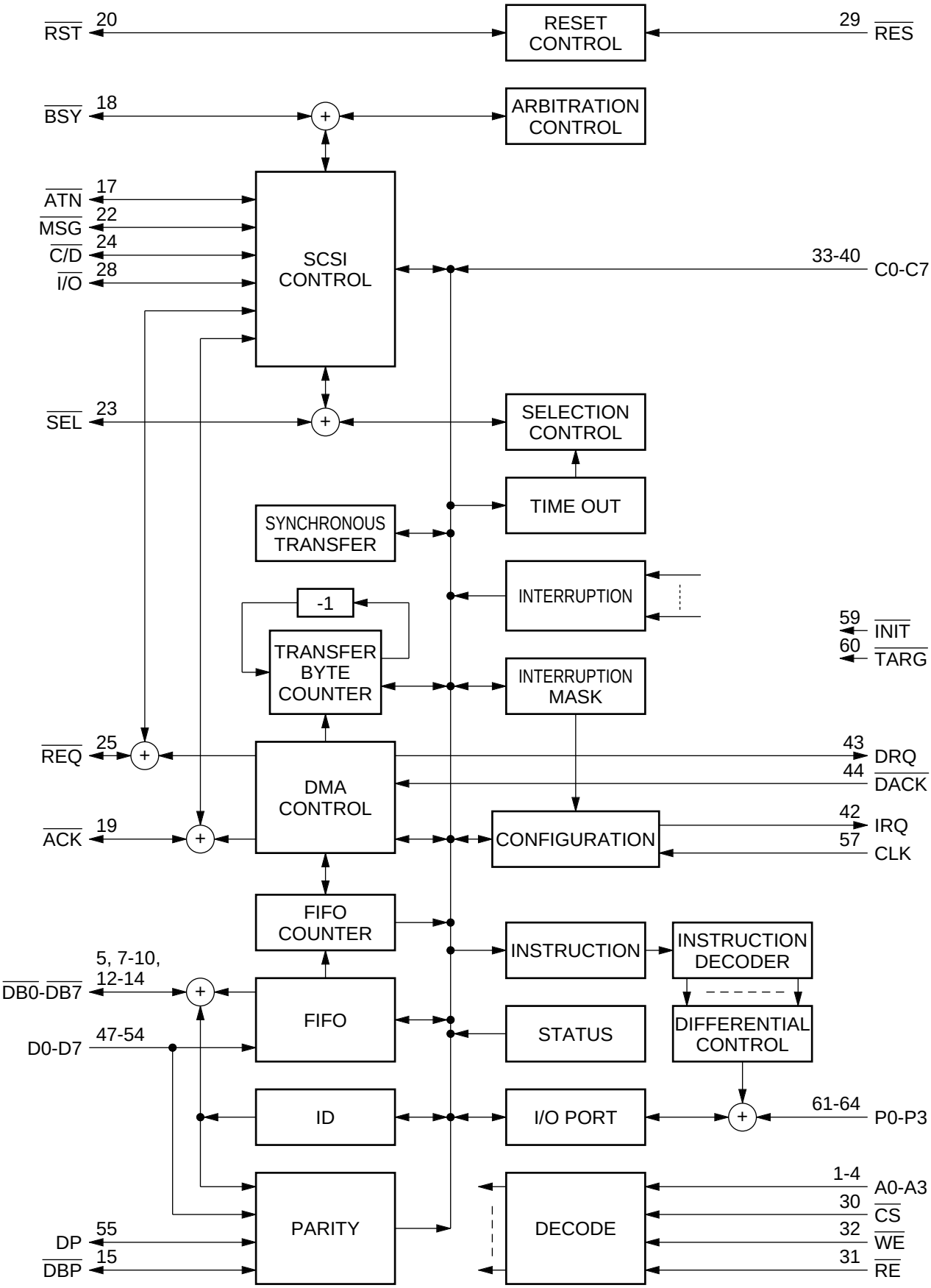
C-MOS SCSI CONTROLLER

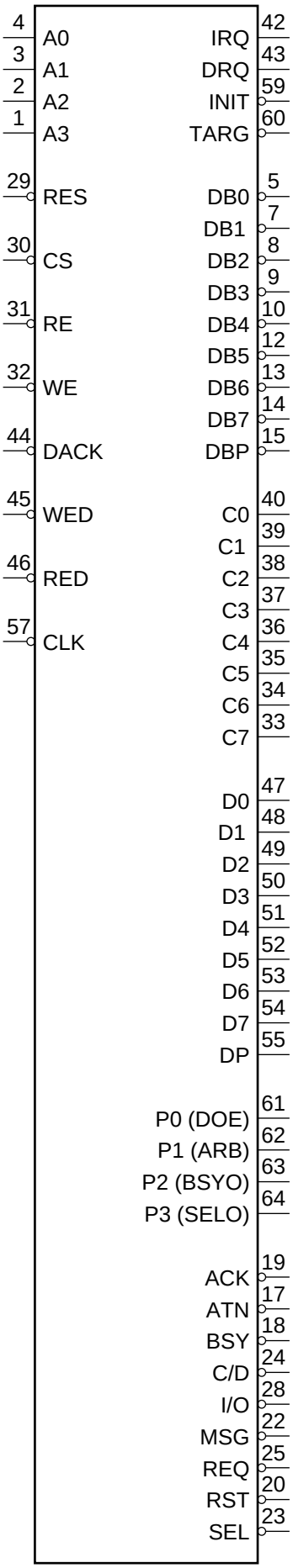
- TOP VIEW -



(V DD =+5V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	A3	17	I/O	$\overline{\text{ATN}}$	33	I/O	C7	49	I/O	D2
2	I	A2	18	I/O	$\overline{\text{BSY}}$	34	I/O	C6	50	I/O	D3
3	I	A1	19	I/O	$\overline{\text{ACK}}$	35	I/O	C5	51	I/O	D4
4	I	A0	20	I/O	$\overline{\text{RST}}$	36	I/O	C4	52	I/O	D5
5	I/O	$\overline{\text{DB0}}$	21	–	GND	37	I/O	C3	53	I/O	D6
6	–	GND	22	I/O	$\overline{\text{MSG}}$	38	I/O	C2	54	I/O	D7
7	I/O	$\overline{\text{DB1}}$	23	I/O	$\overline{\text{SEL}}$	39	I/O	C1	55	I/O	DP
8	I/O	$\overline{\text{DB2}}$	24	I/O	$\overline{\text{C/D}}$	40	I/O	C0	56	–	GND
9	I/O	$\overline{\text{DB3}}$	25	I/O	$\overline{\text{REQ}}$	41	–	GND	57	I	CLK
10	I/O	$\overline{\text{DB4}}$	26	–	VDD	42	O	IRQ	58	–	VDD
11	–	GND	27	–	GND	43	O	DRQ	59	O	$\overline{\text{INIT}}$
12	I/O	$\overline{\text{DB5}}$	28	I/O	$\overline{\text{I/O}}$	44	I	$\overline{\text{DACK}}$	60	O	$\overline{\text{TARG}}$
13	I/O	$\overline{\text{DB6}}$	29	I	$\overline{\text{RES}}$	45	I	$\overline{\text{WED}}$	61	I/O	P0 (DOE)
14	I/O	$\overline{\text{DB7}}$	30	I	$\overline{\text{CS}}$	46	I	$\overline{\text{RED}}$	62	I/O	P1 (ARB)
15	I/O	$\overline{\text{DBP}}$	31	I	$\overline{\text{RE}}$	47	I/O	D0	63	I/O	P2 (BSYO)
16	–	GND	32	I	$\overline{\text{WE}}$	48	I/O	D1	64	I/O	P3 (SELO)


RST
20
RESET CONTROL
29
RES
BSY
18
ARBITRATION CONTROL
ATN
17
MSG
22
C/D
24
I/O
28
SCSI CONTROL
33-40
C0-C7
SEL
23
SELECTION CONTROL
TIME OUT
SYNCHRONOUS TRANSFER
INTERRUPTION
-1
TRANSFER BYTE COUNTER
INTERRUPTION MASK
59
INIT
60
TARG
REQ
25
DMA CONTROL
43
DRQ
44
DACK
ACK
19
CONFIGURATION
42
IRQ
57
CLK
FIFO COUNTER
INSTRUCTION
INSTRUCTION DECODER
5, 7-10, 12-14
DB0-DB7
FIFO
STATUS
DIFFERENTIAL CONTROL
D0-D7
47-54
ID
I/O PORT
61-64
P0-P3
PARITY
DECODE
1-4
A0-A3
30
CS
32
WE
31
RE
DP
55
DBP
15



INPUT

A0-A3 ; RESISTOR SELECT SIGNAL
CLK ; CLOCK
$\overline{CS}$; SHIP SELECT SIGNAL
$\overline{DACK}$; ACKNOWLEDGE SIGNAL
$\overline{RE}$; READ ENABLE
$\overline{RED}$; READ ENABLE (DATA BUS)
$\overline{RES}$; RESET
$\overline{WE}$; WRITE ENABLE
$\overline{WED}$; WRITE ENABLE (DATA BUS)

OUTPUT

DRQ ; DMA SIGNAL
$\overline{INIT}$; SELECT SIGNAL
IRQ ; INTERRUPTION
$\overline{TARG}$; TARGET SELECT SIGNAL

INPUT/OUTPUT

$\overline{ACK}$; ACK SIGNAL (SCSI BUS)
ARB ; EFFECTIVE
$\overline{ATN}$; ATN SIGNAL (SCSI BUS)
$\overline{BSY}$; BSY SIGNAL (SCSI BUS)
BSYO ; SCSI BSY OUT
C0-C7 ; CPU BUS
$\overline{C/D}$; C/D SIGNAL (SCSI BUS)
D0-D7 ; DATA BUS
$\overline{DB0-DB7}$; DB0-DB7 SIGNAL (SCSI BUS)
$\overline{DBP}$; DBP SIGNAL (SCSI BUS)
DOE ; SCSI DATA OUTPUT
DP ; PARITY SIGNAL
$\overline{I/O}$; I/O SIGNAL (SCSI BUS)
$\overline{MSG}$; MSG SIGNAL (SCSI BUS)
P0-P3 ; PORT0-PORT3 SIGNAL
$\overline{REQ}$; REQ SIGNAL (SCSI BUS)
$\overline{RST}$; RST SIGNAL (SCSI BUS)
$\overline{SEL}$; SEL SIGNAL (SCSI BUS)
SELO ; SCSI SEL OUT