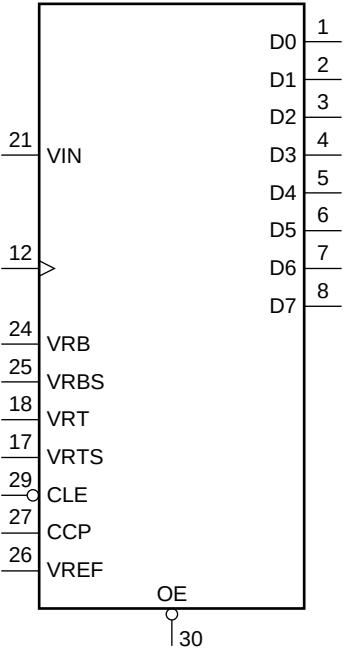
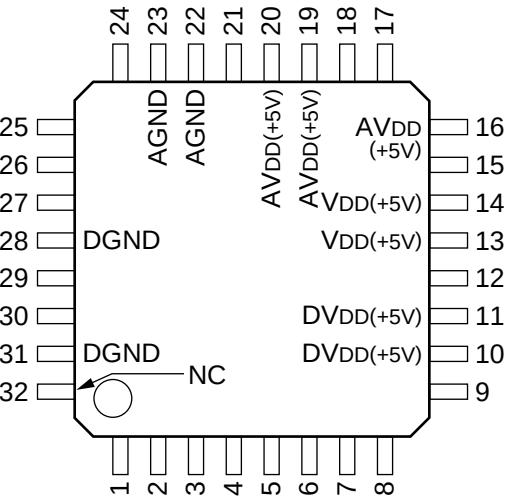

C-MOS VIDEO A/D CONVERTER
-TOP VIEW-



(AVDD, DVDD, VDD=+5V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	O	D0 (LSB)	17	I	VRTS
2	O	D1	18	I	VRT
3	O	D2	19	—	AVDD
4	O	D3	20	—	AVDD
5	O	D4	21	I	VIN
6	O	D5	22	—	AGND
7	O	D6	23	—	AGND
8	O	D7	24	I	VRB
9	—	TEST(OPEN)	25	I	VRBS
10	—	DVDD	26	I	VREF
11	—	DVDD	27	I	CCP
12	I	CLK	28	—	DGND
13	—	VDD	29	I	CLE
14	—	VDD	30	I	OE
15	I	CLP	31	—	DGND
16	—	AVDD	32	—	NC

INPUT

CCP ; CLAMP CONTROL PULSE
 CLE ; CLAMP ENABLE
 CLK ; CLOCK
 CLP ; CLAMP PULSE
 OE ; OUTPUT ENABLE
 VIN ; ANALOG VOLTAGE
 VRB ; BOTTOM REFERENCE VOLTAGE
 VRBS ; BOTTOM REFERENCE VOLTAGE SHORT (+0.5 V)
 VREF ; VOLTAGE REFERENCE
 VRT ; TOP REFERENCE VOLTAGE
 VRTS ; TOP REFERENCE VOLTAGE SHORT (+2.6 V)

OUTPUT

D0-D7 ; DATA

