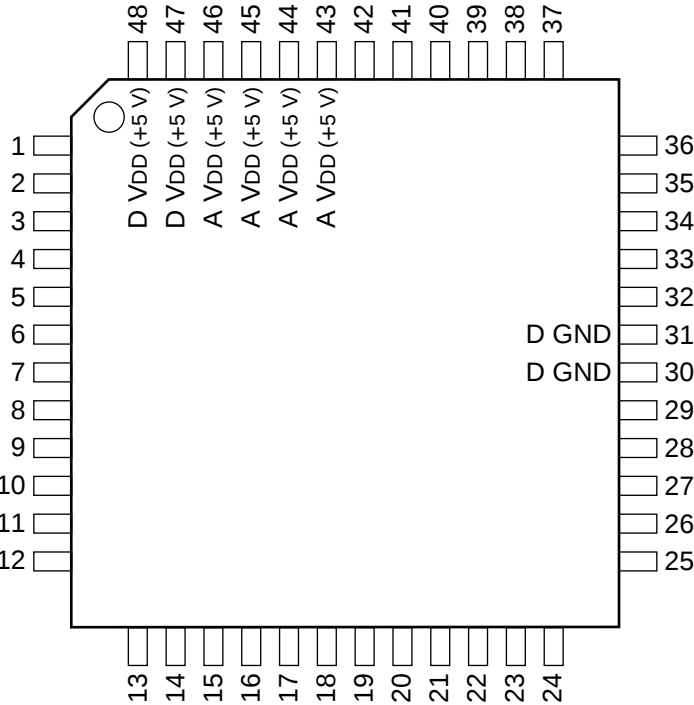

C-MOS 3-CH 8-BIT D/A CONVERTER
—TOP VIEW—



(A VDD, D VDD = +5 V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	R0 (LSB)	13	I	G4	25	I	BLK	37	O	RO
2	I	R1	14	I	G5	26	I	$\overline{\text{CE}}$	38	O	$\overline{\text{GO}}$
3	I	R2	15	I	G6	27	I	RCK	39	O	GO
4	I	R3	16	I	G7	28	I	GCK	40	O	$\overline{\text{BO}}$
5	I	R4	17	I	B0 (LSB)	29	I	BCK	41	O	BO
6	I	R5	18	I	B1	30	—	D GND	42	I	VG
7	I	R6	19	I	B2	31	—	D GND	43	—	A VDD
8	I	R7	20	I	B3	32	I	VB	44	—	A VDD
9	I	G0 (LSB)	21	I	B4	33	—	A GND	45	—	A VDD
10	I	G1	22	I	B5	34	I	VREF	46	—	A VDD
11	I	G2	23	I	B6	35	I	IREF	47	—	D VDD
12	I	G3	24	I	B7	36	O	RO	48	—	D VDD

