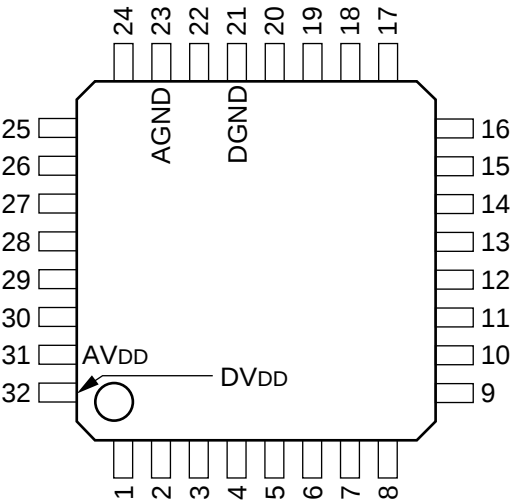
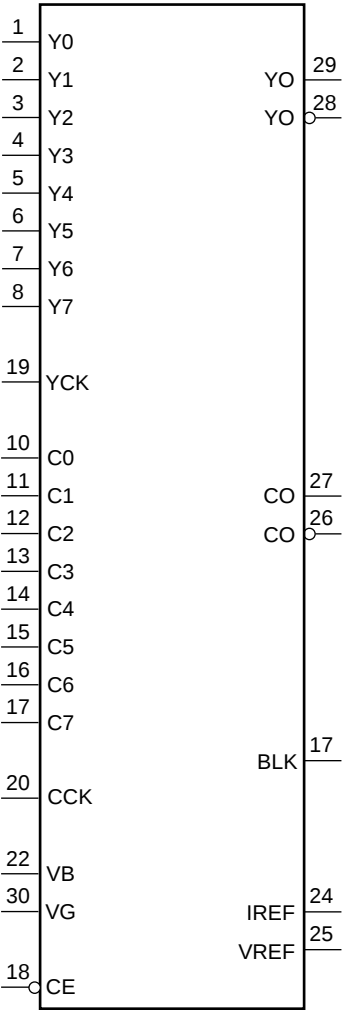


C-MOS Y/C 2 CHANNEL D/A CONVERTER

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	Y0	17	O	BLK
2	I	Y1	18	I	$\overline{\text{CE}}$
3	I	Y2	19	I	YCK
4	I	Y3	20	I	CCK
5	I	Y4	21	—	DGND
6	I	Y5	22	I	VB
7	I	Y6	23	—	AGND
8	I	Y7	24	O	IREF
9	I	C0	25	O	VREF
10	I	C1	26	O	$\overline{\text{CO}}$
11	I	C2	27	O	CO
12	I	C3	28	O	$\overline{\text{YO}}$
13	I	C4	29	O	YO
14	I	C5	30	I	VG
15	I	C6	31	—	AVDD
16	I	C7	32	—	DVDD



INPUT

C0 - C7 ; C DATA
 CCK ; CHROMA CLOCK
 $\overline{\text{CE}}$; CHIP ENABLE
 VB ; BIAS VOLTAGE
 VG ; GND VOLTAGE
 Y0 - Y7 ; Y DATA
 YCK ; LUMINANCE CLOCK

OUTPUT

BLK ; BLANKING PULSE
 CO, $\overline{\text{CO}}$; C DATA
 IREF ; CURRENT REFERENCE
 VREF ; VOLTAGE REFERENCE
 YO, $\overline{\text{YO}}$; Y DATA

