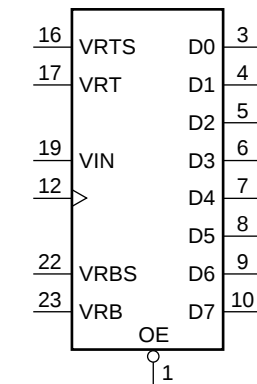
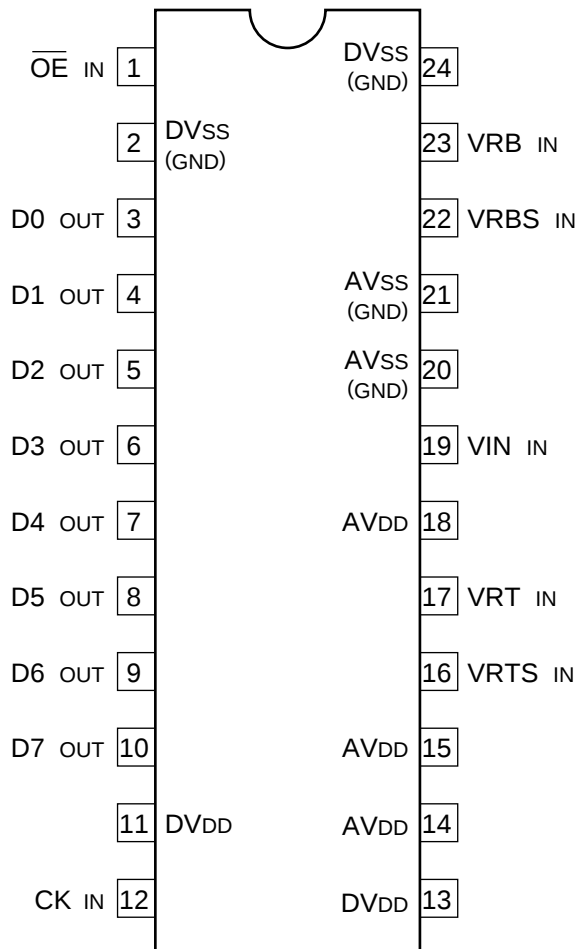


C-MOS 8-BIT 20 MSPS VIDEO A / D CONVERTER

—TOP VIEW—



AVDD ; ANALOG POWER SUPPLY
 AVSS ; ANALOG GROUND
 CK ; CLOCK INPUT
 D0 - 7 ; DIGITAL DATA OUTPUTS (LSB to MSB)
 DVDD ; DIGITAL POWER SUPPLY
 DVSS ; DIGITAL GROUND
 OE ; OUTPUT ENABLE
 (OE = HIGH ; HIGH IMPEDANCE)
 VIN ; ANALOG VOLTAGE INPUT
 VRB ; BOTTOM REFERENCE VOLTAGE INPUT
 VRBS ; AS SHORT-CIRCUIT AT VRB.
 GENERATES +0.6 V
 VRT ; TOP REFERENCE VOLTAGE INPUT
 VRTS ; AS SHORT-CIRCUIT AT VRT.
 GENERATES +2.6 V

STEP	INPUT SIGNAL VOLTAGE	DATA OUTPUTS							
		D7	D6	D5	D4	D3	D2	D1	D0
0	0 V (VRT)	1	1	1	1	1	1	1	1
1	0.01 V	1	1	1	1	1	1	1	0
⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
127	1.34 V	1	0	0	0	0	0	0	0
128	1.35 V	0	1	1	1	1	1	1	1
⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
255	2.7 V (VRB)	0	0	0	0	0	0	0	0

0 ; LOW LEVEL

1 ; HIGH LEVEL

