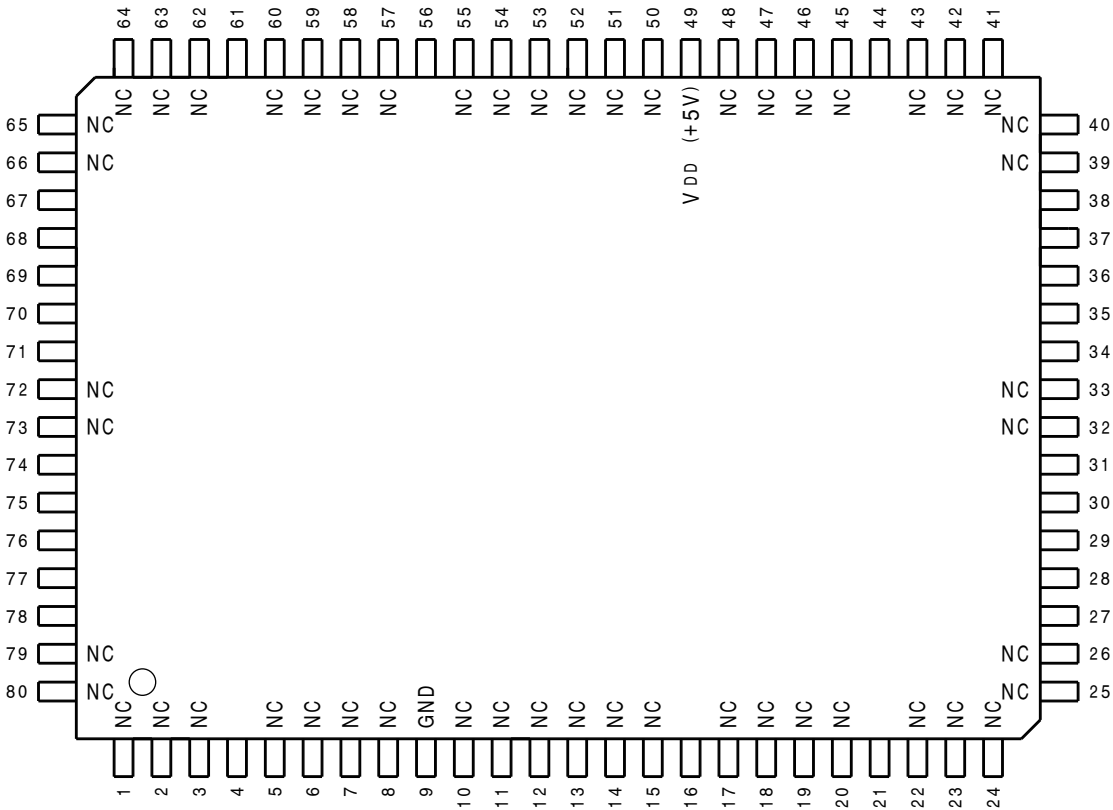

C-MOS DIGITAL AUDIO SIGNAL PROCESSOR
-TOP VIEW-



PIN NO.	I/O	SYMBOL	PIN NO.	I/O	SYMBOL	PIN NO.	I/O	SYMBOL	PIN NO.	I/O	SYMBOL
1	-	NC	21	I	MCK2	41	-	NC	61	O	A2
2	-	NC	22	-	NC	42	-	NC	62	-	NC
3	-	NC	23	-	NC	43	-	NC	63	-	NC
4	I	TST	24	-	NC	44	I	XCLR	64	-	NC
5	-	NC	25	-	NC	45	-	NC	65	-	NC
6	-	NC	26	-	NC	46	-	NC	66	-	NC
7	-	NC	27	I	SI	47	-	NC	67	O	A0
8	-	NC	28	O	SO	48	-	NC	68	O	XRAS
9	-	GND	29	I	BCK	49	-	V DD (+5V)	69	O	XWSO
10	-	NC	30	I	LRCK	50	-	NC	70	I/O	DIO
11	-	NC	31	O	XOVF	51	-	NC	71	O	XCAS
12	-	NC	32	-	NC	52	-	NC	72	-	NC
13	-	NC	33	-	NC	53	-	NC	73	-	NC
14	-	NC	34	O	A6	54	-	NC	74	I	SDT
15	-	NC	35	O	A3	55	-	NC	75	I	SCK
16	I	MCK1	36	O	A4	56	O	A1	76	I	XSLD
17	-	NC	37	O	A5	57	-	NC	77	I	SI O2
18	-	NC	38	O	A7	58	-	NC	78	I	DYSL
19	-	NC	39	-	NC	59	-	NC	79	-	NC
20	-	NC	40	-	NC	60	-	NC	80	-	NC

INPUT

BCK ; SERIAL BIT CLOCK
 FOR SERIAL INPUT DATA
 (SI) AND SERIAL OUTPUT
 DATA (SO)
 DYSL ; DELAY I/O MODE SELECT
 LRCK ; SAMPLING FREQ. CLOCK
 MCK1 ; MASTER CLOCK 1
 MCK2 ; MASTER CLOCK 2
 SCK ; SERIAL CLOCK FOR SDT
 SDT ; SERIAL DATA FROM CPU
 SI ; SERIAL DATA (2's COMPLEMENT)
 SIO2 ; BCK CLOCK MODE SELECT
 (L (GND) : 32 BIT CLOCK MODE,
 H (+5V) : 24 BIT CLOCK MODE)
 TST ; FOR TEST
 XCLR ; FOR TEST
 XSLD ; LATCH FOR SDT (SERIAL DATA)
 ACTIVE LOW

OUTPUT

A0-A7 ; ADDRESS FOR EXT. DRAM
 SO ; SERIAL DATA (2's COMPLEMENT)
 XCAS ; COLUMN ADDRESS STROBE FOR
 EXT. DRAM
 XOVF ; OVERFLOW DETECT
 XRAS ; ROW ADDRESS STROBE FOR
 EXT. DRAM

INPUT/OUTPUT

DIO ; SERIAL DATA INPUT (DYSL=L) OR
 DATA INPUT/OUTPUT FOR
 EXT. DRAM (DYSL=H)

