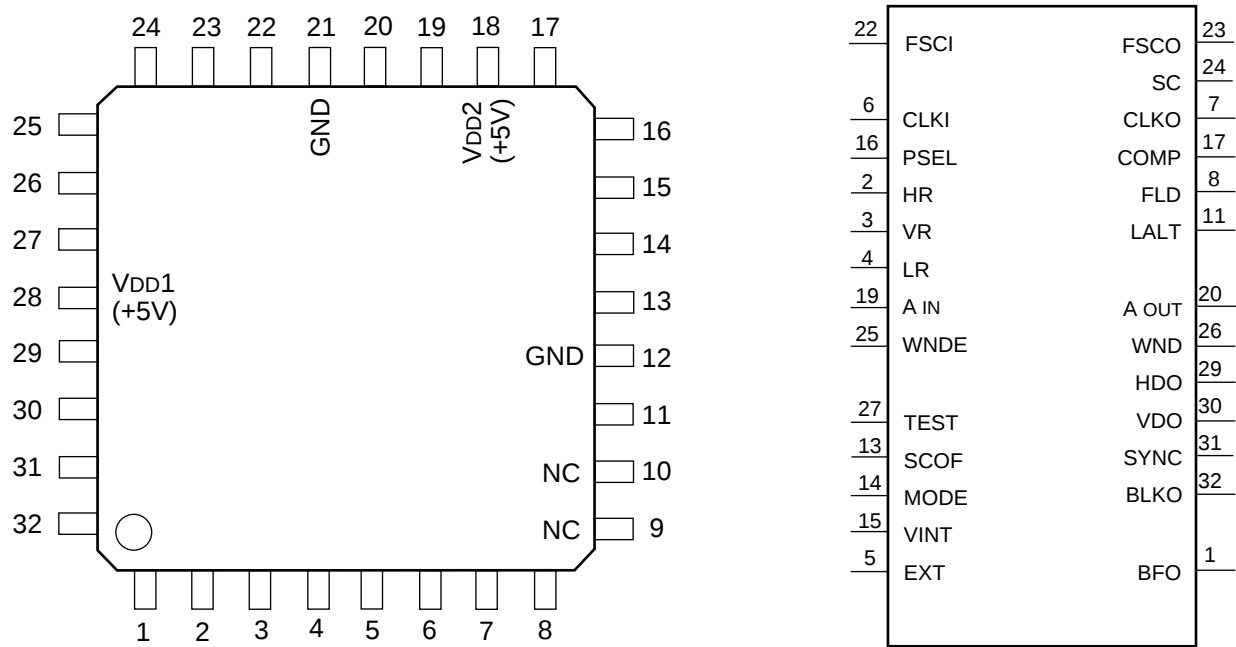
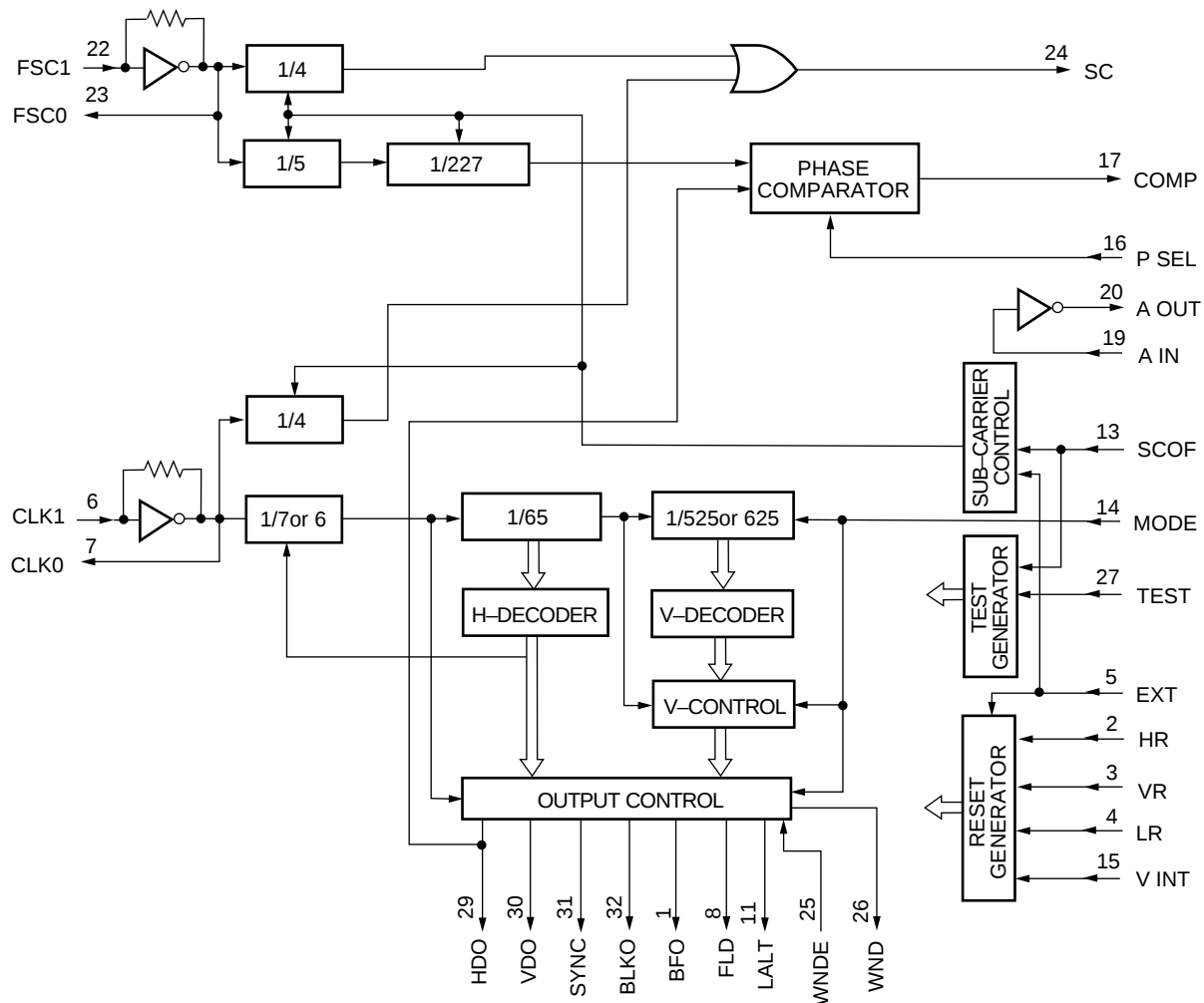

C-MOS SYNC GENERATOR

-TOP VIEW-



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	BFO	9	—	NC	17	O	COMP	25	I	WNDE
2	I	HR	10	—	NC	18	—	VDD2(+5V)	26	O	WND
3	I	VR	11	O	LALT	19	I	AIN	27	I	TEST
4	I	LR	12	—	GND	20	O	AOUT	28	—	VDD1(+5V)
5	I	EXT	13	I	SCOF	21	—	GND	29	O	HDO
6	I	CLKI	14	I	MODE	22	I	FSCI	30	O	VDO
7	O	CLKO	15	I	VINT	23	O	FSCO	31	O	SYNC
8	O	FLD	16	I	PSEL	24	O	SC	32	O	BLKO

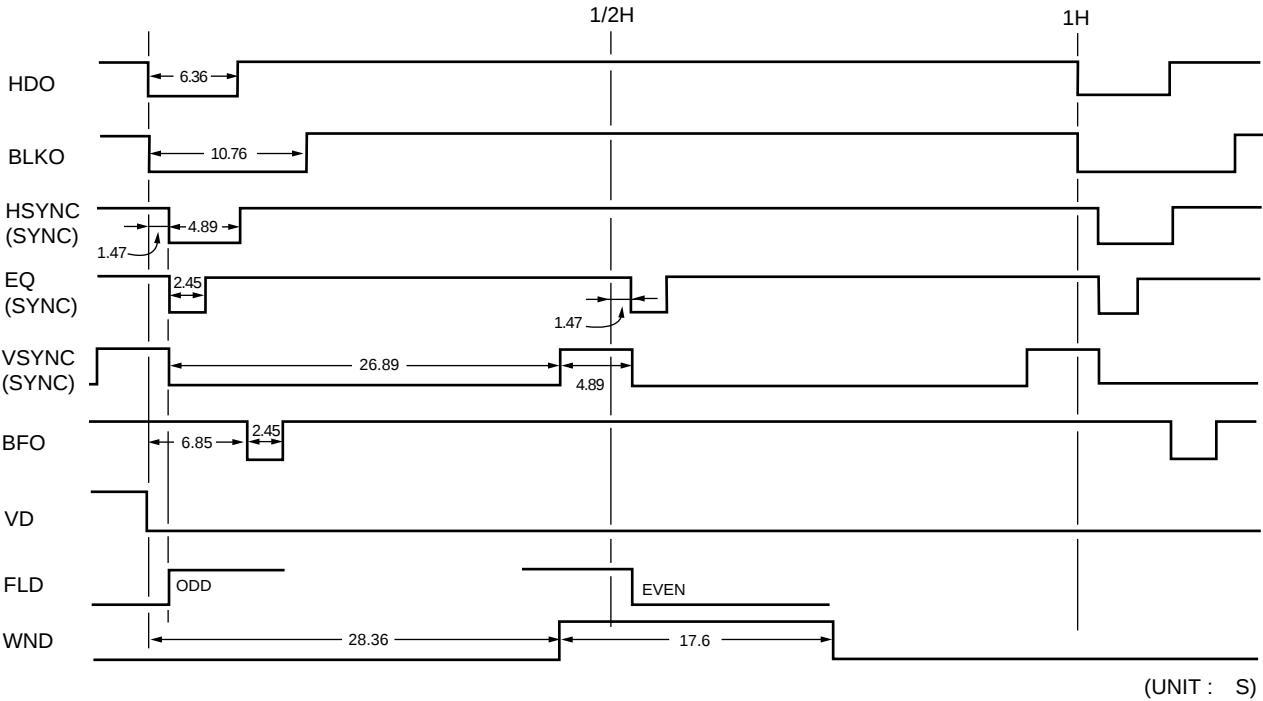
**INPUT**

AIN ; FILTER INVERTER INPUT
 CLKI ; CLOCK INPUT
 (NTSC ; 14.31818MHz
 /PAL ; 14.1875MHz)
 EXT ; INT/EXT (L ; INT)
 FSCI ; 4FSC CLOCK INPUT
 HR ; H RESET
 LR ; LALT RESET
 MODE ; NTSC/PAL (L ; NTSC)
 PSEL ; POLARITY SELECT FOR PHASE COMP
 SCOF ; SUBCARRIER OFF (L ; OFF)
 TEST ; TEST INPUT
 VINT ; INITIALIZE
 VR ; V RESET
 WNDE ; WINDOW ENABLE

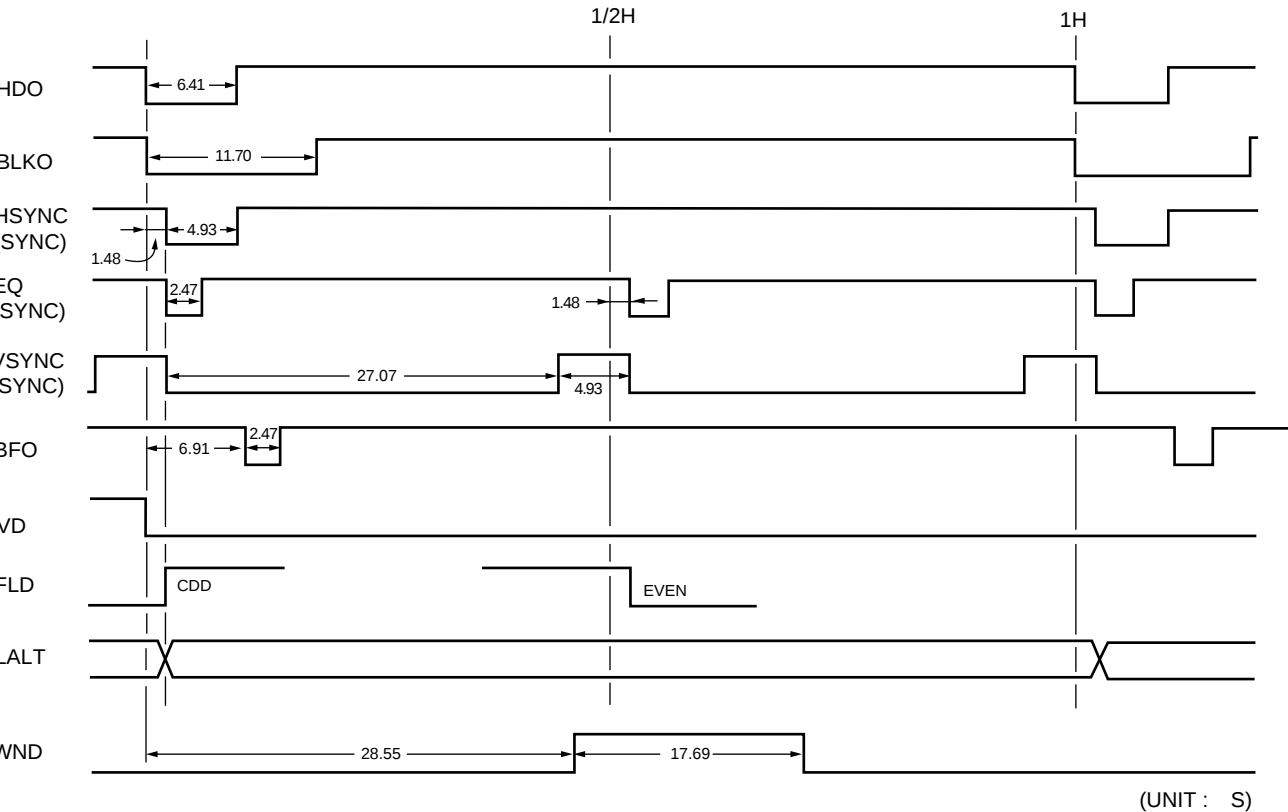
OUTPUT

AOUT ; FILTER INVERTER OUTPUT
 BFO ; BURST FLAG PULSE
 BLKO ; COMPOSITE BLANKING PULSE
 CLKO ; CLOCK OUTPUT
 COMP ; PHASE COMP
 FLD ; FIELD PULSE
 FSCO ; 4FSC CLOCK OUTPUT
 HDO ; H DRIVE PULSE
 LALT ; LINE ALTERNATE PULSE
 SC ; SUBCARRIER
 SYNC ; COMPOSITE SYNC PULSE
 VDO ; V DRIVE PULSE
 WND ; WINDOW

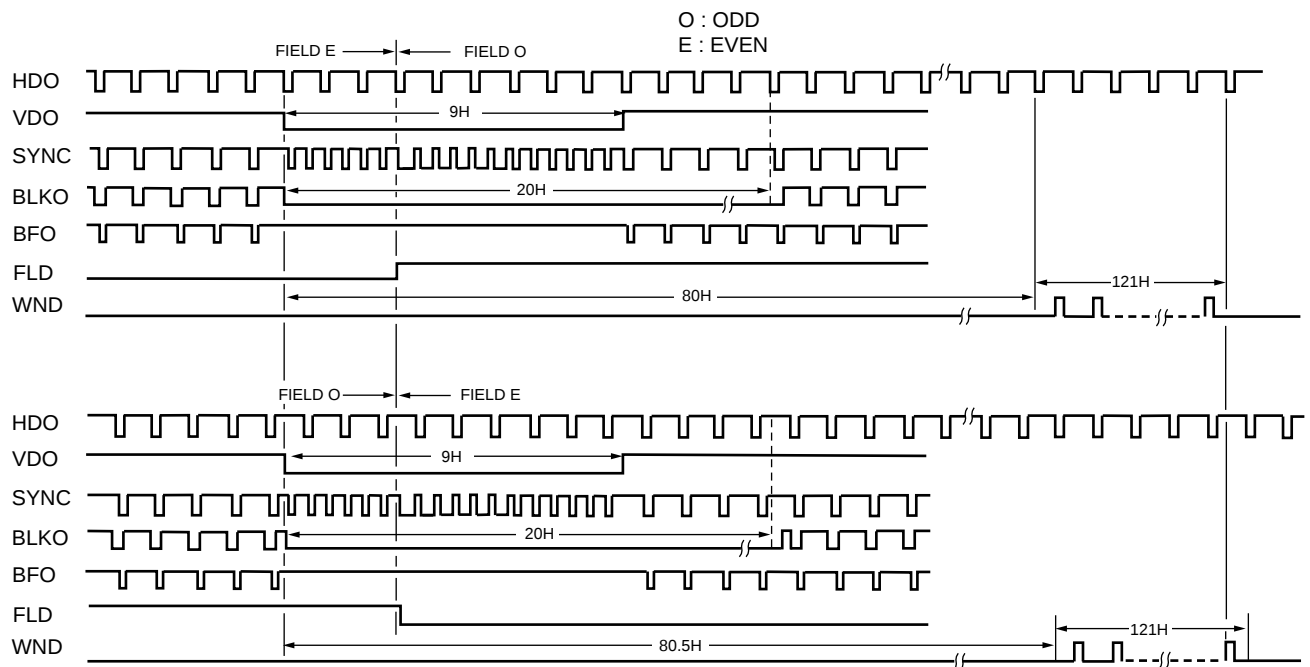
TIMING CHART H (NTSC)



TIMING CHART H (PAL)



TIMING CHART V (NTSC)



TIMING CHART V (PAL)

