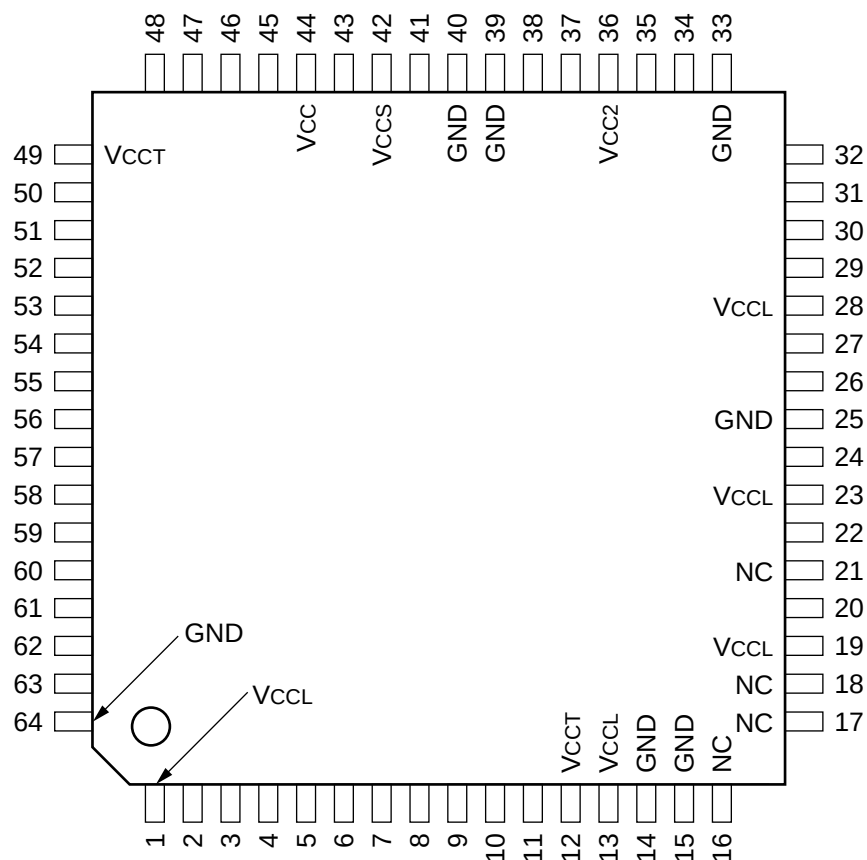


## SERIAL DIGITAL INTERFACE TRANSMISSION ENCODER

—TOP VIEW—



| PIN NO. | I/O | SIGNAL | PIN NO. | I/O | SIGNAL | PIN NO. | I/O | SIGNAL | PIN NO. | I/O | SIGNAL |
|---------|-----|--------|---------|-----|--------|---------|-----|--------|---------|-----|--------|
| 1       | —   | VCC    | 17      | —   | NC     | 33      | —   | GND    | 49      | —   | VCC    |
| 2       | I   | D1X    | 18      | —   | NC     | 34      | I   | MOD0   | 50      | I   | D8X    |
| 3       | I   | D1Y    | 19      | —   | VCC    | 35      | I   | MOD1   | 51      | I   | D8Y    |
| 4       | I   | D0X    | 20      | I   | MDS    | 36      | —   | VCC2   | 52      | I   | D7X    |
| 5       | I   | D0Y    | 21      | —   | NC     | 37      | O   | LST    | 53      | I   | D7Y    |
| 6       | I   | PCX    | 22      | I   | BCAP   | 38      | I   | THRU   | 54      | I   | D6X    |
| 7       | I   | PCY    | 23      | —   | VCC    | 39      | —   | GND    | 55      | I   | D6Y    |
| 8       | I   | SCKX   | 24      | I   | ICP    | 40      | —   | GND    | 56      | I   | D5X    |
| 9       | I   | SCKY   | 25      | —   | GND    | 41      | O   | SX     | 57      | I   | D5Y    |
| 10      | I   | TEST   | 26      | O   | CPX    | 42      | —   | VCCS   | 58      | I   | D4X    |
| 11      | I   | RESET  | 27      | O   | CPY    | 43      | O   | SY     | 59      | I   | D4Y    |
| 12      | —   | VCC    | 28      | —   | VCC    | 44      | —   | VCC    | 60      | I   | D3X    |
| 13      | —   | VCC    | 29      | I   | FSR0   | 45      | I   | ETS    | 61      | I   | D3Y    |
| 14      | —   | GND    | 30      | I   | FSR1   | 46      | O   | PCK    | 62      | I   | D2X    |
| 15      | —   | GND    | 31      | I   | FSR2   | 47      | I   | D9X    | 63      | I   | D2Y    |
| 16      | —   | NC     | 32      | I   | FSR3   | 48      | I   | D9Y    | 64      | —   | GND    |

**INPUT**

BCAP : EXTERNAL CAPACITOR FOR VCO  
D9X - D0X : POSITIVE PARALLEL DATA  
D9Y - D0Y : NEGATIVE PARALLEL DATA  
ETS : 8-BIT/10-BIT SELECT (L = 8-BIT, H OR OPEN = 10-BIT)  
FSR0 - FSR3 : EXTERNAL RESISTOR FOR SETTING THE FREE-RUN FREQUENCY OF VCO  
ICP : EXTERNAL RESISTOR FOR CHARGE PUMP  
MDS : TEST  
MOD0, MOD1 : VCO RATE SELECT  
PCX : POSITIVE PARALLEL CLOCK  
PCY : NEGATIVE PARALLEL CLOCK  
RESET : PLL RESET  
SCKX, SCKY : TEST  
TEST : TEST  
THRU : TEST

**OUTPUT**

LST : PLL LOCK STATUS  
PCK : PARALLEL CLOCK  
CPX, CPY : CHARGE PUMP  
SX, SY : EXTERNAL RESISTOR FOR SERIAL DIFFERENTIAL

