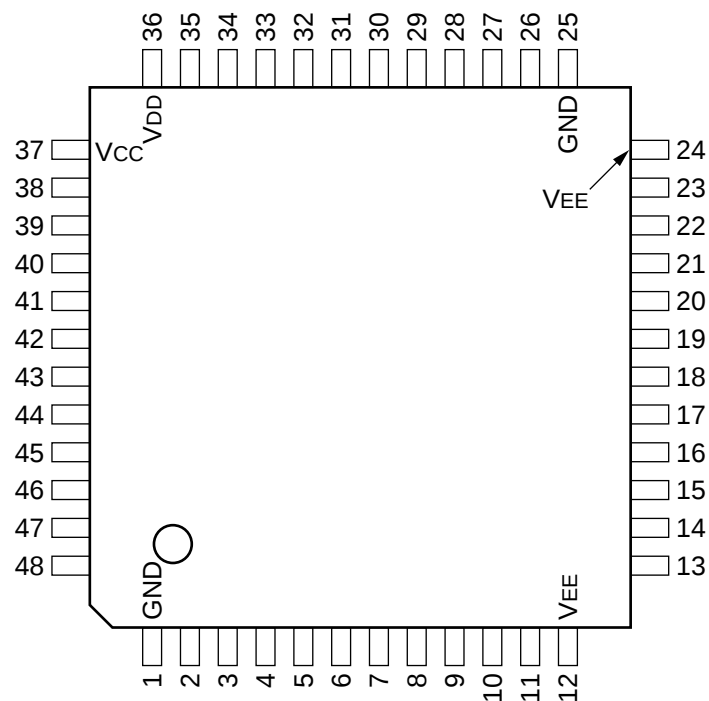


EQUALIZER AND PHASE-LOCKED-LOOP

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	GND	13	O	EQOY	25	—	GND	37	—	VCC
2	I	PBIX	14	O	EQOX	26	O	DTOX	38	I	PRIN
3	I	PBIY	15	O	CPOY	27	O	DTOY	39	I	PROF
4	O	REX	16	O	CPOX	28	O	TDOY	40	I/O	RREF
5	O	REY	17	I	PLLY	29	O	TDOX	41	I	AMPA
6	O	PBOY	18	I	PLLX	30	I	TDIX	42	I	BALA
7	O	PBOX	19	I	FSEL	31	I	TDIY	43	I	AMPB
8	I	OFS	20	I	FIL	32	I	VTH	44	I	BALB
9	I	EQI0	21	I	SPD	33	O	PROY	45	I	PBSW
10	I	EQI1	22	O	CKOX	34	O	PROX	46	I	EQEN
11	I	EQI2	23	O	CKOY	35	I	CTRL	47	I	AGCI
12	—	VEE	24	—	VEE	36	—	VDD	48	O	ENVO

INPUT

AGCI	: AGC REFERENCE
AMPA, AMPB	: EQUALIZER AMPLITUDE PARAMETER
BALA, BALB	: EQUALIZER PHASE PARAMETER
CTRL	: PR-IV EQUALIZER NULL FREQUENCY CONTROL
EQEN	: EQUALIZER ENABLE
EQI1 - EQI2	: EQUALIZER
FIL	: PLL LOOP FILTER
FSEL	: PLL FREQUENCY SELECT
OFS	: OFFSET
PBIX, PBIY	: PRE-AMPLIFIER DIFFERENTIAL INPUT
PBSW	: EQUALIZER PARAMETER CHANNEL SELECT
PLLX, PLLY	: PLL DIFFERENTIAL INPUT
PRIN	: PR-IV EQUALIZER
PROF	: PR-IV EQUALIZER OFFSET
SPD	: PLL FREE RUNNING FREQUENCY CONTROL
TDIX, TDIY	: 3-LEVEL DETECTOR DIFFERENTIAL INPUT
VTH	: 3-LEVEL DETECTOR THRESHOLD VOLTAGE

OUTPUT

CKOX, CKOY	: CLOCK
CPOX, CPOY	: ZERO CROSS COMPARATOR DIFFERENTIAL OUTPUT
DTOX, DTOY	: DATA
ENVO	: ENVELOPE DETECTOR
EQOX, EQOY	: EQUALIZER DIFFERENTIAL OUTPUT
PBOX, PBOY	: PRE-AMPLIFIER DIFFERENTIAL OUTPUT
PROX, PROY	: PR-IV EQUALIZER DIFFERENTIAL OUTPUT
REX, REY	: PRE-AMPLIFIER GAIN CONTROL
TDOX, TDOY	: 3-LEVEL DETECTOR

INPUT/OUTPUT

PREF	: PR-IV EQUALIZER REFERENCE RESISTOR OUTPUT/RESET INPUT
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