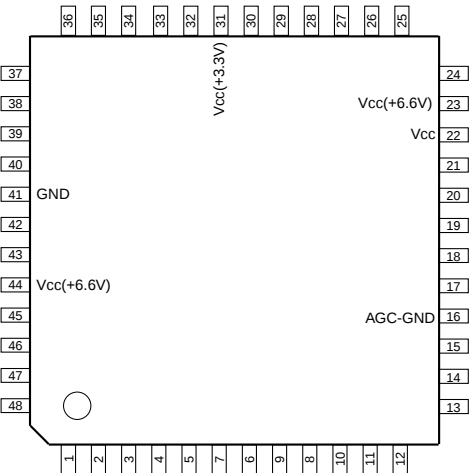

RF EQUALIZER
-TOP VIEW-



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	DCS	25	I	EVR4
2	I	EQ	26	I	REF1
3	I	EQ CONT1	27	I	REF2
4	I	EQ CONT2	28	I	EVR1B
5	I	DECUP1	29	I	EVR1A
6	I	Vcc SW	30	I	DECUP2
7	O	SW	31	—	Vcc(+3.3V)
8	—	GND	32	I	EQ CONT4
9	I	VCA IN2	33	I	EQ CONT3
10	I	VCA IN1	34	I	PS REF1
11	I	VCA IN4	35	I	PS REF2
12	I	VCA IN3	36	I	REF CONST
13	I	EVR2B	37	I	ENV CONST
14	I	EVR2A	38	O	ENV
15	O	VCA	39	I	EQ IN1
16	—	AGC-GND	40	I	EQ IN2
17	I	AGC IN1	41	—	GND
18	I	AGC IN2	42	I	EQ IN3
19	I	AGC REF	43	I	EQ IN4
20	O	AGC 1	44	—	Vcc(+6.6V)
21	O	AGC 2	45	I	HEAD CONT
22	—	Vcc	46	I	S15 CONT
23	—	Vcc(+6.6V)	47	I	S10 CONT
24	I	AGC CONT	48	I	SPEED CONT

- INPUT**
- AGC IN1, AGC IN2 ; AGC INPUT
AGC CONT ; AGC CONTROL
AGC REF ; AGC REFERENCE
DECUP1, DECUP2 ; EXTERNAL DECOUPLING CAPACITOR
ENV CONST ; ENVELOPE TIME CONSTANT
EQ ; EQUALIZER
EQ CONT1 - EQ CONT4 ; EQUALIZER CONTROL
EQ IN1 - EQ IN4 ; RF SIGNAL
EVR1A, EVR1B ; ELECTRONIC VARIABLE RESISTOR
EVR2A, EVR2B ; ELECTRONIC VARIABLE RESISTOR
EVR4 ; ELECTRONIC VARIABLE RESISTOR
HEAD CONT ; HEAD CHANNEL CONTROL
PS REF1, PS REF2 ; POWER SUPPLY REFERENCE
REF CONST ; REF TIME CONSTANT RESISTOR
SPEED CONT ; SPEED CONTROL
VCA IN1 - VCA IN4 ; VOLTAGE CONTROLLED AMPLIFIER INPUT
- OUTPUT**
- AGC1, AGC2 ; AUTOMATIC GAIN CONTROL
DCS ; DC SERVO
ENV ; RF ENVELOPE
SW ; EQ-A OR EQ-B SELECTED SIGNAL
VCA ; VOLTAGE CONTROLLED AMPLIFIER

