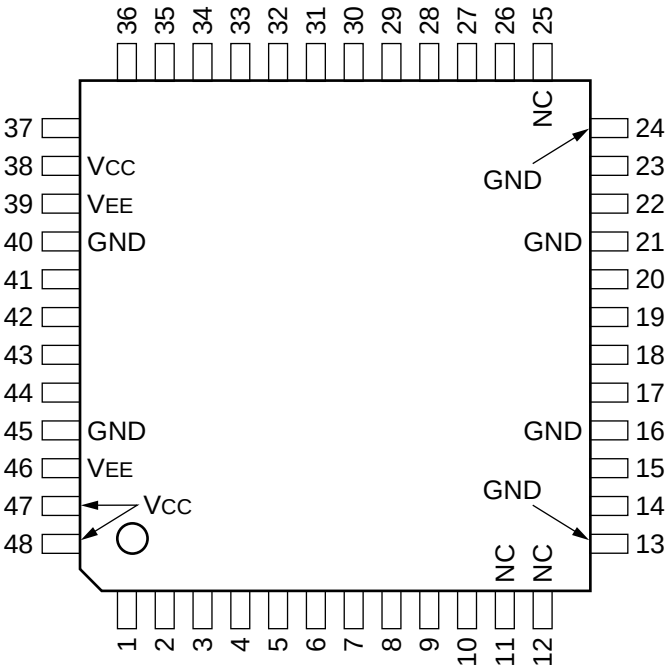
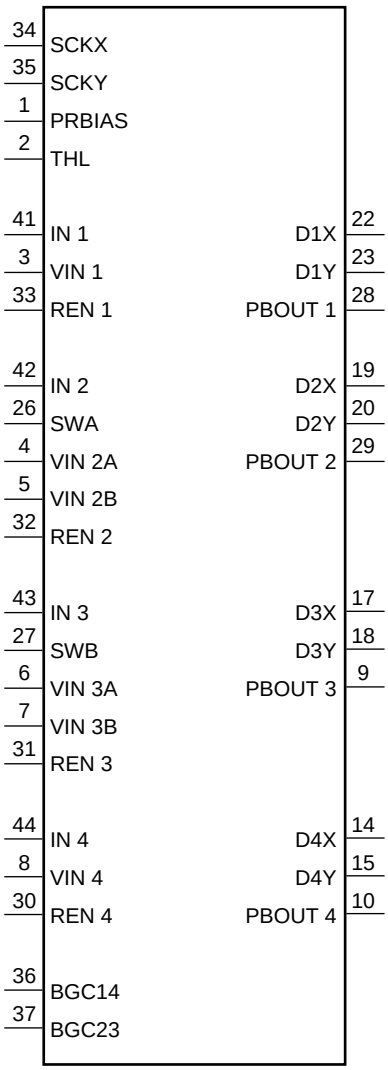
REC DRIVER

—TOP VIEW—





PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	PRBIAS	25	—	NC
2	I	THL	26	I	SWA
3	I	VIN 1	27	I	SWB
4	I	VIN 2A	28	O	PBOUT 1
5	I	VIN 2B	29	O	PBOUT 2
6	I	VIN 3A	30	I	REN 4
7	I	VIN 3B	31	I	REN 3
8	I	VIN 4	32	I	REN 2
9	O	PBOUT 3	33	I	REN 1
10	O	PBOUT 4	34	I	SCKX
11	—	NC	35	I	SCKY
12	—	NC	36	I	BGC14
13	—	GND	37	I	BGC23
14	I/O	D4X	38	—	VCC
15	I/O	D4Y	39	—	VEE
16	—	GND	40	—	GND
17	I/O	D3X	41	I	IN 1
18	I/O	D3Y	42	I	IN 2
19	I/O	D2X	43	I	IN 3
20	I/O	D2Y	44	I	IN 4
21	—	GND	45	—	GND
22	I/O	D1X	46	—	VEE
23	I/O	D1Y	47	—	VCC
24	—	GND	48	—	VCC

INPUT

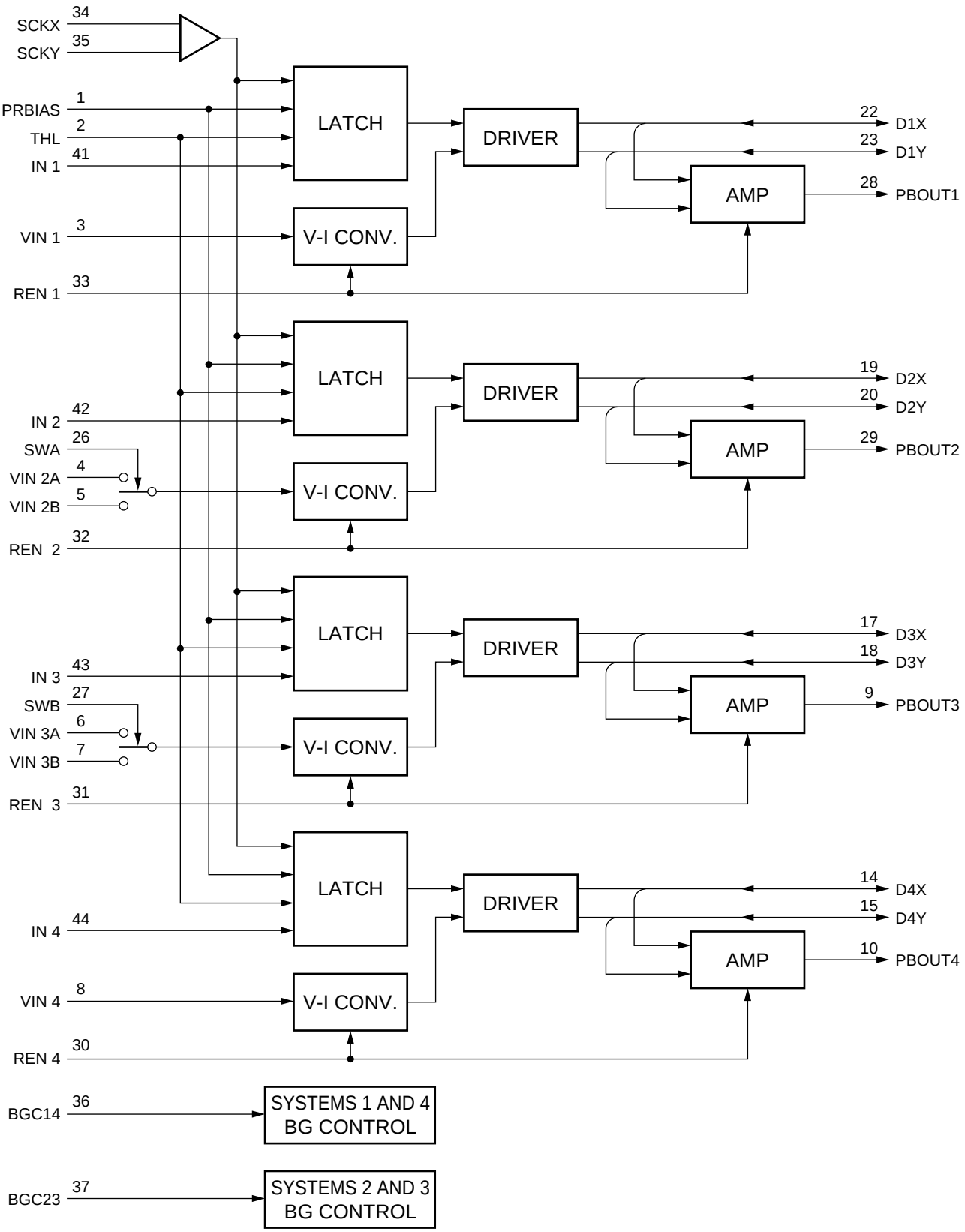
BGC14	;	BAND GAP ENABLE CH-1/CH4 (H : POWER SAVE, L : ENABLE)
BGC23	;	BAND GAP ENABLE CH-2/CH3 (H : POWER SAVE, L : ENABLE)
IN 1	;	DATA INPUT CH-1
IN 2	;	DATA INPUT CH-2
IN 3	;	DATA INPUT CH-3
IN 4	;	DATA INPUT CH-4
PRBIAS	;	PR DATA BIAS
REN 1 - REN 4	;	DRIVE/RF PB SELECT (H : RF PB, L : DRIVE)
SCKX	;	DATA LATCH CLOCK (X)
SCKY	;	DATA LATCH CLOCK (Y)
SWA	;	DRIVE CURRENT OUTPUT SELECT (H : VIN 2A, L : VIN 2B)
SWB	;	DRIVE CURRENT OUTPUT SELECT (H : VIN 3A, L : VIN 3B)
THL	;	PR THROUGH/LATCH CONTROL (H : LATCH, L : THROUGH)
VIN 1	;	DC BIAS
VIN 2A, VIN 2B	;	DC BIAS
VIN 3A, VIN 3B	;	DC BIAS
VIN 4	;	DC BIAS

OUTPUT

PBOUT1 - PBOUT4	;	PB RF

INPUT/OUTPUT

D1X, D1Y	;	REC HEAD PB RF INPUTS/DRIVE CURRENT OUTPUTS
D2X, D2Y	;	REC HEAD PB RF INPUTS/DRIVE CURRENT OUTPUTS
D3X, D3Y	;	REC HEAD PB RF INPUTS/DRIVE CURRENT OUTPUTS
D4X, D4Y	;	REC HEAD PB RF INPUTS/DRIVE CURRENT OUTPUTS


SCKX
34
SCKY
35
PRBIAS
1
THL
2
IN 1
41
VIN 1
3
REN 1
33
LATCH
V-I CONV.
DRIVER
AMP
22
D1X
23
D1Y
28
PBOUT1
IN 2
42
SWA
26
VIN 2A
4
VIN 2B
5
REN 2
32
19
D2X
20
D2Y
29
PBOUT2
IN 3
43
SWB
27
VIN 3A
6
VIN 3B
7
REN 3
31
17
D3X
18
D3Y
9
PBOUT3
IN 4
44
VIN 4
8
REN 4
30
14
D4X
15
D4Y
10
PBOUT4
BGC14
36
SYSTEMS 1 AND 4
BG CONTROL
BGC23
37
SYSTEMS 2 AND 3
BG CONTROL