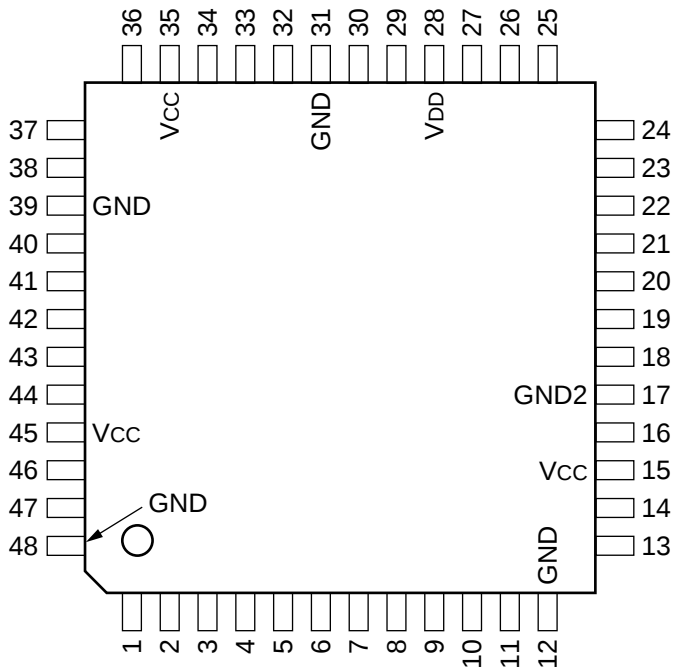
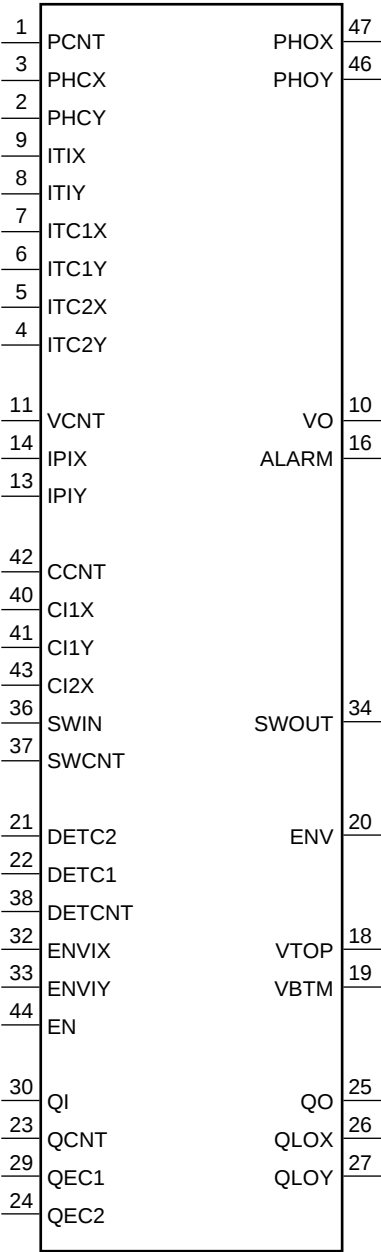


PHASE EQUALIZER AND QUANTIZED FEED-BACK (QFB) CIRCUIT
—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	PCNT	25	O	QO
2	I	PHCY	26	O	QLOX
3	I	PHCX	27	O	QLOY
4	I	ITC2Y	28	—	VDD
5	I	ITC2X	29	I	QEC1
6	I	ITC1Y	30	I	QI
7	I	ITC1X	31	—	GND
8	I	ITiy	32	I	ENVIX
9	I	ITIX	33	I	ENViy
10	O	VO	34	O	SWOUT
11	I	VCNT	35	—	Vcc
12	—	GND	36	I	SWIN
13	I	IPIY	37	I	SWCNT
14	I	IPIX	38	I	DETCNT
15	—	Vcc	39	—	GND
16	O	ALARM	40	I	CI1X
17	—	GND2	41	I	CI1Y
18	O	VTOP	42	I	CCNT
19	O	VBTM	43	I	CI2X
20	O	ENV	44	I	EN
21	I	DETC2	45	—	Vcc
22	I	DETC1	46	O	PHOY
23	I	QCNT	47	O	PHOX
24	I	QEC2	48	—	GND



INPUT

CCNT	; COSINE CORRECTION CONTROL SIGNAL
CI1X, CI1Y, CI2X	; COSINE DELAYED SIGNAL
DETC1, DETC2	; CAPACITOR CONNECTION TERMINALS
DETCNT	; VCA CONTROL FOR ENVELOPE DETECTOR
EN	; BAND GAP ENABLE (H : POWER SAVE)
ENVIX, ENVIY	; INPUTS OF VCA FOR ENVELOPE DETECTOR
IPIX, IPIY	; INPUTS OF VCA
ITC1X, ITC1Y, ITC2X, ITC2Y	; CAPACITOR CONNECTION TERMINALS
ITIX, ITIY	; INPUTS OF INTEGRATOR
PCNT	; PHASE EQ CONTROL
PHCX, PHCY	; CAPACITOR CONNECTION TERMINALS
QCNT	; QFB CONTROL
QEC1, QEC2	; CONNECTOR TERMINALS OF TIME CONSTANT DEVICES
QI	; INPUT OF QFB
SWCNT	; SWITCHING CONTROL
SWIN	; INPUT OF SWITCH
VCONT	; GAIN CONTROL FOR VCA

OUTPUT

ALARM	; ALARM OUTPUT
ENV	; ENVELOPE OUTPUT
PHOX, PHOY	; PHASE EQUALIZED OUTPUTS
QLOX, QLOY	; LIMITER OUTPUTS OF QFB
QO	; OUTPUT OF QFB
SWOUT	; OUTPUT OF SWITCH
VBTM	; ENVELOPE BOTTOM VOLTAGE
VO	; OUTPUT OF VCA
VTOP	; ENVELOPE TOP VOLTAGE

