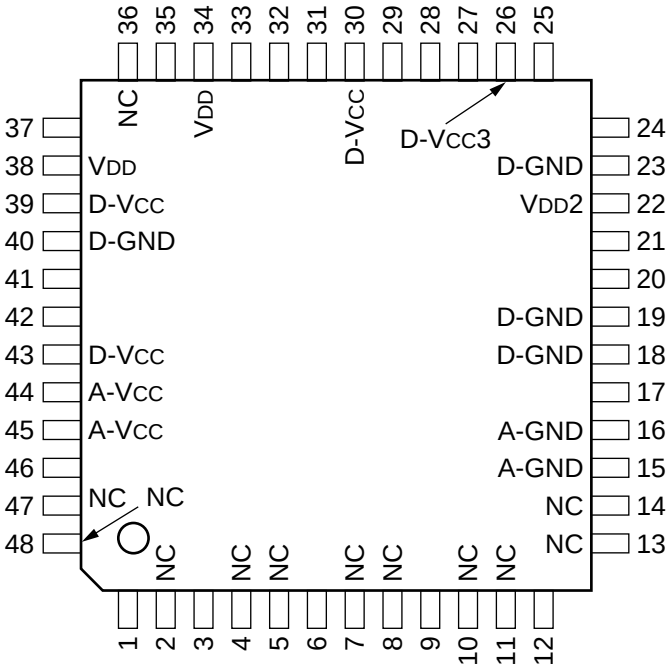
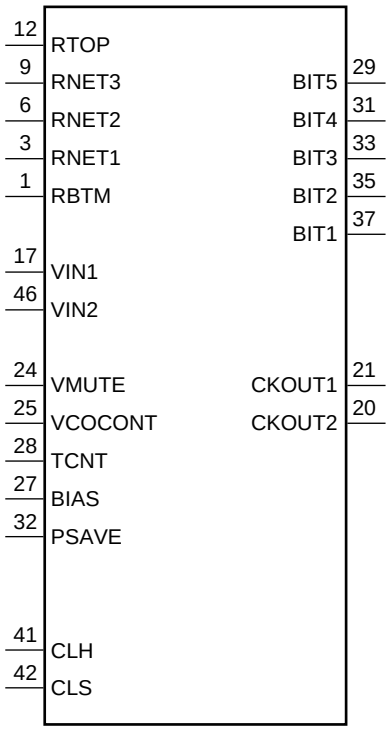
5-BIT A/D CONVERTER AND VCO

—TOP VIEW—





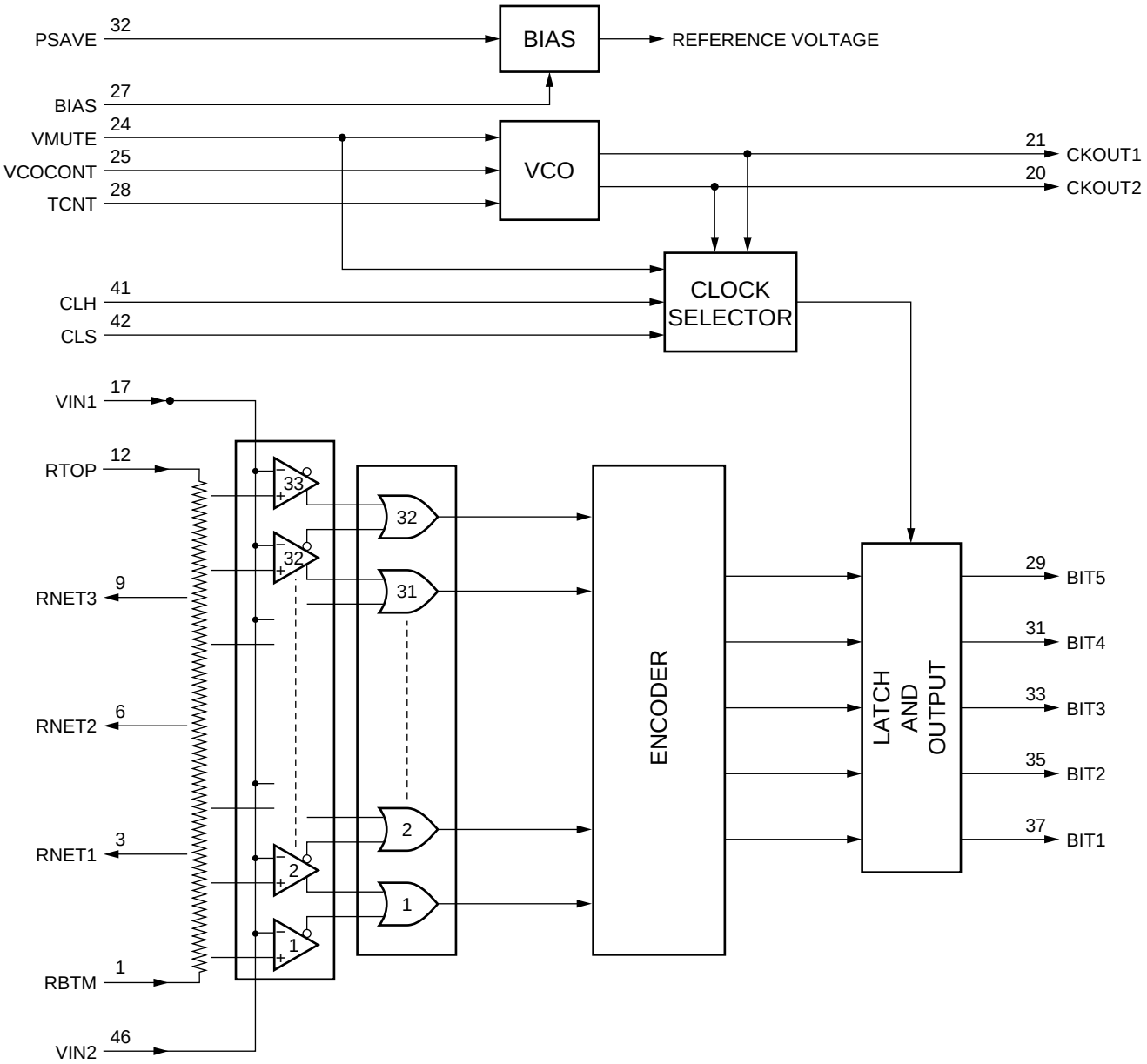
PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	RBTM	13	—	NC	25	I	VCOCONT	37	O	BIT1(LSB)
2	—	NC	14	—	NC	26	—	D-VCC3	38	—	VDD
3	O	RNET1	15	—	A-GND	27	I	BIAS	39	—	D-VCC
4	—	NC	16	—	A-GND	28	I	TCNT	40	—	D-GND
5	—	NC	17	I	VIN1	29	O	BIT5(MSB)	41	I	CLH
6	O	RNET2	18	—	D-GND	30	—	D-VCC2	42	I	CLS
7	—	NC	19	—	D-GND	31	O	BIT4	43	—	D-VCC
8	—	NC	20	O	CKOUT2	32	I	PSAVE	44	—	A-VCC
9	O	RNET3	21	O	CKOUT1	33	O	BIT3	45	—	A-VCC
10	—	NC	22	—	VDD2	34	—	VDD	46	I	VIN2
11	—	NC	23	—	D-GND	35	O	BIT2	47	—	NC
12	I	RTOP	24	I	VMUTE	36	—	NC	48	—	NC

INPUT

BIAS	;	BIAS VOLTAGE
CLH	;	EXTERNAL CLOCK
CLS	;	EXTERNAL CLOCK
PSAVE	;	POWER SAVE (H : POWER SAVE MODE)
RBTM	;	LEAST SIGNIFICANT VOLTAGE
RTOP	;	MOST SIGNIFICANT VOLTAGE
TCNT	;	TEMPERATURE CONTROL FOR VCO
VCOCONT	;	VCO FREQUENCY CONTROL
VIN1, VIN2	;	ANALOG VOLTAGE
VMUTE	;	CLOCK SELECT (H : EXTERNAL, L : INTERNAL VCO)

OUTPUT

BIT1 - BIT5	;	A/D
CKOUT1, CKOUT2	;	VCO CLOCK
RNET1 - RNET3	;	INTERMEDIATE VOLTAGE


PSAVE
32
BIAS
REFERENCE VOLTAGE
BIAS
27
VMUTE
24
VCOCONT
25
TCNT
28
VCO
21
CKOUT1
20
CKOUT2
CLOCK SELECTOR
CLH
41
CLS
42
VIN1
17
RTOP
12
RNET3
9
RNET2
6
RNET1
3
RBTM
1
VIN2
46
33
32
31
2
1
ENCODER
LATCH AND OUTPUT
29
BIT5
31
BIT4
33
BIT3
35
BIT2
37
BIT1