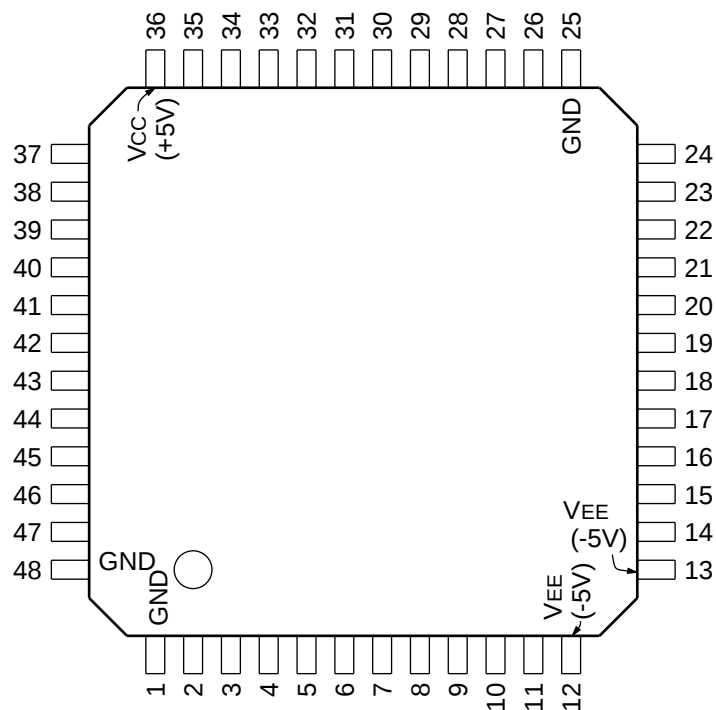

BYPOLAR/SILICON MONOLITHIC IC

- TOP VIEW -



(V_{DD} = +5V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	-	GND	13	-	VEE (-5V)	25	-	GND	37	O	VMON
2	O	VRB1	14	O	VBGR	26	O	EQOX	38	I	PBSW
3	O	VRR	15	I	RDLY	27	O	EQOY	39	I	VTH
4	I	PBIX	16	I	RREF	28	O	DTOX	40	I	ENVI
5	I	PBIY	17	I	RLPF	29	O	DTOY	41	O	ENVO
6	I	REX	18	I	PBEN	30	I	CF2A	42	O	MON2
7	I	REY	19	I	EQEN	31	I	CF2B	43	O	MON1
8	O	PBOX	20	I	GAIN	32	I	CF1A	44	O	MON0
9	O	PBOY	21	I	OFS	33	I	CF1B	45	I	VRB2
10	I	EQIX	22	O	QFBX	34	I	CF0A	46	I	EFLX
11	I	EQIY	23	O	QFBY	35	I	CF0B	47	I	EFLY
12	-	VEE (-5V)	24	I	TSR	36	-	VCC (+5V)	48	-	GND

34	CF0A	VMON	37	INPUT	
35	CF0B	VMON0	44	CF0A,0B,1A,1B,2A,2B	; 3TAP FIR FILTER COEFFICIENT INPUT
32	CF1A	VMON1	43	EFLX,EFLY	; ENVELOPE DETECTOR DRIVE (X,Y)
33	CF1B	VMON2	42	ENVI	; AGC VOLTAGE
30	CF2A			EQIX,EQIY	; EQUALIZER DRIVE (X,Y)
31	CF2B			EQEN	; ENABLE INPUT
6	REX			GAIN	; GAIN (EQUALIZER)
7	REY			OFS	; OFF SET CONTROL (DC SERVO FEEDBACK INPUT)
4	PBIX	PBOX	8	PBEN	; ENABLE INPUT
5	PBIY	PBOY	9	PBIX,PBIY	; PREAMP DRIVE (X,Y)
				PB SW	; 3TAP FIR FILTER COEFFICIENT SELECT
10	EQIX	EQOX	26	RDLY	; ACTIVE DELAY BIAS RESISTER
11	EQIY	EQOY	27	REX,REY	; PREAMP GAIN SELECT (X,Y) (NORMALLY SHORT)
				RLPF	; ACTIVE LPF BIAS RESISTER
18	PBEN	QFBX	22	RREF	; ACTIVE DELAY/LPF BIAS RESISTER
19	EQEN	QFBY	23	TSR	; CHIP THERMAL DETECT RESISTER
				VTH	; AGC GAIN LIMITTER
40	ENVI	ENVO	41	OUTPUT	
				DTOX,DTOY	; ZERO CROSS CONPARATOR ECL DRIVE (X,Y)
46	EFLX	DTOX	28	ENVO	; ENVELOPE DETECTOR
47	EFLY	DTOY	29	EQOX,EQOY	; EQUALIZER DRIVE (X,Y)
				PBOX,PBOY	; PREAMP DRIVE (X,Y)
38	PBSW	VRB1	2	QFBX,QFBY	; QFB FILTER (X,Y)
20	GAIN	VRB2	45	VBGR	; BAND GAP REFERENCE VOLTAGE
21	OFS	VRR	3	VMON	; MONITOR OUT REFERENCE VOLTAGE
39	VTH	VBGR	14	VMON0	; MONITOR OUT (ACTIVE LPF)
				VMON1	; MONITOR OUT (ACTIVE DELAY1)
15	RDLY			VMON2	; MONITOR OUT (ACTIVE DELAY2)
16	RREF			VRB1	; PREAMP REFERENCE VOLTAGE
17	RLPF			VRB2	; ENVEROPE DETECTOR REFERENCE VOLTAGE
24	TSR			VRR	; EQUALIZER REFERENCE VOLTAGE

