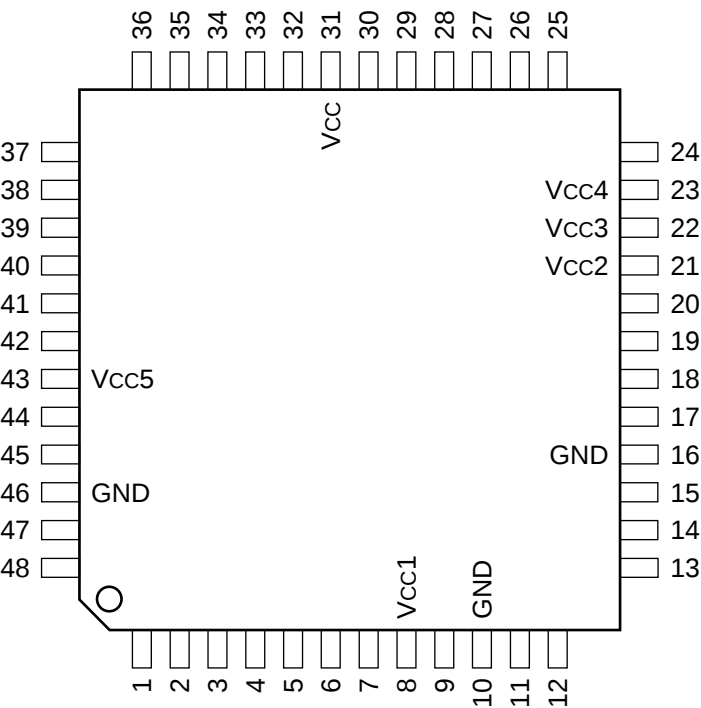


C-MOS DIGITAL VTR EQUALIZER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	JSWP	13	I	EVR2B	25	I	EVR4	37	I	ENVCONST
2	O	DCSOUT	14	I	EVR2A	26	I	REF1	38	I	REFCONST
3	I	EQIN	15	O	VCAOUT	27	I	REF2	39	O	TEST
4	I	EQCNT1	16	—	GND	28	I	EVR1B	40	O	REFOUT
5	I	EQCNT2	17	I	AGCIN1	29	I	EVR1A	41	O	ENVOUT
6	I	DECUP1	18	I	AGCIN2	30	I	DECUP2	42	I	IN1
7	I	EQA/EQB	19	I	AGCREF	31	—	Vcc	43	—	Vcc5
8	—	Vcc1	20	O	AGCOUT	32	I	EQCNT4	44	I	DC1
9	O	SWOUT	21	—	Vcc2	33	I	EQCNT3	45	I	DC2
10	—	GND	22	—	Vcc3	34	I	PSREF1	46	—	GND
11	I	VCAIN1	23	—	Vcc4	35	I	PSREF2	47	I	IN2
12	I	VCAIN2	24	I	AGCCONT	36	I	REF3	48	I	REC/PB

INPUT

AGCCONT : AGC TIME CONSTANT
AGCIN1, AGCIN2 : AUTOMATIC GAIN CONTROLLER
AGCREF : DE-COUPLED FOR AGC REFERENCE VOLTAGE
DC1, DC2 : DC SERVO TIME CONSTANT
DECUP1, DECUP2 : DE-COUPLED CAPACITOR
ENVCONST : ENVELOPE DETECTOR TIME CONSTANT SETTING
EQCNT1 - EQCNT4 : EQUALIZING COMPONENTS
EQIN : INTEGRATION EQUALIZER
EQA/EQB : EQUALIZING SIGNAL
EVR1A, EVR1B : PHASE SHIFT CONTROL
EVR2A, EVR2B : EQUALIZER ADJUSTMENT
EVR4 : AGC OUTPUT AMPLITUDE CONTROL
IN1, IN2 : DC SERVO CONTROL
JSWP : JSWP SIGNAL
PSREF1, PSREF2 : EQUALIZER REFERENCE CURRENT SETTING
REC/PB : REC/PB SIGNAL
REF1 - REF2 : PHASE SHIFTER REFERENCE CURRENT SETTING
REF3 : TEST CIRCUIT REFERENCE CURRENT SETTING
REFCONST : ENVELOPE DETECTOR REFERENCE CURRENT SETTING
VCAIN1, VCAIN2 : COSINE EQUALIZER

OUTPUT

AGCOUT : AGC
DCSOUT : DC SERVO
ENVOUT, REFOUT : ENVELOPE DETECTOR
SWOUT : INTEGRATION EQUALIZER
TEST : TEST
VCAOUT : COSINE EQUALIZER

