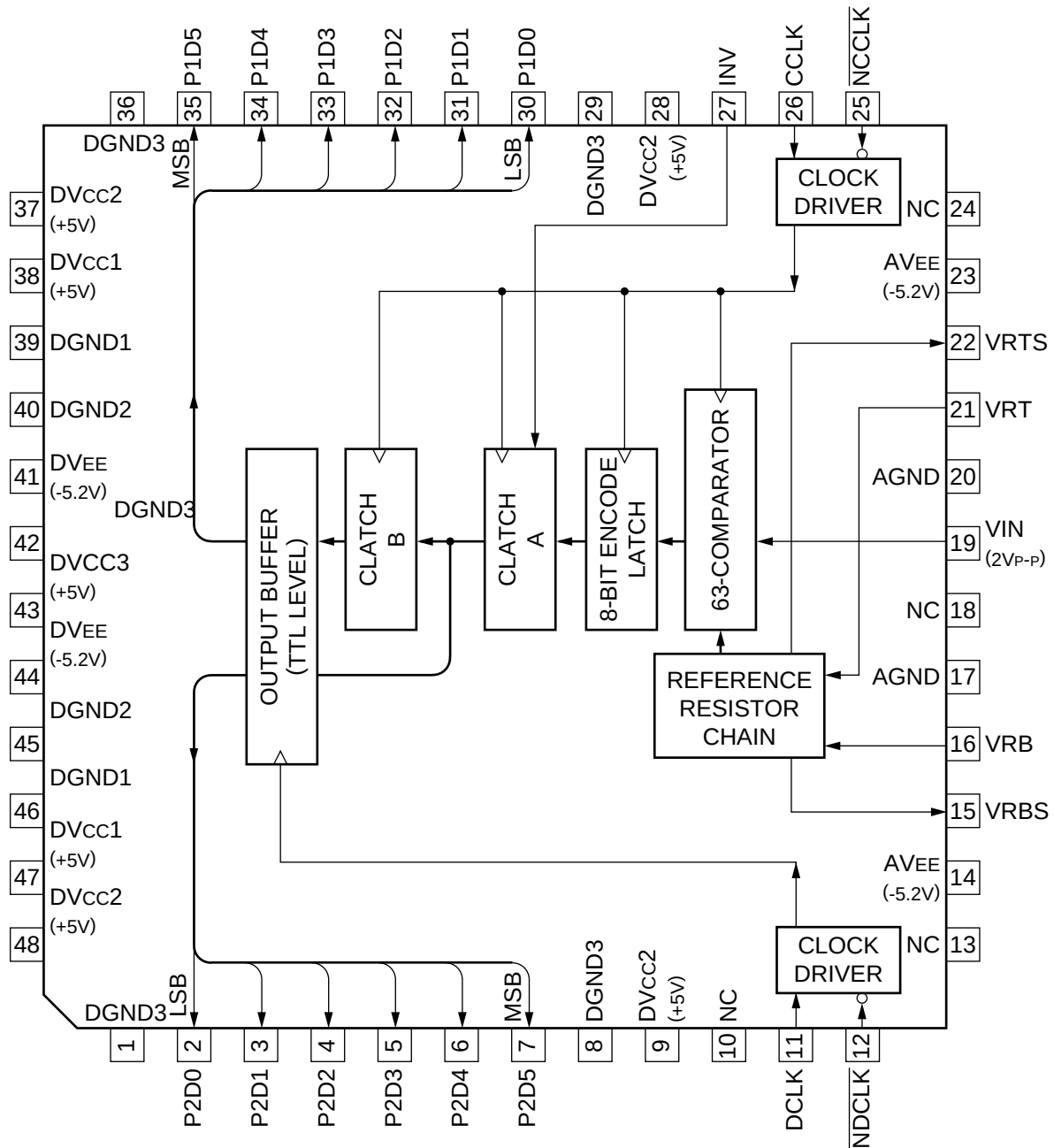

CXA1866Q
IL00

6-BIT 140MSPS FLASH A/D CONVERTER -TOP VIEW-



22	VRTS	P1D5	35
21	VRT	P1D4	34
		P1D3	33
19	VIN	P1D2	32
		P1D1	31
16	VRB	P1D0	30
15	VRBS		
		P2D5	7
27	INV	P2D4	6
		P2D3	5
26	CCLK	P2D2	4
25	NCCLK	P2D1	3
11	DCLK	P2D0	2
12	NDCLK		

INPUT

CCLK, $\overline{\text{NCCLK}}$; CONVERSION CLOCK
DCLK, $\overline{\text{NDCLK}}$; DEMULTIPLEX LATCH CLOCK
INV ; INVERT POLARITY OF DIGITAL DATA
VRB ; BOTTOM REFERENCE VOLTAGE
VRT ; TOP REFERENCE VOLTAGE

OUTPUT

P1D0 - P1D5 ; DIGITAL DATA OUTPUT(PORT1)
(P1D5 ; MSB, P2D5 ; LSB)
P2D0 - P2D5 ; DIGITAL DATA OUTPUT(PORT2)
(P2D5 ; MSB, P2D0 ; LSB)
VRBS ; VRB SENSE
VRTS ; VRT SENSE