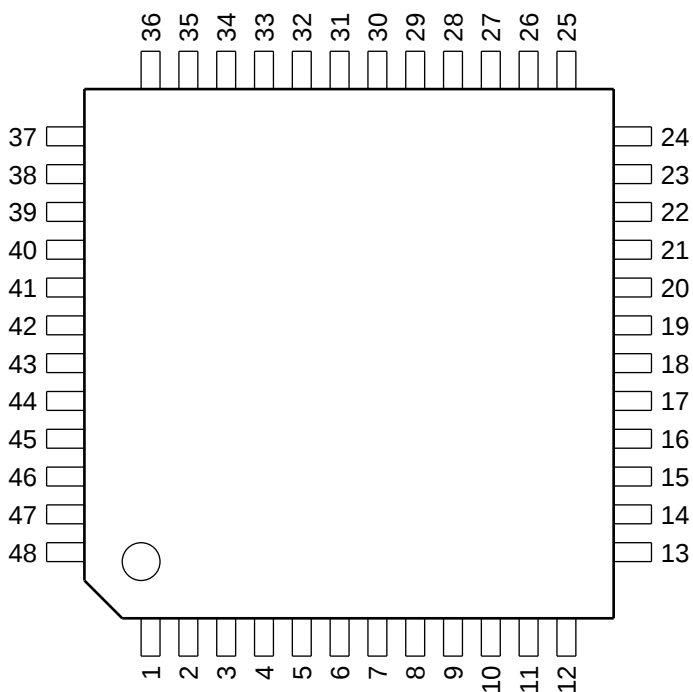

SIGNAL PROCESSING DRIVER

- TOP VIEW -

(V_{CC} 1=+5V, V_{CC} 2=+13V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	–	NC	13	–	NC	25	I	FRP	37	–	NC
2	I	GAMMA	14	I	R CLP	26	I	PRG (0V)	38	–	NC
3	I	B GAIN	15	O	R	27	I	SID BRT	39	I	R
4	I	R GAIN	16	–	GND	28	I	RGB BRT	40	I	G
5	I	RGB GAIN	17	–	GND	29	I	R BRT	41	I	B
6	I	SID GAIN	18	–	V _{CC} 2	30	I	B BRT	42	I	BRT
7	I	SIG CENT	19	–	V _{CC} 2	31	I	SH4	43	–	V _{CC} 1
8	I	B CLP	20	O	SID	32	I	SH3	44	–	V _{CC} 1
9	O	B	21	I	SID CLP	33	I	SH2	45	–	GND
10	I	G CLP	22	O	VCOM	34	I	SH1	46	–	GND
11	O	G	23	I	VCOM CONT	35	I	BF	47	I	MODE (+5V)
12	–	NC	24	–	NC	36	–	NC	48	–	NC

39	R	R	15	INPUT	
40	G	G	11	B	; BLUE SIGNAL
41	B	B	9	BRT	; BRIGHTNESS
35	BF	SID	20	B CLP	; BLUE OUTPUT DETECTED SIGNAL
42	BRT			B GAIN	; BLUE SIGNAL GAIN CONTROL
34	SH1	VCOM	22	BF	; BURST FLAG PULSE
33	SH2			BRT	; BRIGHTNESS
32	SH3			FRP	; FLY BACK PULSE
31	SH4			G	; GREEN SIGNAL
27	SID BRT			C CLP	; GREEN OUTPUT DETECTED SIGNAL
28	RGB BRT			GAMMA	; GAMMA CONTROL
29	R BRT			MODE	; GAIN CONTROL MODE (H: NORMAL MODE, L: HIGH GAIN MODE)
30	B BRT			PRG	; LEVEL OUTPUT SELECT (L: BLACK LEVEL, H: GRAY LEVEL)
25	FRP			R	; RED SIGNAL
26	PRG			R BRT	; RED SIGNAL BRIGHTNESS CONTROL
2	GAMMA			R CLP	; RED OUTPUT DETECTED SIGNAL
3	B GAIN			R GAIN	; RED SIGNAL GAIN CONTROL
4	R GAIN			RGB BRT	; RGB SIGNAL BRIGHTNESS CONTROL
5	RGB GAIN			RGB GAIN	; RGB SIGNAL GAIN CONTROL
6	SID GAIN			SH1	; SAMPLE HOLD PULSE FOR RESAMPLING
14	R CLP			SH2	; SAMPLE HOLD PULSE FOR B SIGNAL
10	G CLP			SH3	; SAMPLE HOLD PULSE FOR G SIGNAL
8	B CLP			SH4	; SAMPLE HOLD PULSE FOR R SIGNAL
7	SIG CENT			SID BRT	; BLACK LEVEL CONTROL OF SIDE BLACK SIGNAL
21	SID CLP			SID CLP	; SIDE BLACK DETECTED SIGNAL
23	VCOM CONT			SID GAIN	; GRAY LEVEL CONTROL OF SIDE BLACK SIGNAL
				SIG CENT	; CENTER VOLTAGE CONTROL OF RGB SIGNAL
				VCOM CONT	; VCOM VOLTAGE CONTROL
				OUTPUT	
				B	; BLUE SIGNAL
				G	; GREEN SIGNAL
				R	; RED SIGNAL
				SID	; SIDE BLACK
				VCOM	; VCOM VOLTAGE

