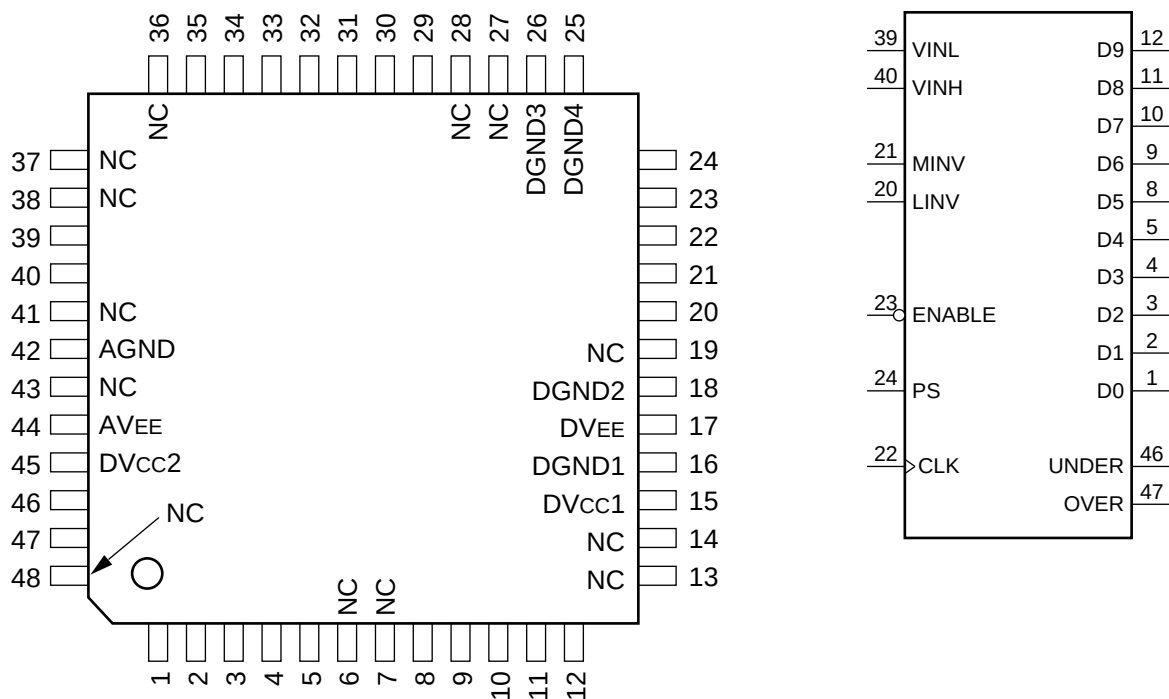


TTL 10-BIT 20MSPS A/D CONVERTER

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	O	D0 (LSB)	13	—	NC	25	—	DGND4	37	—	NC
2	O	D1	14	—	NC	26	—	DGND3	38	—	NC
3	O	D2	15	—	DVcc1	27	—	NC	39	I	VINL
4	O	D3	16	—	DGND1	28	—	NC	40	I	VINH
5	O	D4	17	—	DVEE	29	—	VREFTS	41	—	NC
6	—	NC	18	—	DGND2	30	I	VREF1	42	—	AGND
7	—	NC	19	—	NC	31	—	VREF2	43	—	NC
8	O	D5	20	I	LINV	32	—	VREF3	44	—	AVEE
9	O	D6	21	I	MINV	33	—	VREFB	45	—	DVcc2
10	O	D7	22	I	CLK	34	I	VREFBS	46	O	UNDER
11	O	D8	23	I	ENABLE	35	—	NC	47	O	OVER
12	O	D9 (MSB)	24	I	PS	36	—	NC	48	—	NC

INPUT

CLK ; SYSTEM CLOCK
 ENABLE ; 3 STAGE CONTROL (L : ENABLE)
 LINV ; D0 (LSB)-D8 INVERSE
 MINV ; D9 (MSB) INVERSE
 PS ; POWER SAVE (H : POWER SAVE MODE)
 VINL, VINH ; ANALOG

OUTPUT

D0 (LSB) - D9 (MSB) ; DIGITAL
 UNDER ; UNDER FLOW
 OVER ; OVER FLOW

