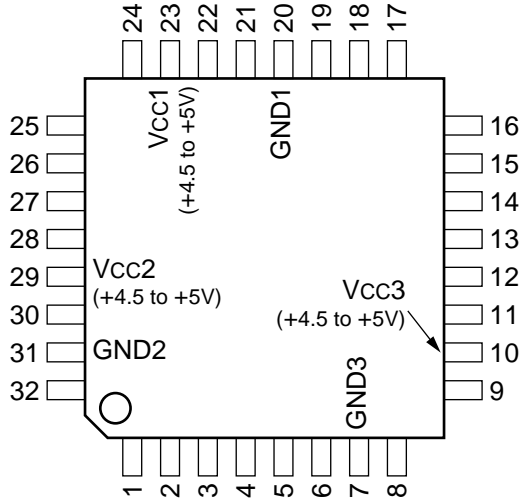
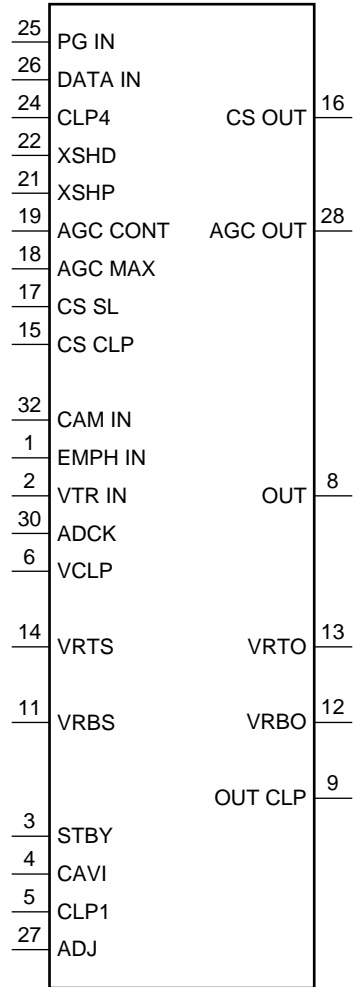

HEAD AMPLIFIER FOR DIGITAL CCD CAMERA

—TOP VIEW—



(VCC1 to 3 = +4.5 to +5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	EMPHIN	17	I	CSSL
2	I	VTRIN	18	I	AGCMAX
3	I	STBY	19	I	AGCCONT
4	I	CAVI	20	—	GND
5	I	CLP1	21	I	XSHP
6	I	VCLP	22	I	XSHD
7	—	GND	23	—	Vcc
8	O	OUT	24	I	CLP4
9	O	OUTCLP	25	I	PGIN
10	—	Vcc	26	I	DATAIN
11	I	VRBS	27	I	ADJ
12	O	VRBO	28	O	AGCOUT
13	O	VRTO	29	—	Vcc
14	I	VRTS	30	I	ADCK
15	I	CSCLP	31	—	GND
16	O	CSOUT	32	I	CAMIN



INPUT

ADCK ; S/H TRIGGER PULSE INPUT FOR A/D
 ADJ ; INTERNAL S/H SAMPLING WIDTH ADJUSTMENT
 AGCCONT ; AGC AMPLIFIER GAIN ADJUSTMENT
 AGCMAX ; AGC AMPLIFIER MAX GAIN ADJUSTMENT
 CAMIN ; AGC OUT (PIN-28) COUPLING DEPENDING ON CAPACITY
 CAVI ; CAMERA MODE (EMPHIN/CAMIN → OUT) AND
 VTR MODE (VTRIN → OUT) SWITCHING AT INPUT STAGE
 CLP1 ; CLAMP PULSE CLP1 : CAMERA
 CLP4 ; CLAMP PULSE
 CSCLP ; CAPACITOR CONNECTION FOR CLAMPING
 CSSL ; THRESHOLD ADJUSTMENT FOR CHROMA SUPPRESSION
 DATAIN ; CCD SIGNAL
 EMPHIN ; CHROMA COMPONENT
 PGIN ; CCD SIGNAL
 STBY ; A1399 POWER SAVE MODE (NORMALLY GND)
 VCLP ; CLAMP PULSE VCLP : VIDEO
 VRBS ; INTERNAL OPERATION AMPLIFIER INVERTED INPUT IN 2V REGULATOR
 VRTS ; INTERNAL OPERATION AMPLIFIER INVERTED INPUT IN 4V REGULATOR
 VTRIN ; VIDEO PLAYBACK SIGNAL
 XSHD ; HIGH SPEED PULSE INPUT FOR S/H
 XSHP ; HIGH SPEED PULSE INPUT FOR S/H

OUTPUT

AGCOUT ; AGC
 CSOUT ; FOR CHROMA SUPPRESSION AND FOR SELECTING
 S/H CIRCUIT OPERATION FOR A/D
 OUT ; DRIVER OUTPUT STANDARD D RANGE : 2.2V
 OULCLP ; CLAMPS BLACK LEVEL OF OUT (PIN-8)
 OUTPUT SIGNAL AT 2.0V
 VRBO ; 2V REGULATOR
 VRTO ; 4V REGULATOR

