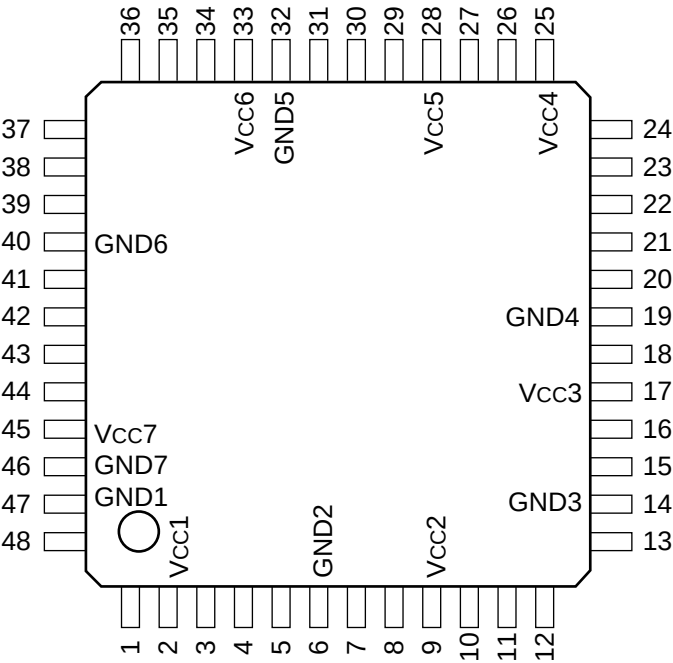


DEMODULATOR/OVER MODULATION COMPENSATOR

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	O	OMC	13	I	DRPV	25	—	Vcc4	37	I	SHREF
2	—	Vcc1	14	—	GND3	26	O	DEMCV	38	I	SHAPL
3	O	MVDC	15	O	BBO	27	I	VDGAIN	39	I	SHCLP
4	I	LMT2	16	O	BAO	28	—	Vcc5	40	—	GND6
5	I	LMT1	17	—	Vcc3	29	I	IEXT2	41	I	LM2IN1
6	—	GND2	18	O	SWO	30	I	VIIN	42	I	LM2IN2
7	I	HFX	19	—	GND4	31	I	IEXT1	43	I	LM1IN1
8	I	LDO	20	I	SW3	32	—	GND5	44	I	LM1IN2
9	—	Vcc2	21	I	SWC1	33	—	Vcc6	45	—	Vcc7
10	O	DMRC	22	I	SW2	34	O	SHO	46	—	GND7
11	O	FDEDC	23	I	SWC2	35	O	SHC	47	—	GND1
12	I	DMGAIN	24	I	SW1	36	I	SHVIN	48	I	REFV

Vcc1, GND1 ; FOR OVER MODULATION COMPENSATOR
Vcc2, GND2 ; FOR DEMODULATOR
Vcc3, GND3 ; FOR BUFFER
Vcc4, GND4 ; FOR 3 INPUT SWITCH
Vcc5, GND5 ; FOR DEMODULATOR
Vcc6, GND6 ; FOR SAMPLE HOLD
Vcc7, GND7 ; FOR LIMITER

5	LMT1	BA0	16
4	LMT2	BBO	15
31	IEXT1		
29	IEXT2		
30	VI IN		
27	VDGAIN	DEMCV	26
12	DMGAIN	DMRC	10
13	DRPV	FDEDC	11
7	HFX		
8	LDO	MVDC	3
43	LM1OM1	OMC	1
44	LM1IN2		
41	LM2IN1		
42	LM2IN2		
48	REFV		
36	SHVIN	SHO	34
37	SHREF	SHC	35
38	SHAPL		
39	SHCLP		
24	SW1	SWO	18
22	SW2		
20	SW3		
21	SWC1		
23	SWC2		

INPUT

DMGAIN ; FOR RESISTOR
 DRPV ; VIDEO SIGNAL
 HFX ; SWITCH CONTROL
 IEXT1, 2 ; FOR RESISTOR
 LM1, 2 ; OVER MODULATION COMPENSATE LIMITER
 LMT1, 2 ; DEMODULATE LIMITER
 LDO ; SWITCH CONTROL
 REFV ; REFERENCE VOLTAGE
 SHAPL ; FOR RESISTOR
 SHCLP ; SAMPLE HOLD PULSE
 SHREF ; SAMPLE HOLD REFERENCE VOLTAGE
 SHVIN ; SAMPLE HOLD DEMODULATE VIDEO
 SW1 - 3 ; 3 INPUT SWITCH
 SWC1, 2 ; 3 INPUT SWITCH CONTROL
 VDGAIN ; GAIN CONTROL VOLTAGE
 VIIN ; $V \rightarrow I$ CONTROL VOLTAGE

OUTPUT

BAO, BBO ; DEMODULATOR
 DEMCV ; VARIABLE DEEMPHASIS CONSTANT
 DMRC ; FIXED DEEMPHASIS CONSTANT
 FDEDC ; FIXED DEEMPHASIS DECOUPLING
 MVDC ; MONOMULTI DECOUPLING
 OMC ; OVER MODULATION COMPENSATER
 SHC ; SAMPLE HOLD CAPACITY
 SHO ; SAMPLE HOLD
 SWO ; 3 INPUT SWITCH

