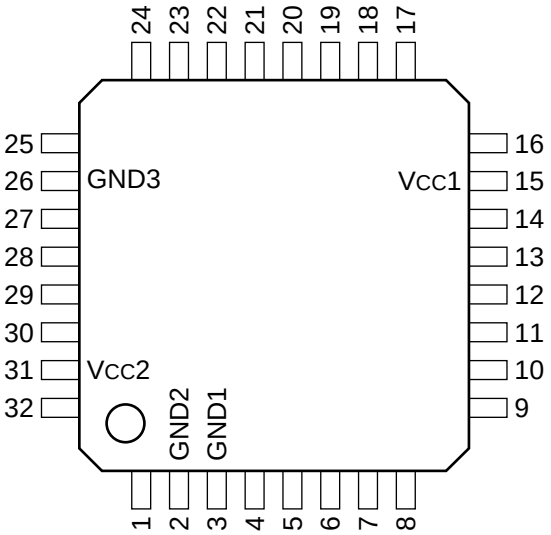
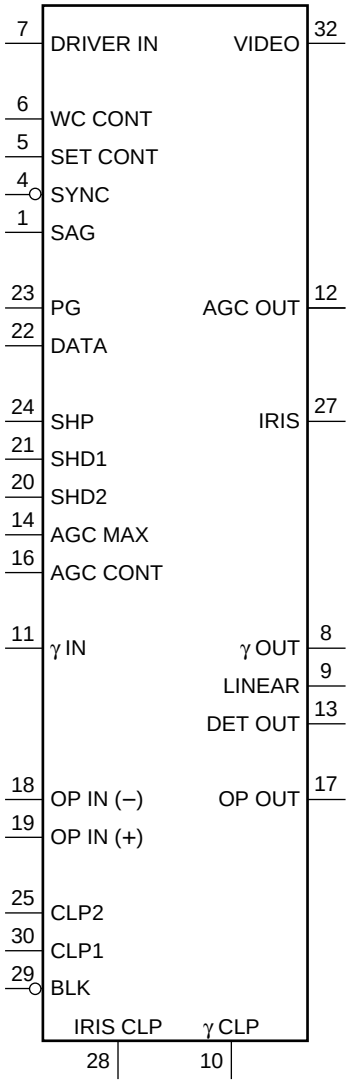
1 CHIP PROCESS FOR CCD BLACK AND WHITE

—TOP VIEW—





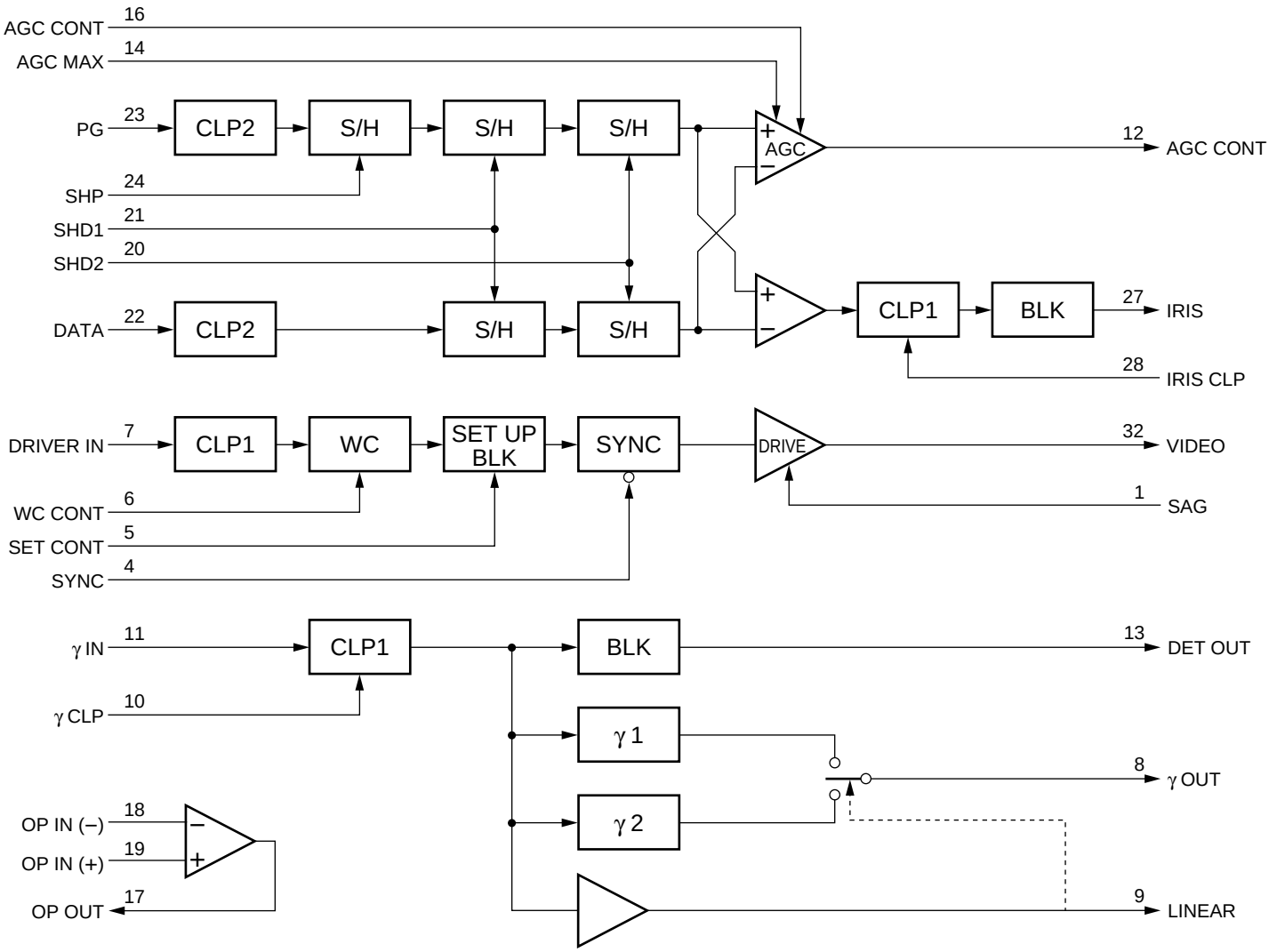
PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	SAG	17	O	OP OUT
2	—	GND2	18	I	OP IN (–)
3	—	GND1	19	I	OP IN (+)
4	I	$\overline{\text{SYNC}}$	20	I	SHD2
5	I	SET CONT	21	I	SHD1
6	I	WC CONT	22	I	DATA
7	I	DRIVER IN	23	I	PG
8	O	γ OUT	24	I	SHP
9	O	LINEAR	25	I	CLP2
10	I	γ CLP	26	—	GND3
11	I	γ IN	27	O	IRIS
12	O	AGC OUT	28	I	IRIS CLP
13	O	DET OUT	29	I	$\overline{\text{BLK}}$
14	I	AGC MAX	30	I	CLP1
15	—	Vcc1	31	—	Vcc2
16	I	AGC CONT	32	O	VIDEO

INPUT

AGC CONT	:	AGC AMPLIFIER GAIN CONTROL
AGC MAX	:	AGC AMPLIFIER MAX GAIN
BLK	:	BLK PULSE
CLP1, CLP2	:	CLP1, 2 PULSE
DATA	:	CCD SIGNAL
DRIVER IN	:	DRIVER
IRIS CLP	:	IRIS OUTPUT CLAMP
OP IN (+)	:	OPERATIONAL AMPLIFIER NON-TURX
OP IN (–)	:	OPERATIONAL AMPLIFIER TURN
PG	:	CCD SIGNAL
SAG	:	SAG CORRECTION SIGNAL
SET CONT	:	SETUP LEVEL ADJUSTMENT
SHD1, SHD2	:	SAMPLE HOLD PULSE
SHP	:	SAMPLE HOLD PULSE
SYNC	:	SYNC PULSE
WC CONT	:	WHITE CLIP LEVEL ADJUSTMENT
γ CLP	:	γ INPUT CLAMP
γ IN	:	γ ADJUSTMENT

OUTPUT

AGC OUT	:	AGC SIGNAL
DET OUT	:	AGC DETECTION SIGNAL
IRIS	:	IRIS CONTROL SIGNAL
LINEAR	:	LINEAR SIGNAL
OP OUT	:	OPERATIONAL AMPLIFIER
VIDEO	:	VIDEO SIGNAL
γ OUT	:	γ CORRECTION SIGNAL


AGC CONT
16
AGC MAX
14
PG
23
CLP2
S/H
S/H
S/H
AGC
12
AGC CONT
SHP
24
SHD1
21
SHD2
20
DATA
22
CLP2
S/H
S/H
CLP1
BLK
27
IRIS
28
IRIS CLP
DRIVER IN
7
CLP1
WC
SET UP BLK
SYNC
DRIVE
32
VIDEO
1
SAG
WC CONT
6
SET CONT
5
SYNC
4
γ IN
11
CLP1
BLK
13
DET OUT
γ CLP
10
γ 1
8
γ OUT
γ 2
OP IN (–)
18
OP IN (+)
19
OP OUT
17
9
LINEAR