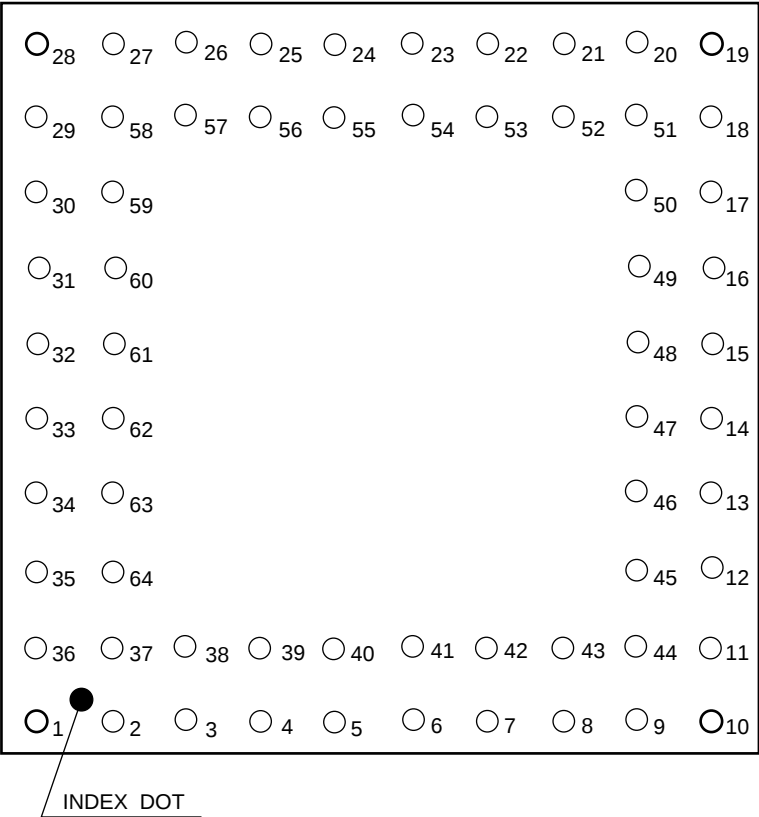
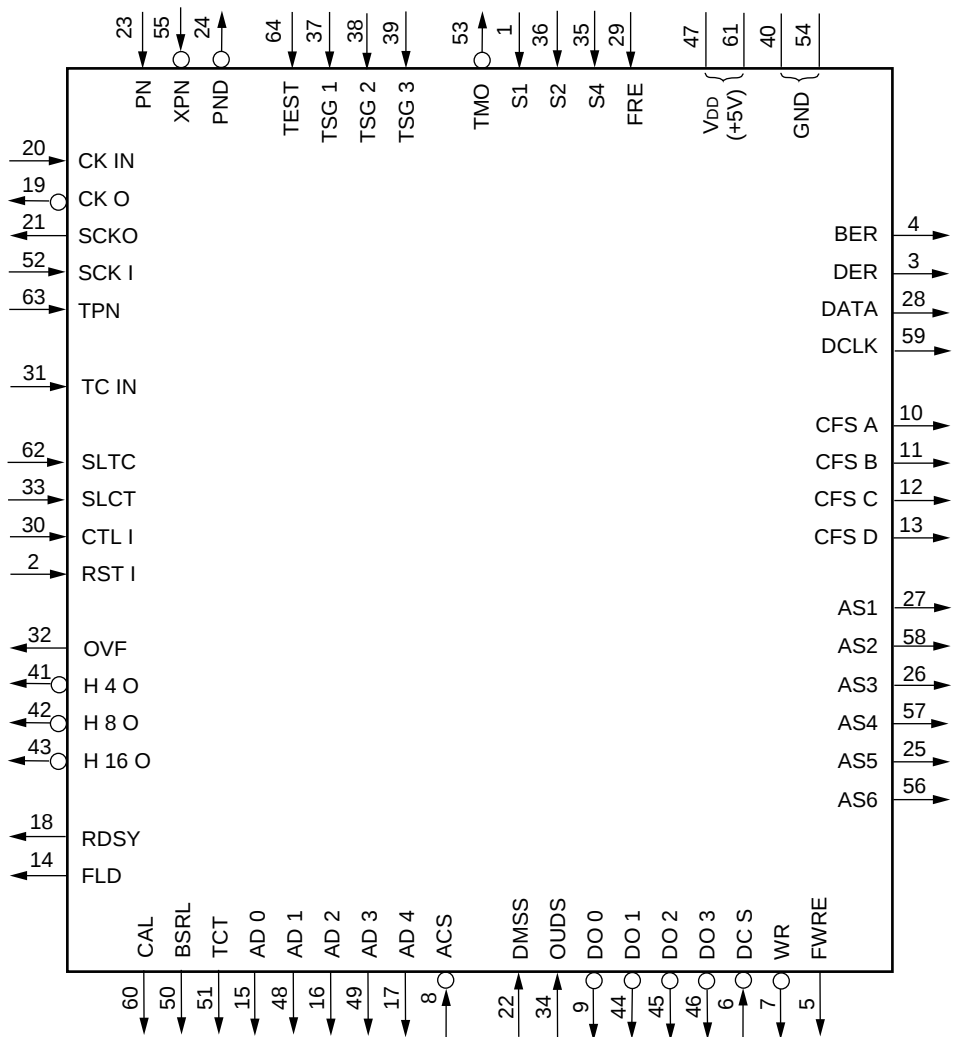

C-MOS TIME CODE READER
-TOP VIEW-



PIN ASSIGNMENT

PIN NO.	IN	OUT	SYMBOL	PIN NO.	IN	OUT	SYMBOL	PIN NO.	IN	OUT	SYMBOL	PIN NO.	IN	OUT	SYMBOL
1	○		S1	17		○	AD 4	33	○		SLCT	49		○	AD 3
2	○		RSTI	18		○	RDSY	34	○		OU DS	50		○	BSRL
3		○	DER	19		○	CK O	35	○		S4	51		○	TCT
4		○	BER	20	○		CK IN	36	○		S2	52	○		SCKI
5		○	FWRE	21		○	SCKO	37	○		TSG 1	53		○	TMO
6	○		DCS	22	○		DMSS	38	○		TSG 2	54		○	GND
7		○	WR	23	○		PN	39	○		TSG 3	55	○		XPN
8	○		ACS	24		○	PND	40		○	GND	56		○	AS 6
9		○	DO 0	25		○	AS5	41		○	H 4 O	57		○	AS 4
10		○	CFS A	26		○	AS3	42		○	H 8 O	58		○	AS 2
11		○	CFS B	27		○	AS1	43		○	H 16 O	59		○	DCLK
12		○	CFS C	28		○	DATA	44		○	DO 1	60		○	CAL
13		○	CFS D	29	○		FRE	45		○	DO 2	61	○		VDD(+5V)
14		○	FLD	30	○		CTL I	46		○	DO 3	62	○		SLTC
15		○	AD 0	31	○		TC IN	47	○		VDD(+5V)	63	○		TPN
16		○	AD 2	32		○	OVF	48		○	AD 1	64	○		TEST



MODE SELECT

SLCT	SLTC	MODE
0	0	AUTO
0	1	TIME CODE
1	0	CTL
1	1	AUTO

CTL COUNT

FRE	RSTI	COUNT
0	X	DOWN COUNT
1	X	UP COUNT
X	1	RESET

SIGNAL FORMAT SELECT

S4	S2	S1	FORMAT	FRAME
0	0	0	FILM	24
0	0	1		
0	1	0		—
0	1	1		
1	0	0	EBU (PAL, SECAM)	25
1	0	1	NOT ALLOWED	—
1	1	0	NTSC (NON DROP FRAME)	30
1	1	1	NTSC (DROP FRAME)	30

0 ; LOW LEVEL
1 ; HIGH LEVEL
X ; DON'T CARE

FUNCTIONAL PIN No.	SYMBOL	DEFINITION
8	ACS	ADDRESS LINE OUTPUT ENABLE (NEGATIVE-LOGIC)
15	AD0	ADDRESS DATA OUTPUT
48	AD1	
16	AD2	
49	AD3	
17	AD4	
27	AS1	PARALLEL DATA OUTPUT
58	AS2	
26	AS3	
57	AS4	
25	AS5	
56	AS6	
4	BER	BI-PHASE MARK ERROR (BIT ERROR) OUTPUT
50	BSRL	DATA OUT FLAG OUTPUT
60	CAL	CALCULATE OUT FLAG OUTPUT
10	CFSFA	DEMODULATED COLOR FRAME OUTPUT
11	CFSB	
12	CFSF	
13	CFSFSD	
20	CKIN	CLOCK INPUT
19	CKO	CLOCK OUTPUT (NEGATIVE-LOGIC)
30	CTL1	CTL SIGNAL INPUT ($\overline{\text{L}}$)
28	DATA	DEMODULATED SERIAL DATA OUTPUT
59	DCLK	DEMODULATED CLOCK OUTPUT
6	DCS	DATA LINE OUTPUT ENABLE (NEGATIVE-LOGIC)
3	DER	DEMODULATE DATA ERROR OUTPUT
22	DMSS	ASSIGNED DATA SELECT
9	DO0	DATA OUTPUT (NEGATIVE-LOGIC)
44	DO1	
45	DO2	
46	DO3	
14	FLD	FIELD DATA OUTPUT
29	FRE	CTL DIRECTION SIGNAL INPUT
5	FWRE	DEMODULATE DIRECTION SIGNAL OUTPUT
41	H4O	SPEED RANGE FLAG 4 OUTPUT (NEGATIVE-LOGIC)
42	H8O	SPEED RANGE FLAG 8 OUTPUT (NEGATIVE-LOGIC)
43	H16O	SPEED RANGE FLAG 16 OUTPUT (NEGATIVE-LOGIC)
34	OU DS	ERROR BYPASS ON/OFF SELECT
32	OVF	OVERFLOW FLAG OUTPUT
23	PN	POWER ON SIGNAL INPUT
24	PND	POWER ON SIGNAL OUTPUT (NEGATIVE-LOGIC)
18	RDSY	READ SYNC WORD DATA OUTPUT
2	RST1	CTL TIMER RESET INPUT ($\overline{\text{L}}$)
1	S1	SIGNAL FORMAT SELECT S1
36	S2	SIGNAL FORMAT SELECT S2
35	S4	SIGNAL FORMAT SELECT S4
52	SCKI	SYSTEM CLOCK INPUT
21	SCKO	SYSTEM CLOCK OUTPUT
33	SCLT	SELECT CTL INPUT
62	SLTC	SELECT LTC INPUT
55	XPIN	SYSTEM RESET SIGNAL INPUT (NEGATIVE-LOGIC)
31	TCIN	LONGITUDINAL TIME AND CONTROL CODE INPUT
51	TCT	INPUT SELECT FLAG OUTPUT
53	TMO	TIME OVER SIGNAL OUTPUT (NEGATIVE-LOGIC)
7	WR	WRITE SIGNAL OUTPUT (NEGATIVE-LOGIC)
47	VDD	POWER LINE (+5V)
61	VDD	
40	GND	
54	GND	GND LINE
64	TEST	
63	TPN	
37	TSG1	TEST MODE SET INPUT (TO BE CONNECTED WITH GND)
38	TSG2	
39	TSG3	

TIMING CHART

CX7912(4/4)

