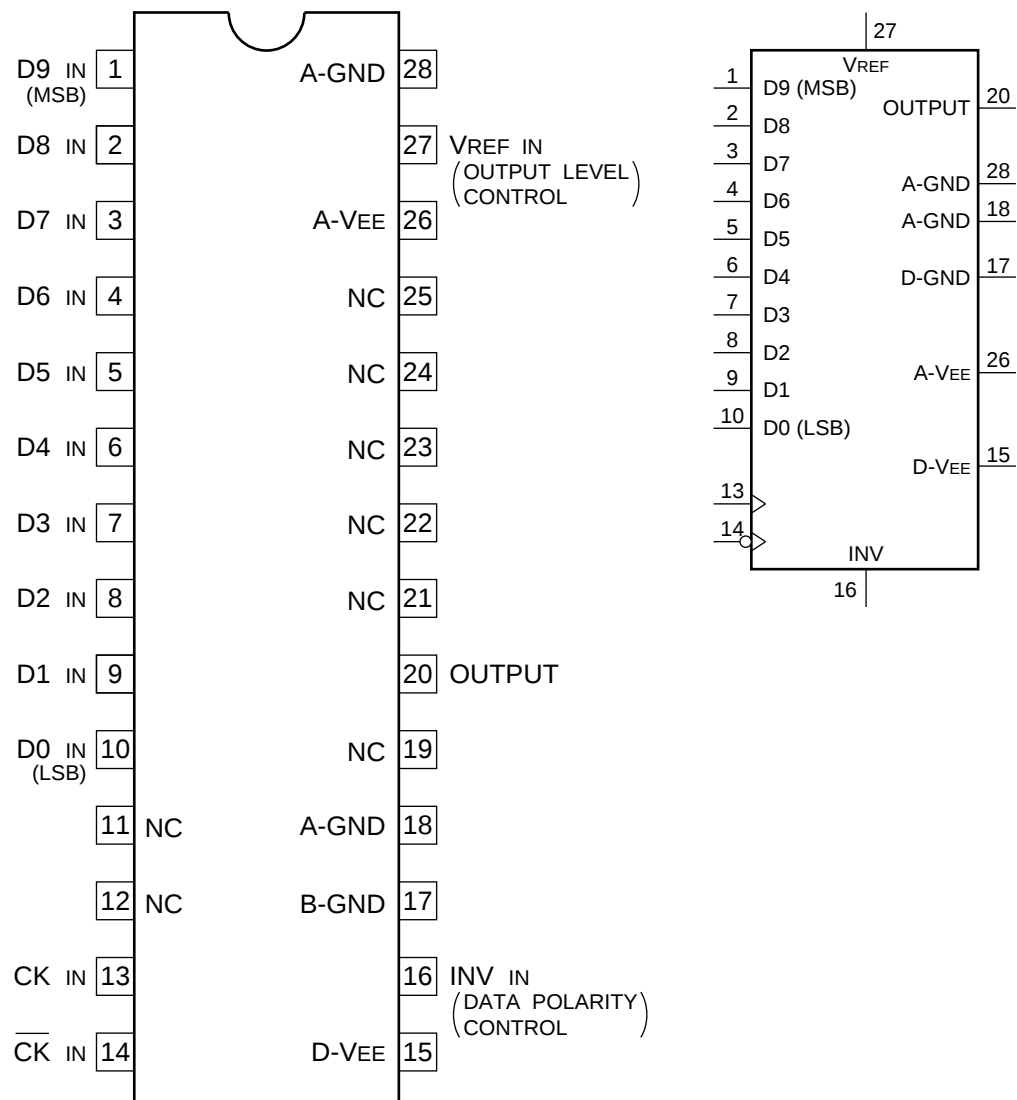
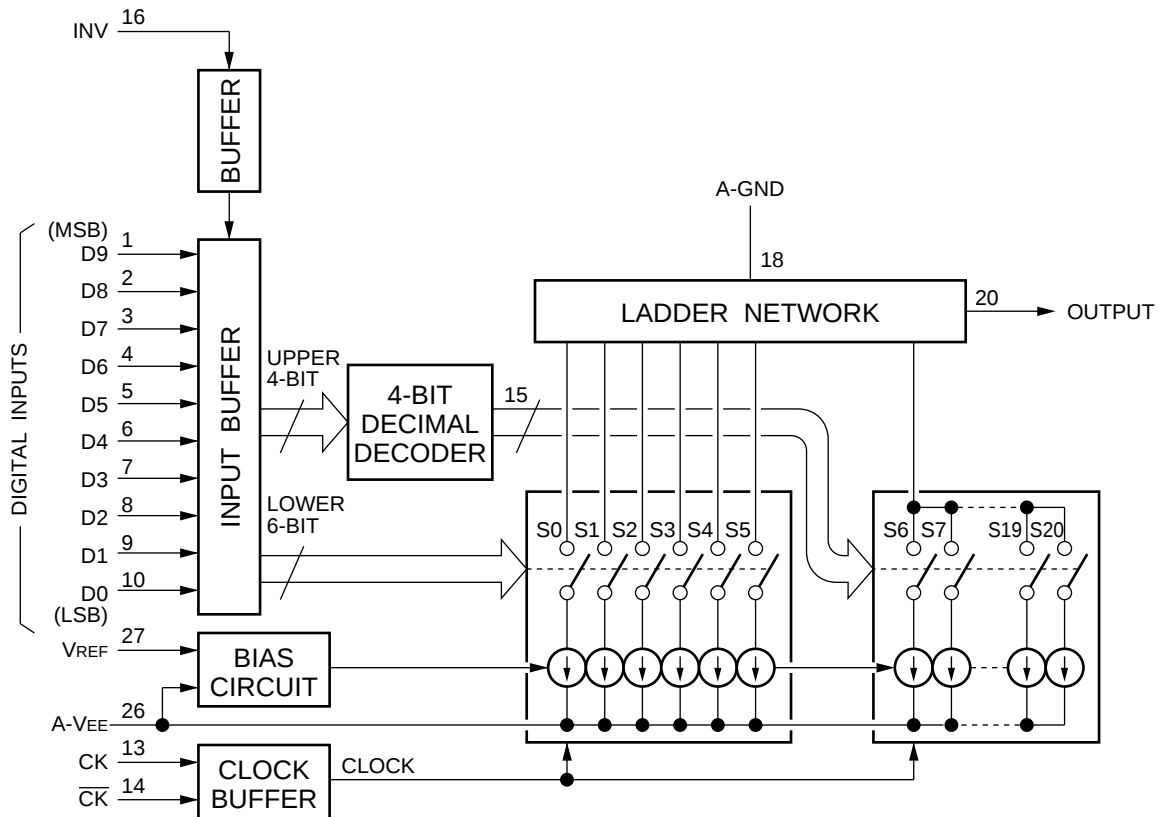


ECL 10-BIT D/A CONVERTER

—TOP VIEW—





INV SELECTION

DATA INPUTS										OUTPUT	
D9	D8	D7	D6	D5	D4	D3	D2	D1	D0	INV "0"	INV "1"
1	1	1	1	1	1	1	1	1	1	V_0 (OS)	V_0 (OS)–1.000 V
1	1	1	1	1	1	1	1	1	0	V_0 (OS)–0.001 V	V_0 (OS)–0.999 V
1	1	1	1	1	1	1	1	0	1	V_0 (OS)–0.002 V	V_0 (OS)–0.998 V
⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
1	0	0	0	0	0	0	0	0	0	V_0 (OS)–0.500 V	V_0 (OS)–0.502 V
0	1	1	1	1	1	1	1	1	1	V_0 (OS)–0.501 V	V_0 (OS)–0.501 V
0	1	1	1	1	1	1	1	1	0	V_0 (OS)–0.502 V	V_0 (OS)–0.500 V
⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
0	0	0	0	0	0	0	0	0	0	V_0 (OS)–1.000 V	V_0 (OS)

1 ; ECL HIGH LEVEL (= –0.89 V)

0 ; ECL LOW LEVEL (= –1.75 V)

 V_0 (OS) ; ZERO OFFSET