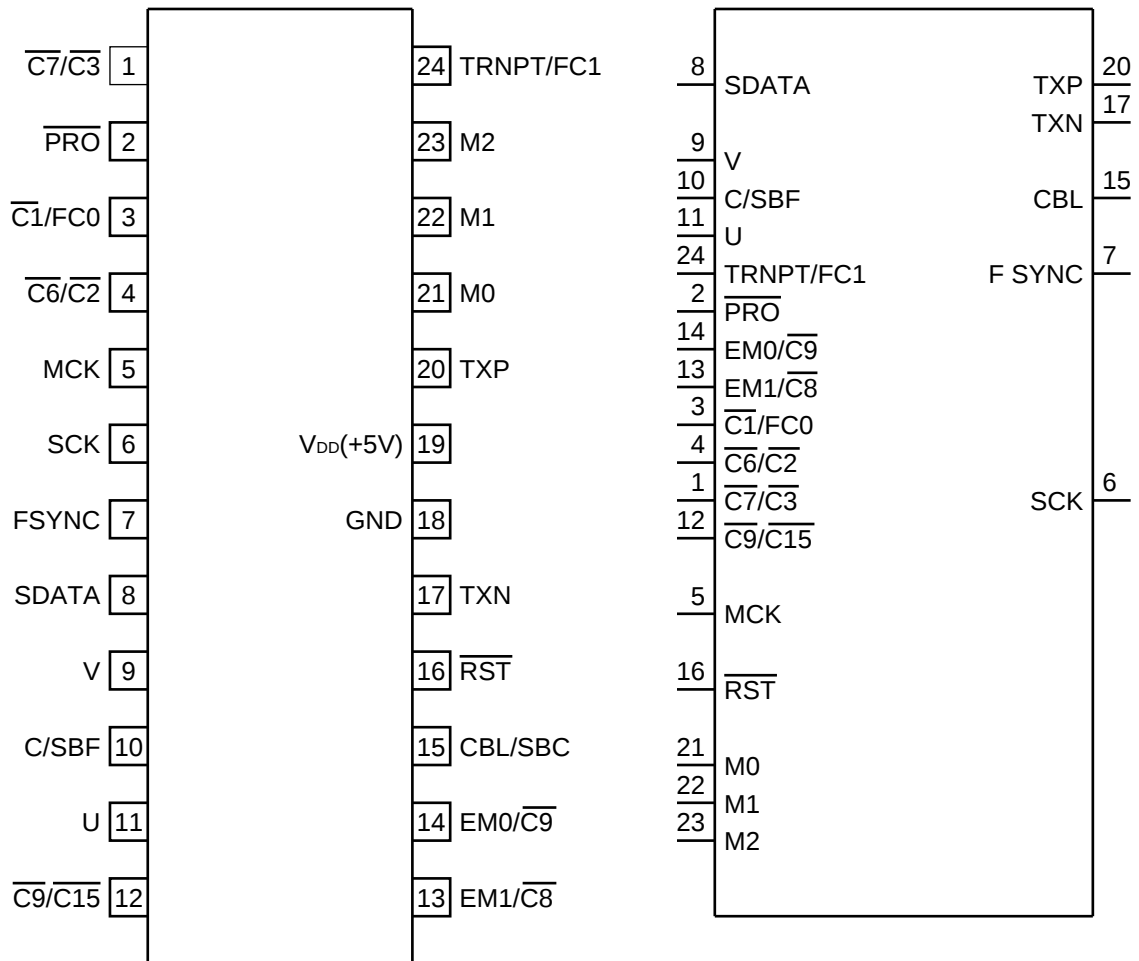

C-MOS DIGITAL AUDIO INTERFACE TRANSMITTER

- TOP VIEW -

**INPUT**

SDATA ; SERIAL DATA
 V ; VALIDITY
 C/SBF ; CHIP SELECT/ SUBCORD FRAME CLOCK
 U ; USER DATA
 TRNPT/FC1 ; TRANSPARENT MODE/FREQUENCY
 SELECT 1
 $\overline{\text{PRO}}$; PRO MODE
 EM0/ $\overline{\text{C9}}$; EMPHASIS 0 / CHIP SELECT 9
 EM1/ $\overline{\text{C8}}$; EMPHASIS 1 / CHIP SELECT 8
 $\overline{\text{C1/FC0}}$; CHIP SELECT 1 / FREQUENCY SELECT 0
 $\overline{\text{C6/C2}}$; CHIP SELECT 6 / 2
 $\overline{\text{C7/C3}}$; CHIP SELECT 7 / 3
 $\overline{\text{C9/C15}}$; CHIP SELECT 9 / 15
 MCK ; MASTER CLOCK
 $\overline{\text{RST}}$; RESET
 M0 ; SERIAL PORT MODE SELECT 0
 M1 ; SERIAL PORT MODE SELECT 1
 M2 ; SERIAL PORT MODE SELECT 2

OUTPUT

TXP ; DIFFERENTIAL LINE DRIVER
 TXN ; DIFFERENTIAL LINE DRIVER
 CBL ; CS BLOCK START
 F SYNC ; FRAME SYNC

INPUT/OUTPUT

SCK ; SERIAL DATA CLOCK

