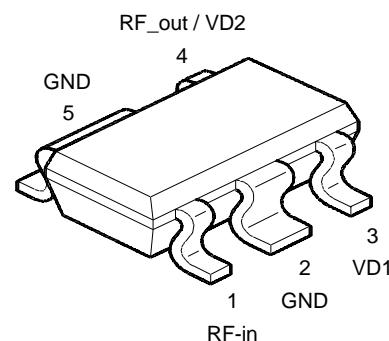


Tentative Data

- Power amplifier for DECT application,
- single voltage supply
- Operating voltage range: 2.7 to 6 V
- $P_{out} = 26\text{dBm}$ at $V_d=3.3\text{V}$
- Overall power added efficiency 44 %

ESD: Electrostatic discharge sensitive device,
observe handling precautions!



VPW05980

Type	Marking	Ordering code (taped)	Package 1) (taped)
CGY 195	t.b.d.	t.b.d.	MW 5

Maximum ratings

Characteristics	Symbol	max. Value	Unit
Positive supply voltage	V_D	8	V
Supply current	I_D	t.b.d.	A
Maximum input power	P_{inmax}	t.b.d.	dBm
Channel temperature	T_{Ch}	150	°C
Storage temperature	T_{stg}	-55...+150	°C
Total power dissipation ($T_s \leq 81$ °C)	P_{tot}	t.b.d.	W
<i>Ts: Temperature at soldering point</i>			
Pulse peak power	P_{Pulse}	t.b.d.	W

Thermal Resistance

Channel-soldering point	R_{thChS}	t.b.d.	K/W
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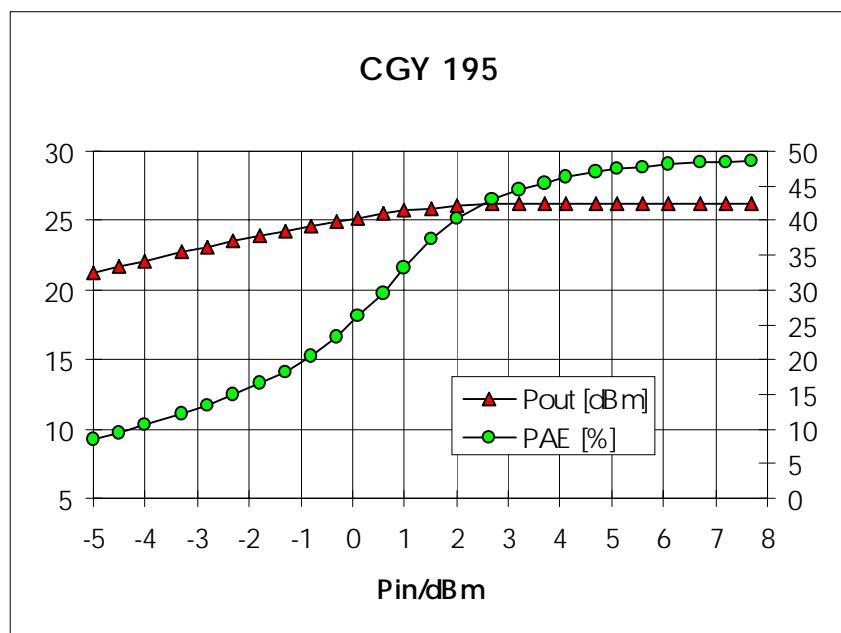
1) Plastic body identical to MW-6

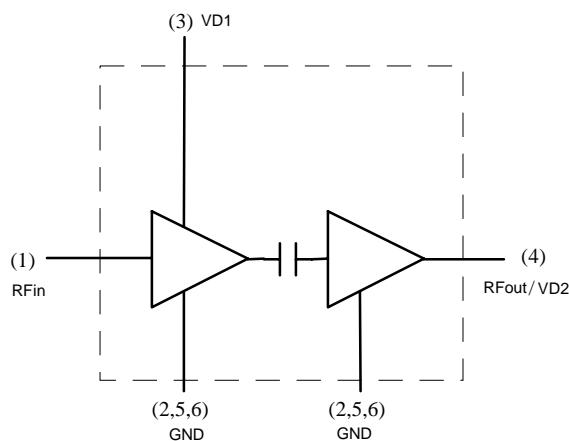
Electrical characteristics(T_A = 25°C , f=1.89 GHz, Z_S=Z_L=50 Ohm, , unless otherwise specified)

Characteristics	Symbol	min	typ	max	Unit
Supply current VD=3.3V; Pin = +3 dBm	I _{DD}	-	270	-	mA
Gain VD=3.3V; Pin = -10 dBm	G	-	26	-	dB
Output Power VD=3.3V; Pin = 3 dBm	P _O	-	26	-	dBm
Output Power VD=4.8V; Pin = 5 dBm	P _O	-	28	-	dBm
Overall Power added Efficiency VD=3.3V; Pin = 3 dBm	PAE		42	-	%

Output power and power added efficiency

pulsed mode: T=577μs, duty cycle 12.5%



Functional Block Diagram

Pin #		Configuration
1	RFin	RF input power
2	GND	RF and DC ground
3	VD1	Pos. drain voltage of the 1st stage
4	RFout/VD2	RF output power / Pos. drain voltage of the 2nd stage
5	GND	RF and DC ground
6	GND	RF and DC ground