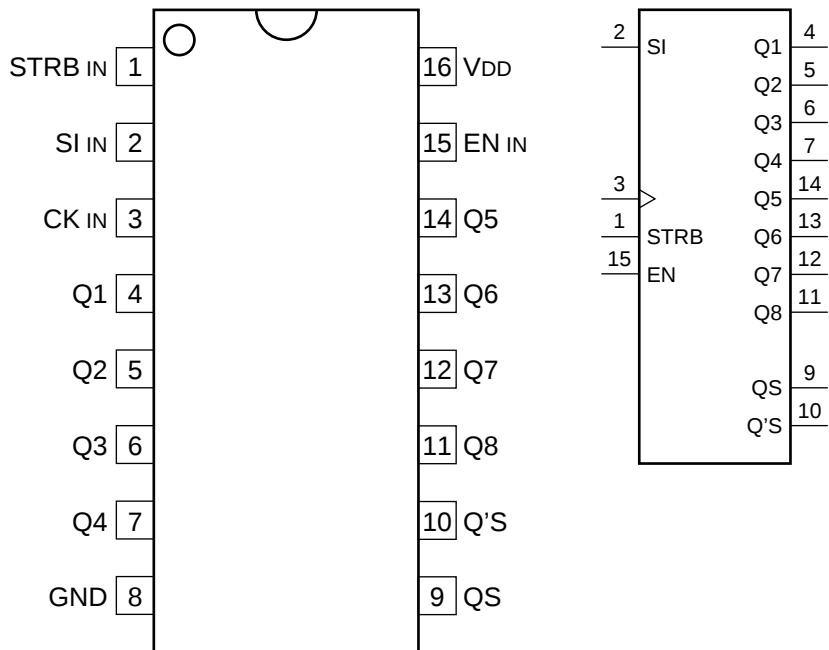


C-MOS 8-STAGE SHIFT-AND-STORE BUS REGISTER

—TOP VIEW—

**INPUTS**

SI : SERIAL DATA
 CK : CLOCK
 STRB : STROBE
 EN : OUTPUT ENABLE

OUTPUTS

Q1 - Q8 : PARALLEL DATA
 QS, Q'S : SERIAL DATA

INPUTS				PARALLEL OUT		SERIAL OUT	
CK	EN	STRB	SI	Q1	Qn	QS	Q's
	0	x	x	HI-Z	HI-Z	Q7	NC
	0	x	x	HI-Z	HI-Z	NC	Q7
	1	0	x	NC	NC	Q7	NC
	1	1	0	0	Qn-1	Q7	NC
	1	1	1	1	Qn-1	Q7	NC
	1	1	1	NC	NC	NC	Q7

0 : LOW LEVEL

HI-Z : HIGH IMPEDANCE

1 : HIGH LEVEL

NC : NO CHANGE

x : DON'T CARE