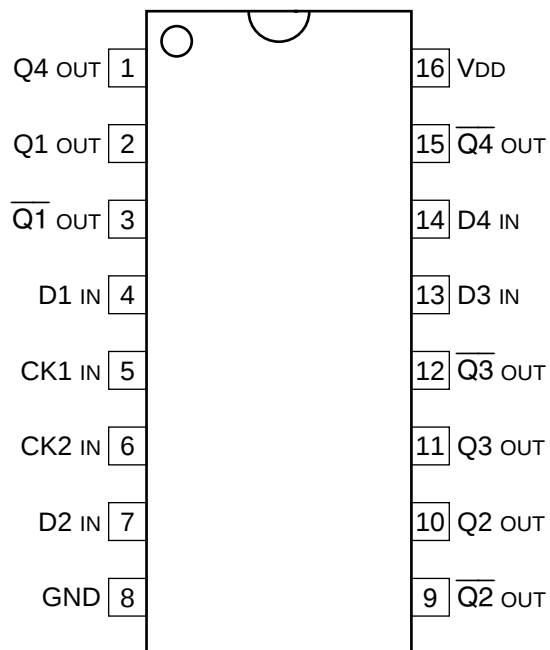


C-MOS D-TYPE LATCH

—TOP VIEW—



FUNCTION TABLE

CK1	CK2	Q / $\overline{Q}$
0	0	PASS DATA
0	1	LATCH DATA
1	0	LATCH DATA
1	1	PASS DATA

